IoT and Digitization Will Reconnect System Engineering and Science

John Blyler

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IoT and Digitization Will Reconnect System Engineering and Science

By John Blyler, Founder, JB Systems
PSU SySc Seminar, Feb 22, 2019
John Blyler’s Background

- BS Engineering Physics, MS EE (digital and RF)
- Engineering/Management Background
  - 20 years – Systems of Systems, HW-SW Dev.
  - DOD – DOE – Semi - Commercial
- Teaching Background
  - Develop/taught courses for industry and university
  - Affiliate Professor – PSU, Systems Engineering
  - Lecturer – UC Irvine, SE and IOT Certificates
- Media Background
  - VP, Editor-in-Chief: Systems, Semi, Electronics
  - Associate Editor, IEEE publications
  - Accellera-IEEE Standards (IP-XACT)
  - Book author: IEEE, Wiley, Elsevier, SAE
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Key Points

• Developing an Internet of Things (IoT) system requires managing multidiscipline and multidomain system complexities. To be successful, this will require a tailoring of the traditional system-of-system (SoS) engineering approach.

• System engineers can no longer be merely process or requirements experts. They must also have some specific domain knowledge in hardware, software, network/connectivity AND data technologies. This represents a change from traditional systems engineering professionals.

• Model-Based Systems Engineering (MBSE) with domain knowledge will bring the engineering back into systems engineering and enable closer ties to system science.
Evolution of SE Over the Last 40 Years

“Write what you know.” – Mark Twain

Systems Engineering (according to one practitioner)

1980s – Hardware dominance, software servers, SEs as integrators

1990s – IT, embedded and app software, SE as requirement jockeys

2000s – Hardware is commoditized, software-based automation design tools and apps, IoT, MBSE
Putting Engineering Back into Systems Engineering

• Has there been a degradation of systems analysis skills to the detriment of the standing of the discipline?
• Has systems engineering become much more about process than outcomes?
• Is it just another engineering discipline rather than the integrating discipline of the parts of the system and the system in its context?
Putting Engineering Back into Systems Engineering (Continued)

• The Digital Thread and its connection to the Digital Twin have put the engineering back into systems engineering.

• MBSE and digital continuity among multidomain architectural-design-testing-manufacturing tools
Relationship to System Science:
Two opposite trends, same answer

• Trend 1: Divergence of two SEs – for traditional and complex systems
  – Traditional systems engineering: mechanistic, linear, predictable, deterministic, reducible, controllable, static, and existing at one scale
  – Complex systems engineering: include people as well as machines; cross organization boundaries; include multiple disciplines; less predictable, less deterministic, more chaotic, more autonomous, less centrally controlled, and more self organized and adaptive, multiscale

• Trend 2: Blurring of traditional boundaries
  – Natural vs artificial systems
  – Physical vs conceptual systems
  – Science vs engineering disciplines

• Answer to both trends: Integrate while maintaining separation
  – Use systems science as encompassing and integrating foundation of all systems – traditional/complex, natural/artificial, physical/conceptual
  – Use modeling orientation as encompassing and integrating method for all systems, and for integrating and separating science and engineering

“Ideas on systems science and systems engineering,” Duane Hybertson, MITRE 2006
Agenda

• Lifecycles and Legacies
• Systems Engineering – Key Elements
• IoT Properties
• SE-IoT Motivation and 3 Key Issues
• Summary
Life Cycle Models (Obligatory Graph)
V-Diagram
(Example – Automotive)
Middle-Out Engineering – V Diagram

FIGURE 1.11 Top-down/bottom-up system development process.
Middle-Out Engineering – Iterative Flow

- Requirements Analysis
- Functional Analysis/Allocation
- Synthesis
- System Analysis and Control

Flow directions:
- Top-Down
- Middle-Out or Inside-Out
- Bottom-Up
Middle-Out Engineering Overview

• Combination of the two:
  • Bottom – get sense of the system, simple functionalities and scenarios.
  • Top – decomposition that matches top-level physical architecture. Refer to functionalities.

Top-Down: Learning SE

Middle-out: SE in practice

Bottom-up: Engineering Disciplines
Hardware is Different from Software

• Truly, this is another lecture!
• Suffice to say and especially with the Agile software process:
  – Agile SW and HW Manifesto – Why HW is different:
    1. Lead Time (and timing as shown in the next slide)
    2. Component Cost
    3. Multi-Facetted Work

→ If interested, read work on applying Agile Hardware approach to RISC-V (Open Source) MCU: “An Agile Approach to Building RISC-V Microprocessors”
A Word About HW-SW Lifecycles

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<th>Other Terminology</th>
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<tr>
<td>TL4</td>
<td>PV (Programmer’s View)</td>
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<td>• No timing</td>
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<tr>
<td></td>
<td>• TLM2.0 B.P. (LT)</td>
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<tr>
<td>TL3</td>
<td>AV (Architect’s view)</td>
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<tr>
<td></td>
<td>• Interburst or no timing</td>
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<tr>
<td></td>
<td>• TLM-2.0 B.P. (AT)</td>
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<tr>
<td>TL2</td>
<td></td>
</tr>
<tr>
<td></td>
<td>• Intraburst or no timing</td>
</tr>
<tr>
<td></td>
<td>• Extends TLM-2.0 (AT)</td>
</tr>
<tr>
<td>TL1</td>
<td>VV (Verification view)</td>
</tr>
<tr>
<td></td>
<td>• Fully cycle accurate</td>
</tr>
<tr>
<td></td>
<td>• Extends TLM-2.0 (AT)</td>
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<tr>
<td></td>
<td>• Supports clock cycle synchronization and combinatorial paths</td>
</tr>
<tr>
<td>TL0</td>
<td>RTL</td>
</tr>
<tr>
<td></td>
<td>Signal level (not transaction level)</td>
</tr>
</tbody>
</table>

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Typical SoC Design Flow

- Emulator
  - In-system emulator
  - HW-SW co-debugging
HW – SW Differences

1. Changing when something goes wrong:
   – It is ‘really hard’ for hardware, and somewhere on spectrum from ‘really hard’ to ‘completely trivial’ in software.

2. Adding abstraction to deal with complexity:
   – Software is typically able to ‘absorb’ more of this overhead than hardware.
   – Software it is far easier to only optimize the fast path.

3. Tool set differences:
   – Hardware projects typically stick with really old tools for so long. Why?
Agenda

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• Systems Engineering – Key Elements
  • IoT Properties
  • SE-IoT Motivation and 3 Key Issues
  • Summary
Which System?
Which System Engineer Do You Mean?

• Hardware
• Software (device, application, middleware, etc.)
• Network
• System-of-Systems (SoS)
SE is Good Engineer

• Systems engineering is good engineering with certain designed areas of interest:
  – Top-Down approach required to view entire system
  – Life-cycle orientation to address all activities and phases
  – Identify the initial system-level requirements to ensure early decision making in design process
  – Interdisciplinary collaborative effort (team environment)
  – Interface management
  – Maintain a multidisciplinary balance
    • Technical product – HW/SW trade-offs
    • Cost and Risk
    • Schedule and budget
  – Define and allocate system requirements
  – Coordinate integration and verification of system
Major Supporting Design Disciplines

Systems Engineering integrates all disciplines and specialties (as shown below):

• Hardware and Software
• Reliability - Resiliency
• Maintainability
• Human factors and safety
• Security engineering

• Manufacturing – Production
• Logistics and supportability
• Disposability
• Environmental
Tech. Planning Requires Tech. View

Lifecycle Phases / Events
- Work Product 1: Final Update
- Work Product 2: Prelim Final Update
- Work Product N:

IMP/IMS
- Events:
  - Sig Accom (SA)
  - Acc Crit (AC)

CDRLs
- SRR
- SFR
- PDR
- CDR
- TRR

Work Product Repository

Program Customized Plan
(Basis for Proposal Estimate & Schedule)

Detailed / Refined Technical Plan

Detailed Functional Work Flows

Standard Deck + Metrics

Gate C
Gate D
ATP
IBR

Pgm Plans
- PEP
- SEMP
- PE
- RM

Organization
- PM
- SEIT
- IPT
- EE
- SW

Plans

CDRLs
- Contract
- SOW
- SOO

WBS / PBS
- WBS
- PBS
- 1.0
- 2.0
- 3.0
- 2.1
- 2.2
- 2.3

Organization

Work Products
- Work Product 1: Final
- Work Product 2: Prelim
- Work Product N:

IMP/IMS

Airframe
- Wing

System Integration Lab
SW Integration Lab

Program Customized Plan
(Basis for Proposal Estimate & Schedule)

Detailed / Refined Technical Plan

Detailed Functional Work Flows

Task Maturation

Gate C
Gate D
ATP
IBR
Make or Buy
(System Decisions)

• When to make (contractor’s view)
  – Proprietary product
  – Limited or nonexistent market
  – To reduce costs
• When to buy (contractor’s view)
  – Workforce limited
  – Lack of skills
  – Outside of product line
  – Off-the-shelf availability
Technology Issues

Forecasting

• Prediction that technical development can occur within specific time period given enough resources

• Normative technology forecasting
  – After desired future goal is chosen, process is developed backward – from future to past – to achieve the goal

• Exploratory technology forecasting
  – Starts at present state of technology and extrapolates into future (assumes some expected rate of technology development).
Managing at the Interface

• Essential for HW-SW systems
• Interface-related issues
• Resource margin allocation
• Technical Performance Measurement (TPM) techniques.
Need for MBSE - Silos

Stovepiped efforts utilizing independent representations hides context, requires extraordinary data management, and complicates the SE effort.
Need for MBSE – NASA SRD Development Process

**Requirements Gathering & Operational Analysis**
- Identify Source Material, Operational Context, Use Cases, Scenarios, Information Exchange
- Establish Initial Requirements Set
- Establish Design Constraints
- Capture Issues / Risks / Decisions

**Functional Behavior Analysis**
- Operational Scenarios
- Integrated Behavior Models
- Derive Functional / Performance Requirements
- Define I/O
- Define Effectiveness Measures

**System Architecture Analysis**
- System Structure (i.e., Hierarchy of System Elements)
- Interfaces between System Elements
- Allocate Functional Behavior and Non-Functional Requirements

**Analysis Results**
- Risk Assessment
- Compliance & Cost Assessment
- Design Verification & Validation

**Technical Rules, Standards, and Conventions**
- Test Planning
- Select Design Solution
- Document Generation

**Specifications**

**Product Evaluation & Document Generation**

**System Architectures**

**Equipment List**

**System of Systems**
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What Does the IoT Encompass

• IoT devices are special-purpose (computers are general purpose)
  – Microcontroller (Audrino) and Microprocessor (Raspberry Pi) w/ RTOS
• Hardware size and power
  – Smaller, lighter, less power (older tech nodes) – Resource constrained
• Yet many IoT devices need significant computation and speed
  – Speed-to-text, audio processing, network communication
• Internet accessible – Wired, wireless (including LiFi), cellular, 4/5 G, edge and cloud computing
• Software – firmware, RTOS, OS, NOS, Middleware, Apps, etc
• Data – Big, Little, AI, machine learning, high-level synthesis downward
• Security – Maybe the most important element
• Business models
Properties of IoT Systems (continued)

• Wireless Internet Access
  – Wireless access (cell phone, Wi-Fi) enables networking with cheap infrastructure
    • Less need to install physical cables
  – Data costs are fairly low
    • This point is arguable, but many can afford it
  – Data bandwidth is high
    • Can stream multiple movies in real-time

• Interface to the Edge (gateway) and the Cloud
  – IoTdevice interfaces can leverage powerful servers and large databases
  – Ex: Siri enables search with verbal questions
Raspberry Pi or Arduino Uno

Assignment for Week 2 in UC-I IoT course
→ Done from a systems tradeoff perspective
Internet of Things (IoT): Applications and Opportunities
Agenda

• Lifecycles and Legacies
• Systems Engineering – Key Elements
• IoT Properties
• SE-IoT Motivation and 3 Key Issues
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Motivation For Systems Engineering

• IOT promises huge revenue potential
  – Amount of devices that connect to the internet will rise from about 13 billion today to 50 billion by 2020
  – By 2025, five sixths of semi growth is going to be the result of AI.
• Moore’s Law “slowdown” supports IOT
• Comoditization of hardware
• Aging workforce
• Aging industrial infrastructure
• Others??
Issue #1 IOT vs Embedded

• IoT is the networked interconnection of everyday objects with embedded computers, sensors and actuators
  – IOT = **Connected** embedded devices
  – “Things" (embedded system devices) sense and collect data and send it to the internet. This data can be accessible by other "things" too.

• Connectivity – Data – Security
  – Connectivity means IOT has huge data side
  – Big data industry is expected to grow from US$10.2 billion in 2013 to about US$54.3 billion by 2017.
  – Handling the data is a big deal → Hence topics like machine learning.
  – Dark side of connectivity: Everything fails if security is lacking!
IOT Requires SOS Engineering

• Increasing interconnections between independent components or subsystems leads to more complexity and chance for system failures.

• Metcalfe’s Law:
  – Three interconnected components result in a maximum of three interconnections
  – Four interconnected components results in a maximum of six interconnections
  – Ten interconnected components results in a maximum of 45 interconnections
  – Millions or billions of interconnected things on the Internet will results in how many interconnections?
    • Very high rate of complexity
Ease of HW-SW IOT Development

• Falling prices of sensors, low power embedded systems, network and connectivity devices
• All the major system vendors (IBM, ARM, NXP, etc) have demo’s to easily put together an IOT applications
  – Smartphone, sensor, low power processor or Raspberry Pi = Done!
• Is the best way to build a complex, reliable system?
System of Systems

• Techniques of systems engineering (SE) have long history of developing complex systems

• But SE must change to meet the new IOT challenge:
  – Short time-to-market
  – Distributed global work force and supply chain
  – Ease of use – no difficult or problem-laden installation and operation
  – Dealing with hardware, software and non-traditional developers
Differences in SE and SW – HW Systems

• 10 Assertions in Paper
  - Requirements Criticality
  - Who tests the system?
  - Apportionment of complexity
  - Scope of “SE Work”
  - Interfaces
  - Engineering Degrees
  - Addressing “ilities”
  - Process focus
  - Object Orientation
  - Background and Emphasis

6 major differences
  - Physics
  - User Domain
  - Cohesiveness of HW and SW
  - Requirements Changes
  - Software Youth
  - “ilities”
Software SEs come from CS or math

• Some software engineers consider hardware issues “out of scope”
• Little cross training across hardware and software domains

Hardware SEs come from engineering

Software Does Not Address “ilities” As Much As Hardware
Software’s “Ilities”

• **Design qualities*** (“conceptual integrity,” maintainability, reusability*)

• **Run-time qualities*** (availability, interoperability, manageability,* performance, reliability, scalability,* **security** (see separate section) )

• **System Qualities*** (Supportability, Testability),

• **User qualities*** (usability). Also modifiability.

• Reliability issues are not based on mechanical failures. More based on bugs, or unintended interactions.

• For vulnerabilities, redundancy doesn’t work. If a flaw takes out one helicopter, it’ll take out the second one too.

• **SW architecture** is largely driven by non-functional (ilities) = “quality attribute” requirements. Not always easy to fold this into systems engineering’s functional decomposition (need something to track, something to target...)

*(Kaniss, Al. What is a software engineer, CrossTalk May/June 2015)*
Embedded system Life Cycle

Ideally, development of Hardware and software goes in parallel.
# Methodology – HW-SW Integration

## Alternative approaches

- **Physical prototyping**
  - Real pre-production hardware
  - Real-time execution
  - Poor controllability & visibility
  - Limited access – few prototypes

- **Emulation**
  - X86 based emulation hardware
  - HIL with real sensors/actuation
  - Close-to real-time execution
  - Improved control and visibility
  - Access limited only by hardware cost

- **Simulation**
  - No special hardware required as each ECU is modeled as a Win32 process
  - HIL with simulated sensors/actuation
  - Full control and visibility
  - Access limited by software cost
System Modeling Workbench
Multidiscipline
Issue #2 – Engineering Expertise

• Recall: Issue #1 - IOT vs Embedded
• Issue #2 - How will semi and embedded HW-SW fabless Intellectual Property (IP) companies address large number of inexperienced SOC designers needed for IOT market?
Internet of Things Drivers

• Predicted 20 Billion devices
• IoT is driven by Data and Subscription business models
• IoT devices are enablers rather than money makers
• Majority of devices will NOT be wearables
• There will be tens of 1000’s of new IoT businesses
• Therefore,

  There will be >> 10,000’s new IoT device designers

Thanks to Jim Bruister, CEO, SOC Solutions (from REUSE 2016)
Where will all these new IoT designers come from?

– Systems designers
– Software designers
– FPGA designers  (large %)
– PCB Board designers
– Semiconductor designers  (small %)
– College graduates

*Majority are non-experienced in SOC design*
Likely approach New IoT Designer will take?

- Google search SOC, Chip or ASIC
- Trade Magazines
  - EETimes, EDN, Manufacturing Today, *Sports Illustrated, Field & Stream*, etc...
- Look for SOC experts, semiconductor consultants
- Look at IP Portals
  - if they know about them
- Contact Design Houses
  - if they can find them
Issue #3– Complexity and Chaos

– Overwhelming number of life-cycle tools, engineering skill sets, life-cycle costs, IP acquisition and industry associations needed to navigate the IOT chip and embedded development process
New IoT Designer Anxiety Disorder

- TMI (information overload)
- Too many IP choices
- Tools sticker shock
- Design Flow complications
- Development Cycle elongation
- NRE fever

- Where can I get help?
What’s missing ?
From the scope of the New IoT Designer

• Practical education
  • “How to” guides, IoT and electronic (HW-SW-IT) design for Dummies

• Where to find pertinent information
  • Not the overwhelming info from Google searches, etc.

• Enough consultants or industry experts
  • Not enough to go around.

• Easy, Fast, Inexpensive, process from Concept to Product
  • Not enough “One stop shops” with streamlined design processes

• Easy platforms or reference designs to start from
Typical IoT SOC Architecture

- CPU
- Power Management Unit
- Direct Memory Access
- User Masters
- APB Bridge
- APB Peripherals
  - I²C, SPI, Timers, GPIO
- SRAM
- External Flash Controller
- User Slaves
- Security
- Internal Mem Ctrl
- AHB or AXI Interconnect
- Differentiator

Digital Subsystem
- APB Channel
- APB Peripherals
  - DACs, ADCs, Analog Muxes, Op-Amps
- Radio Baseband Interface
- Sensor Interface
- Sensors
- Pressure
- Temp
- Motion
- RF AFE
- DACs, ADCs, Analog Muxes, Op-Amps

Analog Mixed Signal Subsystem

Interchangeable

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Agenda

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Design Abstraction

Raise the bar on design abstraction by providing:

• Modular Architectures
  • Specific to IoT Devices

• Class Libraries for hardware
  • Analog Models
  • Digital Subsystems

• Software Abstractions
  • Standard API’s and HAL’s

Think Arduino and Raspberry Pi
How do we improve? (continued)

• Systems engineers must employ agile (not necessarily Agile), lean techniques which bring rigor without aerospace-sized overhead.

• Ilities roles especially Security roles
  – Together work reliability, availability, maintainability, etc.
  – Partner regarding security. What SW vulnerabilities might be leverageable by adversaries to compromise systems (both IT and embedded <- newer)
Summary

• Developing an Internet of Things (IoT) system requires managing multidiscipline and multidomain system complexities. To be successful, this will require a tailoring of the traditional system-of-system (SoS) engineering approach.

• System engineers can no longer be merely process or requirements experts. They must also have some specific domain knowledge in hardware, software, network/connectivity AND data technologies. This represents a change from traditional systems engineering professionals.

• Model-Based Systems Engineering (MBSE) with domain knowledge will bring the engineering back into systems engineering and enable closer ties to system science.
Certificates and Continuing Education

- Systems Engineering
- Internet of Things
- Others

### Required Courses

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<td>Foundations of the Systems Engineering Process</td>
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<td>Systems Requirements Engineering</td>
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<td>System Design and Integration</td>
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<td>System Validation and Verification</td>
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### Elective Courses (Minimum 5 units)

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<tr>
<td>Simulation-Based Engineering of Complex Systems</td>
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<tr>
<td>Systems Engineering: Tools &amp; Methods</td>
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### Course Schedule

#### Required Courses (9 units)

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<tr>
<td>Designing and Integrating IOT Devices</td>
<td>3</td>
</tr>
<tr>
<td>Networking and Securing IOT Devices</td>
<td>3</td>
</tr>
</tbody>
</table>
Many Ways to Teach Systems Engineering

• Across a typical system or product lifecycle, from beginning to end
• In terms of key concepts or elements such as planning, design, operations,ilities, etc.
• Early NSF studies promoted active learning for online courses.
• Experience shows that case studies, short in-class and especially hands-on exercises have proven invaluable with either of the above two approaches.
Thank You!
Backup Slides