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## Needed Computations Shortcutting Needed Steps

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# Needed Computations Shortcutting Needed Steps

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We define a compilation scheme for a constructor-based, strongly-sequential, graph rewriting system which shortcuts some needed steps. The object code is another constructor-based graph rewriting system. This system is normalizing for the original system when using an innermost strategy. Consequently, the object code can be easily implemented by eager functions in a variety of programming languages. We modify this object code in a way that avoids total or partial construction of the *contracta* of some needed steps of a computation. When computing normal forms in this way, both memory consumption and execution time are reduced compared to ordinary rewriting computations in the original system.

## 1 Introduction

Rewrite systems are models of computations that specify the actions, but not the control. The object of a computation is a graph referred to as an *expression*. The actions are encoded by rules that define how to replace (rewrite) one expression with another. The goal of a computation is to reach an expression, called a *normal form*, that cannot be further rewritten.

In the computation of an expression, a rewrite system does not tell which subexpression should be replaced to reach the goal.

**Example 1.** Consider the following rewrite system. The syntax is Curry [17].

$$\begin{aligned} \text{loop} &= \text{loop} \\ \text{snd } (\_, y) &= y \end{aligned} \tag{1}$$

A computation of  $\text{snd } (\text{loop}, 0)$  terminates with 0 if the second rule of (1) is ever applied, but goes on forever without making any progress if only the first rule is applied.

In a computation a *strategy* is a policy or algorithm that defines both which subexpression should be replaced and its replacement. The intended goal of a strategy is to efficiently produce a normal form of an expression when it exists. A practical strategy, called *needed*, is known for the class of the strongly sequential term rewriting systems [18]. This strategy relies on the fact that, in every reducible expression  $e$ , there exists a *redex*, also called *needed*, that is reduced in any computation of  $e$  to a normal form.

The needed strategy is defined and implemented as follows: given an expression  $e$ , while  $e$  is reducible, reduce an arbitrarily chosen, needed redex of  $e$ . In the systems considered in this paper, finding a needed redex is easy without look-ahead [3]. This strategy is normalizing: if an expression  $e$  has a normal form, repeatedly reducing arbitrary needed redexes will terminate with that normal form. This strategy is also optimal in the number of reduced redexes for graph (not term) rewriting.

The above outline shows that implementing a needed strategy is a relatively straightforward task. Surprisingly, however, it is possible to shortcut some of the needed steps in the computation. This paper shows how this shortcutting can be introduced into an implementation of a needed strategy.

Terminology and background information are recalled in Sect. 2. The compilation scheme and its properties are in Sect. 3 and 4. The transformation that allows shortcutting needed redexes, and its properties, are in Sect. 5 and 6. Sect. 7 presents two benchmarks and sketches further opportunities to shortcut needed steps. Sect. 8 discusses the application of our work to the implementation of functional logic languages. Related work and conclusion are in Sect. 9 and 10, respectively.

## 2 Preliminaries

A *rewrite system* is a pair  $(\Sigma \cup \mathcal{X}, \mathcal{R})$  in which  $\Sigma = \mathcal{C} \uplus \mathcal{D}$  is a *signature* partitioned into *constructors* and *operations* (or functions),  $\mathcal{X}$  is a denumerable set of *variables*, and  $\mathcal{R}$  is a set of *rewrite rules* defined below. Without further mention, we assume that the symbols of the signature have a type, and that any expression over the signature is well typed.

An *expression* is a single-rooted, directed, acyclic *graph* defined in the customary way [11, Def. 2]. An expression  $e$  is a *constructor form* (or *value*) if, and only if, every node of  $e$  is labeled by a constructor symbol. Constructor forms are normal forms, but not vice versa. For example,  $head([])$  where  $head$  is the usual operation that returns the first element of a (non-empty) list, is a normal form, but not a constructor form. In a constructor-based system, such expressions are regarded as *failures* or *exceptions* rather than results of computations. Likewise, a *head-constructor form* is an expression whose root node is labeled by a constructor symbol.

A *rule* is a graph with two roots abstracting the left- and right-hand sides respectively. The rules follow the *constructor discipline* [23]. Each rule's left-hand side is a *pattern*, i.e., an *operation* symbol applied to zero or more expressions consisting of only *constructor* symbols and variables. Rules are left-linear, i.e., the left-hand side is a tree.

The objects of a computation are graphs rather than terms. Sharing some subexpressions of an expression is a requirement of functional logic programming [13, 14, 15, 21]. Incidentally, this sharing ensures that needed redexes are never duplicated during a computation. The difference between our graphs and ordinary terms concerns only the sharing of subexpressions.

A *computation* of an expression  $t$  is a possibly infinite sequence

$$t = t_0 \rightarrow t_1 \rightarrow \dots$$

such that  $t_i \rightarrow t_{i+1}$  is a rewrite step [11, Def. 23]. For all  $i$ ,  $t_i$  is a *state* of the computation of  $t$ .

Given a rewrite system  $R$ , an *expression of  $R$*  is an expression over the signature of  $R$ . When  $s$  is a signature symbol and  $n$  is a natural number,  $s/n$  denotes that  $n$  is the *arity* of  $s$ . When  $t$  and  $u$  are expressions and  $v$  is a variable,  $[u/v]$  is the *substitution* that maps  $v$  to  $u$ , and  $t[u/v]$  is the application of  $[u/v]$  to  $t$ . The reflexive closure of the rewrite relation “ $\rightarrow$ ” is denoted “ $\rightarrow^*$ ”.

Each operation in  $\mathcal{D}$  is *inductively sequential*; that is, its rewrite rules are organized into a hierarchical structure called a *definitional tree* [1] which we informally recall below. An example of a definitional tree is shown in (3). In a definitional tree of an operation  $f$ , there are up to 3 kinds of nodes called *branch*, *rule* and *exempt*. Each kind contains a pattern of  $f$  and other items of information depending on the kind. A *rule* node with pattern  $\pi$  contains a rule of  $f$  whose left-hand side is equal to  $\pi$  modulo renaming nodes and variables. An *exempt* node with pattern  $\pi$  contains no other information. There is no rule of  $f$  whose left-hand side is equal to  $\pi$ . A *branch* node with a pattern  $\pi$  contains children that are subtrees of the definitional tree. At least one child is a *rule* node. The children are obtained by “narrowing” pattern  $\pi$ . Let  $x$  be any variable of  $\pi$ , which is called *inductive*. For each constructor  $c/m$  of the type of  $x$ , there is a child whose pattern is obtained from  $\pi$  by instantiating  $x$  with  $c(x_1, \dots, x_m)$ , where  $x_i$  is a fresh variable.

An operation  $f/n$  is *inductively sequential* [1] if there exists a definitional tree whose root has pattern  $f(x_1, \dots, x_n)$ , where  $x_i$  is a fresh variable, and whose leaves contain all, and only, the rules of  $f$ . A rewrite system is *inductively sequential* if all of its operations are inductively sequential.

Inductively sequential operations can be thought of as “well designed” with respect to evaluation. To compute a needed redex of an expression  $e$  rooted by an operation  $f$ , match to  $e$  the pattern  $\pi$  of a maximal (deepest in the tree) node  $N$  of a definitional tree of  $f$ . If  $N$  is an *exempt* node,  $e$  has no constructor normal form, and the computation can be aborted. If  $N$  is a *rule* node,  $e$  is a redex and can be reduced by the rule in  $N$ . If  $N$  is a *branch* node, let  $x$  be the inductive variable of  $\pi$  and  $t$  the subexpression of  $e$  to which  $x$  is matched. Then, recursively compute a needed redex of  $t$ .

The inductively sequential systems are the intersection [16] of the strongly sequential systems [18] and the constructor-based systems [23]. The following notion [8] for inductively sequential systems is key to our work. We abuse the word “needed” because we will show that our notion extends the classic one [18]. Our notion is a binary relation on nodes, or equivalently on the subexpressions rooted by these nodes, since they are in a bijection.

**Definition 1.** *Let  $R$  be an inductively sequential system,  $e$  an expression of  $R$  rooted by a node  $p$ , and  $n$  a node of  $e$ . Node  $n$  is needed for  $e$ , and similarly is needed for  $p$ , if, and only if, in any computation of  $e$  to a head-constructor form, the subexpression of  $e$  at  $n$  is derived to a head-constructor form. A node  $n$  (and the redex rooted by  $n$ , if any) of a state  $e$  of a computation in  $R$  is needed if, and only if, it is needed for some outermost operation-rooted subexpression of  $e$ .*

Our “needed” relation is interesting only when both nodes are labeled by operation symbols. If  $e$  is an expression whose root node  $p$  is labeled by an operation symbol, then  $p$  is trivially needed for  $p$ . This holds whether or not  $e$  is a redex and even when  $e$  is *already* a normal form, e.g.,  $head([])$ . In particular, any expression that is not a value has pairs of nodes in the needed relation. Finally, our definition is concerned with reaching a *head-constructor* form, not a *normal form*.

Our notion of need generalizes the classic notion [18]. Also, since our systems follow the constructor discipline [23] we are not interested in expressions that do not have a value.

**Lemma 1.** *Let  $R$  be an inductively sequential system and  $e$  an expression of  $R$  derivable to a value. If  $e'$  is an outermost operation-rooted subexpression of  $e$ , and  $n$  is both a node needed for  $e'$  and the root of a redex  $r$ , then  $r$  is a needed redex of  $e$  in the sense of [18].*

*Proof.* Since  $e'$  is an outermost operation-rooted subexpression of  $e$ , any node in any path from the root of  $e$  to the root of  $e'$ , except for the root of  $e'$ , is labeled by constructor symbols. Hence,  $e$  can be derived to a value only if  $e'$  is derived to a value and  $e'$  can be derived to a value only if  $e'$  is derived to a head-constructor form. By assumption, in any derivation of  $e'$  to a head-constructor form  $r$  is derived to a head-constructor form, hence it is reduced. Thus,  $r$  is a needed redex of  $e$  according to [18].  $\square$

**Lemma 2.** *Let  $R$  be an inductively sequential system,  $e$  an expression of  $R$ ,  $e_1$ ,  $e_2$  and  $e_3$  subexpressions of  $e$  such that  $n_i$  is the root of  $e_i$  and the label of  $n_i$  is an operation, for  $i = 1, 2, 3$ . If  $n_3$  is needed for  $n_2$  and  $n_2$  is needed for  $n_1$ , then  $n_3$  is needed for  $n_1$ .*

*Proof.* By hypothesis, if  $e_3$  is not derived to a constructor-rooted form,  $e_2$  cannot be derived to a constructor-rooted form, and if  $e_2$  is not derived to a constructor-rooted form,  $e_1$  cannot be derived to a constructor-rooted form. Thus, if  $e_3$  is not derived to a constructor-rooted form,  $e_1$  cannot be derived to a constructor-rooted form.  $\square$

```

compile  $\mathcal{T}$ 
01  case  $\mathcal{T}$  of
02  when  $branch(\pi, o, \tilde{\mathcal{T}})$  then
03     $\forall \mathcal{T}_i \in \tilde{\mathcal{T}}$  compile  $\mathcal{T}_i$ 
04    output “ $\mathbf{H}(\{\pi\}) = \mathbf{H}(\{\pi[\mathbf{H}(\pi|_o)]_o\})$ ”
05  when  $rule(\pi, l \rightarrow r)$  then
06    case  $r$  of
07      when operation-rooted then
08        output “ $\mathbf{H}(\{l\}) = \mathbf{H}(\{r\})$ ”
09      when constructor-rooted then
10        output “ $\mathbf{H}(\{l\}) = \{r\}$ ”
11      when variable then
12        for each constructor  $c/n$  of the sort of  $r$ 
13          let  $l' \rightarrow r' = (l \rightarrow r)[c(x_1, \dots, x_n)/r]$ 
14          output “ $\mathbf{H}(\{l'\}) = \{r'\}$ ”
15          output “ $\mathbf{H}(\{l\}) = \mathbf{H}(\{r\})$ ”
16  when  $exempt(\pi)$  then
17    output “ $\mathbf{H}(\{\pi\}) = \text{abort}$ ”

```

Figure 1: Procedure `compile` takes a definitional tree of an operation  $f$  of  $R$  and produces the set of rules of  $\mathbf{H}$  that pattern match  $f$ -rooted expressions.

### 3 Compilation

For simplicity and abstraction, we present the object code,  $C_R$ , of  $R$  as a constructor-based graph rewriting system as well.  $C_R$  has only two operations called *head* and *norm*, and denoted  $\mathbf{H}$  and  $\mathbf{N}$ , respectively. The constructor symbols of  $C_R$  are all, and only, the symbols of  $R$ . The rules of  $C_R$  have a priority established by the textual order. A rule reduces an expression  $t$  only if no other preceding rule could be applied to reduce  $t$ . These semantics are subtle, since  $t$  could become reducible by the preceding rule only after some internal reduction. However, all our claims about computations in  $C_R$  are stated for an innermost strategy. In this case, when a rule is applied, no internal reduction is possible, and the semantics of the priority are straightforward.

Operation  $\mathbf{H}$  is defined piecemeal for each operation of  $R$ . Each operation of  $R$  contributes a number of rules dispatched by pattern matching with textual priority. The rules of  $\mathbf{H}$  contributed by an operation with definitional tree  $\mathcal{T}$  are generated by the procedure `compile` defined in Fig. 1. The intent of  $\mathbf{H}$  is to take an expression of  $R$  rooted by an operation and derive an expression of  $R$  rooted by a constructor by performing only needed steps.

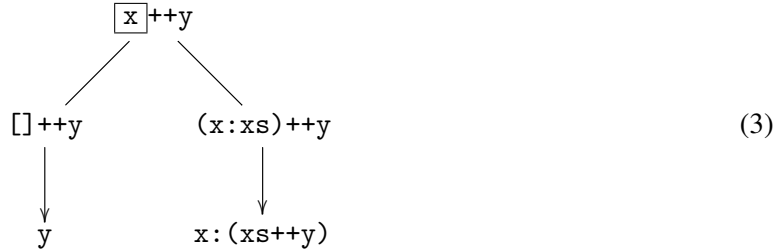
The expression “ $\{x\}$ ” embedded in a string, denotes interpolation as in modern programming languages, i.e., the argument  $x$  is replaced by a string representation of its value. The notation  $t[u]_p$  stands for an expression equal to  $t$ , in which the subexpression identified by  $p$  is replaced by  $u$ . In procedure `compile`, the notation is used to “wrap” an application of  $\mathbf{H}$  around the subexpression of the pattern at  $o$ , the inductive node. An example is the last rule of (4). The loop at statement 12 is for collapsing rules, i.e., rules whose right-hand side is a variable. When this variable matches an expression rooted by a constructor of  $R$ , no further application of  $\mathbf{H}$  is required; Otherwise,  $\mathbf{H}$  is applied to the contractum. Symbol “abort” is not considered an element of the signature of  $C_R$ . If any redex is reduced to “abort”,

the computation is aborted since it can be proved that the expression object of the computation has no *constructor* normal form.

**Example 2.** Consider the rules defining the operation that concatenates two lists, denoted by the infix identifier “++”:

$$\begin{aligned} [] ++ y &= y \\ (x : xs) ++ y &= x : (xs ++ y) \end{aligned} \quad (2)$$

The definitional tree of operation “++” is pictorially represented below. The only branch node of this tree is the root. The inductive variable of this branch, boxed in the representation, is  $x$ . The rule nodes of this tree are the two leaves. There are no exempt nodes in this tree since operation “++” is completely defined.



Applying procedure `compile` to this tree produces the following output:

$$\begin{aligned} \mathbf{H}([] ++ []) &= [] && \text{compile line \#14} \\ \mathbf{H}([] ++ (y : ys)) &= (y : ys) && \text{compile line \#14} \\ \mathbf{H}([] ++ y) &= \mathbf{H}(y) && \text{compile line \#15} \\ \mathbf{H}((x : xs) ++ y) &= x : (xs ++ y) && \text{compile line \#10} \\ \mathbf{H}(x ++ y) &= \mathbf{H}(\mathbf{H}(x) ++ y) && \text{compile line \#04} \end{aligned} \quad (4)$$

Operation  $\mathbf{N}$  of the object code is defined by one rule for each symbol of  $R$ . In the following *metarules*,  $c/m$  stands for a constructor of  $R$ ,  $f/n$  stands for an operation of  $R$ , and  $x_i$  is a fresh variable for every  $i$ .

$$\begin{aligned} \mathbf{N}(c(x_1, \dots, x_m)) &= c(\mathbf{N}(x_1), \dots, \mathbf{N}(x_m)) \\ \mathbf{N}(f(x_1, \dots, x_n)) &= \mathbf{N}(\mathbf{H}(f(x_1, \dots, x_n))) \end{aligned} \quad (5)$$

**Example 3.** The rules of  $\mathbf{N}$  for the list constructors and the operation “++” defined earlier are:

$$\begin{aligned} \mathbf{N}([]) &= [] \\ \mathbf{N}(x : xs) &= \mathbf{N}(x) : \mathbf{N}(xs) \\ \mathbf{N}(x ++ y) &= \mathbf{N}(\mathbf{H}(x ++ y)) \end{aligned} \quad (6)$$

**Definition 2.** The rewrite system consisting of the  $\mathbf{H}$  rules generated by procedure `compile` for the operations of  $R$  and the  $\mathbf{N}$  rules generated according to (5) for all the symbols of  $R$  is the object code of  $R$  and is denoted  $C_R$ .

**Example 4.** We show the computation of  $[1] ++ [2]$  in both  $R$  and  $C_R$ . We use the desugared notation for list expressions to more easily match the patterns of the rules of “++”.

$$(1 : []) ++ (2 : []) \rightarrow 1 : ( [] ++ (2 : [])) \rightarrow 1 : 2 : [] \quad (7)$$

and

$$\begin{aligned}
& \mathbf{N}((1: []) ++ (2: [])) \\
& \rightarrow \mathbf{N}(\mathbf{H}((1: []) ++ (2: []))) \\
& \rightarrow \mathbf{N}(1: ([] ++ (2: []))) \\
& \rightarrow \mathbf{N}(1) : \mathbf{N}([] ++ (2: [])) \\
& \rightarrow 1 : \mathbf{N}(\mathbf{H}([] ++ (2: []))) \\
& \rightarrow 1 : \mathbf{N}(2: []) \\
& \rightarrow 1 : \mathbf{N}(2) : \mathbf{N}([]) \\
& \rightarrow 1 : 2 : []
\end{aligned} \tag{8}$$

Computation (8) is longer than (7). If all the occurrences of  $\mathbf{N}$  and  $\mathbf{H}$  are "erased" from the states of (8), a concept formalized shortly, and repeated states of the computation are removed, the remaining steps are the same as in (7). The introduction and removal of occurrences of  $\mathbf{N}$  and  $\mathbf{H}$  in (8), which lengthen the computation, represent the control, what to rewrite and when to stop. These activities occur in (7) too, but are in the mind of the reader rather than explicitly represented in the computation.

## 4 Compilation Properties

$C_R$ , the object code of  $R$ , correctly implements  $R$ . Computations performed by  $C_R$  produce the results of corresponding computations in  $R$  as formalized below. Furthermore,  $C_R$  implements a needed strategy, because every reduction performed by  $C_R$  is a needed reduction in  $R$ . In this section, we prove these properties of the object code.

Let  $Expr$  be the set of expressions over the signature of  $C_R$  output by `compile` on input a rewrite system  $R$ . The erasure function  $\mathcal{E} : Expr \rightarrow Expr$  is inductively defined by:

$$\begin{aligned}
\mathcal{E}(H(t)) &= \mathcal{E}(t) \\
\mathcal{E}(N(t)) &= \mathcal{E}(t) \\
\mathcal{E}(s(t_1, \dots, t_n)) &= s(\mathcal{E}(t_1), \dots, \mathcal{E}(t_n)) \text{ for } s/n \in \Sigma_R
\end{aligned} \tag{9}$$

Intuitively, the erasure of an expression  $t$  removes all the occurrences of  $\mathbf{H}$  and  $\mathbf{N}$  from  $t$ . The result is an expression over the signature of  $R$ .

**Lemma 3.** *Let  $R$  be an inductively sequential system and  $\mathbf{H}$  the head function of  $C_R$ . For any operation-rooted expression  $t$  of  $R$ ,  $\mathbf{H}(t)$  is a redex.*

*Proof.* Let  $f/n$  be the root of  $t$ , and  $\mathcal{T}$  the definitional tree of  $f$  input to procedure `compile`. The pattern at the root of  $\mathcal{T}$  is  $f(x_1, \dots, x_n)$ , where each  $x_i$  is a variable. Procedure `compile` outputs a rule of  $\mathbf{H}$  with left-hand side  $\mathbf{H}(f(x_1, \dots, x_n))$ . Hence this rule, or a more specific one, reduces  $t$ .  $\square$

Comparing graphs modulo a renaming of nodes, as in the next proof, is a standard technique [11] due to the fact that any node created by a rewrite is fresh.

**Lemma 4.** *Let  $R$  be an inductively sequential system and  $\mathbf{H}$  the head function of  $C_R$ . Let  $t$  be an operation-rooted expression of  $R$ , and  $\mathbf{H}(t)$  be reduced by a step resulting from the application of a rule  $r$  originating from statement 04 of procedure `compile`. The argument of the inner application of  $\mathbf{H}$  in the contractum is both operation-rooted and needed for  $t$ .*

*Proof.* Let  $\mathcal{T}$  be a definitional tree of the root of  $t$ . Let  $\pi$  be the pattern of the branch node  $n$  of  $\mathcal{T}$  from which rule  $r$  originates and let  $o$  be the inductive node of  $\pi$ . Since  $r$  rewrites  $t$  and  $\pi$  is the left-hand side of  $r$  modulo a renaming of variables and nodes, there exists a graph homomorphism  $\sigma$  such that  $t = \sigma(\pi)$ . Our convention on the specificity of the rules defining  $\mathbf{H}$  establishes that no rule textually





*Proof.* We prove that for every index  $i$  such that  $e_i$  is a state of  $A$ , every argument of an application of  $\mathbf{H}$  in  $e_i$  is needed for  $\mathcal{E}(e_i)$ . Preliminarily, we define a relation “ $\prec$ ” on the nodes of the states of  $\mathcal{E}(A)$  as follows. Let  $p$  and  $q$  be nodes of states  $\mathcal{E}(e_i)$  and  $\mathcal{E}(e_j)$  of  $\mathcal{E}(A)$  respectively. We define  $p \prec q$  iff  $i < j$  or  $i = j$  and the expression at  $q$  is a proper subexpression of the expression at  $p$  in  $\mathcal{E}(e_i)$ . Relation “ $\prec$ ” is a well-founded ordering with minimum element the root of  $t$ . The proof of the theorem is by induction on “ $\prec$ ”. Base case: Directly from the definition of “need”, since  $t$  is rooted by an operation of  $R$ . Induction case: Let  $q$  be the root of the argument of an application of  $\mathbf{H}$  in  $e_j$  for  $j > 0$ . We distinguish whether  $q$  is the root of the argument of an application of  $\mathbf{H}$  in  $e_{j-1}$ . If it is, then the claim is a direct consequence of the induction hypothesis. If it is not,  $e_{j-1} \rightarrow e_j$  is an application of a rule  $r$  generated by one of the statements 04, 08 or 15 of procedure `compile`. For statement 04, there is a node  $p$  of  $\mathcal{E}(e_j)$  that by the induction hypothesis is needed for  $\mathcal{E}(e_j)$  and matches the pattern  $\pi$  of the branch node of a definitional tree from which rule  $r$  originates. Let  $q$  be the node of the subexpression of  $e_j$  rooted by  $p$  matched by  $\pi$  at  $o$ . By Lemma 4,  $q$  is needed for  $p$ . Since  $p$  is needed for  $\mathcal{E}(e_j)$ , by Lemma 2,  $q$  is needed for  $\mathcal{E}(e_j)$  and the claim holds. For statements 08 and 15,  $q$  is the root of the contractum of the redex matched by  $r$  which by the induction hypothesis is needed for  $\mathcal{E}(e_{j-1})$ . Node  $q$  is still labeled by an operation, hence it is needed for  $\mathcal{E}(e_j)$  directly by the definition of “need”.  $\square$

**Corollary 1.** *Let  $R$  be an inductively sequential system. Let  $t$  be an expression of  $R$  and let  $A$  denote an innermost finite or infinite computation  $\mathbf{N}(t) = e_0 \rightarrow e_1 \rightarrow \dots$  in  $C_R$ . Every step of  $\mathcal{E}(A)$  is needed.*

*Proof.* Operation  $\mathbf{N}$  of  $C_R$  applied to an expression  $t$  of  $R$  applies operation  $\mathbf{H}$  to every outermost operation-rooted subexpression of  $t$ . All these expressions are needed by Def. 1. The claim is therefore a direct consequence of Th. 1.  $\square$

**Corollary 2.** *Let  $R$  be an inductively sequential system. For all expressions  $t$  and constructor forms  $u$  of  $R$ ,  $t \xrightarrow{*} u$  in  $R$  if, and only if,  $\mathbf{N}(t) \xrightarrow{*} u$  in  $C_R$  modulo a renaming of nodes.*

*Proof.* Let  $A$  denote some innermost computation of  $\mathbf{N}(t)$ . Observe that if  $A$  terminates in a constructor form  $u$  of  $R$ , then every innermost computation of  $\mathbf{N}(t)$  terminates in  $u$  because the order of the reductions is irrelevant. Therefore, we consider whether  $A$  terminates normally. Case 1:  $A$  terminates normally. If  $\mathbf{N}(t) \xrightarrow{*} u$ , then by Lemma 5, point 1,  $t \xrightarrow{*} u$ . Case 2:  $A$  does not terminate normally. We consider whether  $A$  aborts. Case 2a:  $A$  aborts. Suppose  $\mathbf{N}(t) = e_0 \rightarrow e_1 \rightarrow \dots \rightarrow e_i$ , and the step of  $e_i$  reduces a redex  $r$  to “abort”. By Theorem 1,  $r$  is needed for  $e_i$ , but there is no rule in  $R$  that reduces  $r$ , hence  $t$  has no constructor form. Case 2b:  $A$  does not terminate. Every step of  $\mathcal{E}(A)$  is needed. The complete tree unraveling [9, Def. 13.2.9] of the rules of  $R$  and the states of  $\mathcal{E}(A)$ , gives an orthogonal *term* rewriting system and a computation of the unraveled  $t$ . Since redexes are innermost, in this computation an infinite number of needed redexes are reduced. The hypernormalization of the needed strategy [9, Sect. 9.2.2] shows that hence  $t$  has no constructor form.  $\square$

The object code  $C_R$  for a rewrite system  $R$  is subjectively simple. Since innermost reductions suffice for the execution, operations  $\mathbf{H}$  and  $\mathbf{N}$  can be coded as functions that take their argument by-value. This is efficient in most programming languages. Corollary 2, in conjunction with Theorem 1, shows that  $C_R$  is a good object code: it produces the value of an expression  $t$  when  $t$  has such value, and it produces this value making only steps that must be made by any rewrite computation. One could infer that there cannot be a substantially better object code, but this is not true. The next section discusses why.

## 5 Transformation

We transform the object code to avoid totally, or partially, constructing certain contracta. The transformation consists of two phases.

The first phase replaces certain rules of  $\mathbf{H}$ . Let  $r$  be a rule of  $\mathbf{H}$  in which  $\mathbf{H}$  is recursively applied to a variable, say  $x$ , as in the third rule of (4). Rule  $r$  is replaced by the set  $S_r$  of rules obtained as follows. A rule  $r_f$  is in  $S_r$ , iff  $r_f$  is obtained from  $r$  by instantiating  $x$  with  $f(x_1, \dots, x_n)$ , where  $f/n$  is an operation of  $R$ ,  $x_1, \dots, x_n$  are fresh variables, and the sorts of  $f(x_1, \dots, x_n)$  and  $x$  are the same. If a rule in  $S_r$  still applies  $\mathbf{H}$  to another variable, it is again replaced in the same way.

**Example 6.** *The following rule originates from instantiating  $y$  for “++” in the third rule of (4).*

$$\mathbf{H}([\ ] ++ (u ++ v)) = \mathbf{H}(u ++ v) \quad (10)$$

The first phase of the transformation ensures that  $\mathbf{H}$  is always applied to an expression rooted by some operation  $f$  of  $R$ . The second phase introduces, for each operation  $f$  of  $R$ , a new operation, denoted  $\mathbf{H}_f$ . This operation is the composition of  $\mathbf{H}$  with  $f$ , and then replaces every occurrence of the composition of  $\mathbf{H}$  with  $f$  with  $\mathbf{H}_f$ .

**Example 7.** *The second phase transforms (10) into:*

$$\mathbf{H}_{++}([\ ], u ++ v) = \mathbf{H}_{++}(u, v) \quad (11)$$

After the second phase, operation  $\mathbf{H}$  can be eliminated from the object code since it is no longer invoked. We denote the transformed  $C_R$  with  $T_R$  and the outcome of the first phase on  $C_R$  with  $C'_R$ . The mapping  $\tau$ , from expressions of  $C_R$  to expressions of  $T_R$ , formally defines the transformation:

$$\tau(t) = \begin{cases} \mathbf{H}_f(\tau(t_1), \dots, \tau(t_n)), & \text{if } t = \mathbf{H}(f(t_1, \dots, t_n)); \\ s(\tau(t_1), \dots, \tau(t_n)), & \text{if } t = s(t_1, \dots, t_n), \text{ with } s \text{ symbol of } R; \\ v, & \text{if } t = v, \text{ with } v \text{ variable.} \end{cases} \quad (12)$$

$T_R$  is more efficient than  $C_R$  because, for any operation  $f$  of  $R$ , the application of  $\mathbf{H}_f$  avoids the allocation of a node labeled by  $f$ . This node is also likely to be pattern matched later.

**Example 8.** *Consider the usual length-of-a-list operation:*

$$\begin{aligned} \text{length } [\ ] &= 0 \\ \text{length } (\_ : \text{xs}) &= 1 + \text{length } \text{xs} \end{aligned} \quad (13)$$

*The compilation of (13), where we omit rules irrelevant to the point we are making, produces:*

$$\begin{aligned} \mathbf{H}(\text{length}([\ ])) &= 0 \\ \mathbf{H}(\text{length}(\_ : \text{xs})) &= \mathbf{H}(1 + \text{length}(\text{xs})) \\ \dots & \end{aligned} \quad (14)$$

*The transformation of (14), where again we omit rules irrelevant to the point we are making, produces:*

$$\begin{aligned} \mathbf{H}_{\text{length}}([\ ]) &= 0 \\ \mathbf{H}_{\text{length}}(\_ : \text{xs}) &= \mathbf{H}_+(1, \text{length}(\text{xs})) \\ \dots & \end{aligned} \quad (15)$$

*Below, we show the traces of a portion of the computations of  $\mathbf{N}(\text{length } [7])$  executed by  $C_R$  (left) and  $T_R$  (right), where the number 7 is an irrelevant value. The rules of “+” are not shown. Intuitively, they evaluate the arguments to numbers, and then perform the addition.*

$\begin{aligned} & \mathbf{N}(\mathbf{H}(\text{length } [7])) \\ & \rightarrow \mathbf{N}(\mathbf{H}(1+\text{length } [])) \\ & \rightarrow \mathbf{N}(\mathbf{H}(1+\mathbf{H}(\text{length } []))) \\ & \rightarrow \mathbf{N}(\mathbf{H}(1+0)) \\ & \rightarrow \mathbf{N}(1) \\ & \rightarrow 1 \end{aligned}$	$\begin{aligned} & \mathbf{N}(\mathbf{H}_{\text{length}}([7])) \\ & \rightarrow \mathbf{N}(\mathbf{H}_+(1, \text{length } [])) \\ & \rightarrow \mathbf{N}(\mathbf{H}_+(1, \mathbf{H}_{\text{length}}([]))) \\ & \rightarrow \mathbf{N}(\mathbf{H}_+(1, 0)) \\ & \rightarrow \mathbf{N}(1) \\ & \rightarrow 1 \end{aligned}$
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$C_R$  constructs the expression rooted by the underlined occurrence of “+”, and later pattern matches it. The same expression is neither constructed nor pattern matched by  $T_R$ .

The transformation increases the size of a program. Certain rules are replaced by sets of rules. The number of rules in a replacing set is the number of constructors of some type. A coarse upper bound of the size of the transformed program is a constant factor of the size of the original program. Modern computers have gigabytes of memory. We believe that the growth in size could become a problem only in extreme cases, and likely would not be the most serious problem in those cases.

## 6 Transformation Properties

We show that both phases of the transformation described in the previous section preserve the object code computations.

**Lemma 6.** *Let  $R$  be an inductively sequential system. Every step of  $C_R$  is a step of  $C'_R$  and vice versa, modulo a renaming of nodes.*

*Proof.* Every rule of  $C'_R$  is an instance of a rule of  $C_R$ . Hence every step of  $C'_R$  is a step of  $C_R$ . For the converse, let  $t \rightarrow u$  be a step of  $C_R$  where some rule  $r$  is applied. It suffices to consider the case in which  $t$  is the redex and the rule  $r$  applied in the step is not in  $C'_R$ . Let  $v$  be the variable “wrapped” by  $\mathbf{H}$  in  $r$ . Rule  $r$  is output by statement either 04 or 15 of procedure `compile`. We show that in both cases the match of  $v$ , say  $s$ , is an operation-rooted subexpression of  $t$ . If  $r$  is output by statement 04, this property is ensured by Lemma 4. If  $r$  is output by statement 15, and the match of  $v$  were constructor-rooted, then some rule output by statement 14 of procedure `compile`, which textually precedes  $r$  and is tried first, would match  $t$ . Therefore, let  $f/n$  be the root of  $s$ . By the definition of phase 1 of the transformation, rule  $r[f(x_1, \dots, x_n)/v]$  is in  $C'_R$ . Therefore, modulo a renaming of nodes,  $t \rightarrow u$  in  $C'_R$   $\square$

**Corollary 3.** *Let  $R$  be an inductively sequential system. For every operation-rooted expression  $t$  and head-constructor form  $u$  of  $R$ ,  $\mathbf{H}(t) \xrightarrow{+} u$  in  $C'_R$  if, and only if,  $\tau(\mathbf{H}(t)) \xrightarrow{+} u$  in  $T_R$  modulo a renaming of nodes.*

*Proof.* Preliminarily, we show that for any  $s$ ,  $\mathbf{H}(t) \rightarrow s$  in  $C'_R$  iff  $\tau(\mathbf{H}(t)) \rightarrow \tau(s)$  in  $T_R$ . Assume  $\mathbf{H}(t) \rightarrow s$  in  $C'_R$ . There exists a rule  $l \rightarrow r$  of  $C'_R$  and a match (graph homomorphism)  $\sigma$  such that  $\mathbf{H}(t) = \sigma(l)$  and  $s = \sigma(r)$ . From the definition of phase 2 of the transformation,  $\tau(l) \rightarrow \tau(r)$  is a rule of  $T_R$ . We show that this rule reduces  $\tau(\mathbf{H}(t))$  to  $\tau(s)$ . Since  $\tau$  is the identity on variables, and  $\sigma$  is the identity on non variables,  $\sigma \circ \tau = \tau \circ \sigma$ . Thus  $\tau(\mathbf{H}(t)) = \tau(\sigma(l)) = \sigma(\tau(l)) \rightarrow \sigma(\tau(r)) = \tau(\sigma(r)) = \tau(s)$ . The converse is similar because there a bijection between the steps of  $C'_R$  and  $T_R$ .

Now, we prove the main claim. First, the claim just proved holds also when  $\mathbf{H}(t)$  is in a context. Then, an induction on the length of  $\mathbf{H}(t) \xrightarrow{+} u$  in  $C'_R$  shows that  $\tau(\mathbf{H}(t)) \xrightarrow{+} \tau(u)$  in  $T_R$ . Since by assumption  $u$  is an expression of  $R$ , by the definition of  $\tau$ ,  $\tau(u) = u$ .  $\square$

Finally, we prove that object code and transformed object code execute the same computations.

**Theorem 2.** *Let  $R$  be an inductively sequential system. For all expressions  $t$  and  $u$  of  $R$ ,  $\mathbf{N}(t) \xrightarrow{+} u$  in  $C_R$  if, and only if,  $\mathbf{N}(t) \xrightarrow{+} u$  in  $T_R$ .*

*Proof.* In the computation of  $\mathbf{N}(t)$  in  $C_R$ , by the definition of  $\tau$ , each computation of  $\mathbf{H}(s)$  in  $C_R$ , for some expression  $s$ , is transformed into a computation of  $\tau(\mathbf{H}(s))$  in  $T_R$ . By Lemma 5, the former ends in a head-constructor form of  $R$ . Hence, by Corollary 3,  $\tau(\mathbf{H}(s))$  ends in the same head-constructor form of  $R$ . Thus,  $\mathbf{N}(t) \xrightarrow{+} u$  in  $T_R$  produces the same result. The converse is similar.  $\square$

## 7 Benchmarking

Our benchmarks use integer values. To accommodate a built-in integer in a graph node, we define a kind of node whose label is a built-in integer rather than a signature symbol. An arithmetic operation, such as addition, retrieves the integers labeling its argument nodes, adds them together, and allocates a new node labeled by the result of the addition.

Our first benchmark evaluates  $\text{length}(l_1++l_2)$ , where  $\text{length}$  is the operation defined in (13). In the table below, we compare the same rewriting computation executed by  $C_R$  and  $T_R$ . We measure the number of rewrite and shortcut steps executed, the number of nodes allocated, and the number of node labels compared by pattern matching. The ratio between the execution times of  $T_R$  and  $C_R$  varies with the implementation language, the order of execution of some instructions, and other code details that would seem irrelevant to the work being performed. Therefore, we measure quantities that are language and code independent. The tabular entries are in units per 10 rewrite steps of  $C_R$ , and are constant functions of this value except for very short lists. For lists of one million elements, the number of rewrite steps of  $C_R$  is two million.

$\text{length}(l_1++l_2)$	$C_R$	$T_R$	$O_R$
rewrite steps	10	6	6
shortcut steps	0	4	4
node allocations	20	16	12
node matches	40	26	18

The column labeled  $O_R$  refers to object code that further shortcuts needed steps using the same idea behind the transformation. For example, in the second rule of (14), both arguments of the addition in the right-hand side are needed. This information is known at compile-time, therefore the compiler can wrap an application of  $\mathbf{H}$  around the right operand of “+” in the right-hand side of the rule.

$$\mathbf{H}(\text{length}(\_ : xs)) = \mathbf{H}(1+\mathbf{H}(\text{length}(xs))) \quad (16)$$

The composition of  $\mathbf{H}$  with  $\text{length}$  is replaced by  $\mathbf{H}_{\text{length}}$  during the second phase. The resulting rule is:

$$\mathbf{H}_{\text{length}}(\_ : xs) = \mathbf{H}_+(1, \mathbf{H}_{\text{length}}(xs)) \quad (17)$$

Of course, there is no need to allocate a node for expression 1, the first argument of the addition, every time rule (15) or (17) is applied. A single node can be shared by the entire computation. However, since the first argument of the application of  $\mathbf{H}_+$  is constant, this application can be specialized or partially evaluated as follows:

$$\mathbf{H}_{\text{length}}(\_ : \text{xs})) = \mathbf{H}_{+1}(\mathbf{H}_{\text{length}}(\text{xs})) \quad (18)$$

The application of rule (18) allocates no node of the contractum. In our benchmarks, we ignore any optimization that is not directly related to shortcutting. Thus  $C_R$ ,  $T_R$  and  $O_R$  needlessly allocate this node every time these rules are applied.

The number of shortcut steps of  $T_R$  and  $O_R$  remain the same because, loosely speaking,  $O_R$  shortcuts a step that was already shortcut by  $T_R$ , but the number of nodes allocated and matched further decreases. The effectiveness of  $T_R$  to reduce node allocations or pattern matching with respect to  $C_R$  varies with the program and the computation.

Our second benchmark computes the  $n$ -th Fibonacci number for a relatively large value of  $n$ . The program we compile is:

$$\begin{aligned} \text{fib } 0 &= 0 \\ \text{fib } 1 &= 1 \\ \text{fib } n &= \text{fib } (n-1) + \text{fib } (n-2) \end{aligned} \quad (19)$$

To keep the example simple, we assume that pattern matching is performed by scanning the rules in textual order. Therefore, the last rule is applied only when the argument of `fib` is neither 0 nor 1.

$\text{fib}(n)$	$C_R$	$T_R$	$O_R$
rewrite steps	10	8	8
shortcut steps	0	2	2
node allocations	24	22	10
node matches	44	26	16

The tabular entries are in units per 10 rewrite steps of  $C_R$  and are constant functions of this value except for very small arguments of `fib`. For  $n = 32$ , the number of steps of  $C_R$  is about 17.5 million. With respect to  $C_R$ ,  $T_R$  avoids the construction of the root of the right-hand side of the third rule of (19).  $O_R$  transforms the right-hand side of this rule into:

$$\mathbf{H}_{+}(\mathbf{H}_{\text{fib}}(\mathbf{H}_{-}(n, 1)), \mathbf{H}_{\text{fib}}(\mathbf{H}_{-}(n, 2))) \quad (20)$$

since every node that is not labeled by the variable or the constants 1 and 2 is needed. In this benchmark, there is also no need to allocate a node for either 1 or 2 every time (20) is constructed/executed. With this further optimization, the step would allocate no new node for the contractum, and the relative gains of our approach would be even more striking.

## 8 Functional Logic Programming

Our work is motivated by the implementation of functional logic languages. The graph rewriting systems modeling functional logic programs are a superset of the inductively sequential ones. A minimal extension consists of a single binary operation, called *choice*, denoted by the infix symbol “?”. An expression  $x ? y$  reduces non-deterministically to  $x$  or  $y$ . There are approaches [5, 6] for rewriting computations involving the *choice* operation that produce all the values of an expression without ever making a non-deterministic choice. These approaches are ideal candidates to host our compilation scheme.

Popular functional logic languages allow variables, called *extra variables*, which occur in the right-hand side of a rewrite rule, but not in the left-hand side. Computations with extra variables are executed by *narrowing* instead of rewriting. Narrowing simplifies encoding certain programming problems into

programs [4]. Since our object code selects rules in textual order, and instantiates some variables of the rewrite system, narrowing with our object code is not straightforward. However, there is a technique [7] that transforms a rewrite system *with* extra variables into an equivalent system *without* extra variables. Loosely speaking, “equivalent”, in this context, means a system with the same input/output relation. In conjunction with this technique, our compiler generates code suitable for narrowing computations.

## 9 Related Work

The redexes that we reduce are needed to obtain a constructor-rooted expression, therefore they are closely related to the notion of *root-neededness* of [22]. However, we are interested only in normal forms that are constructor forms. In contrast to a computation according to [22], our object code may abort the computation of an expression  $e$  if no constructor normal form of  $e$  is reachable, even if  $e$  has a needed redex. This is a very desirable property in our intended domain of application since it saves useless rewrite steps, and in some cases may lead to the termination of an infinite computation.

Machines for graph reduction have been proposed [10, 20] for the implementation of functional languages. While there is a commonality of intent, these efforts differ from ours in two fundamental aspects. Our object code is easily translated into a low-level language like  $C$  or assembly, whereas these machines have instructions that resemble those of an interpreter. There is no explicitly notion of *need* in the computations performed by these machines. Optimizations of these machines are directed toward their internal instructions, rather than the needed steps of a computation by rewriting, a problem less dependent on any particular mechanism used to compute a normal form.

Our compilation scheme has similarities with deforestation [24], but is complementary to it. Both anticipate rule application, to avoid the construction of expressions that would be quickly taken apart and disposed. This occurs when a function producing one of these expressions is nested within a function consuming the expression. However, our expressions are operation-rooted whereas in deforestation they are constructor-rooted. These techniques can be used independently of each other and jointly in the same program.

A compilation scheme similar to ours is described in [2]. This effort makes no claims of correctness, of executing only needed steps and of shortcutting needed steps. Transformations of rewrite systems for compilation purposes are described in [12, 19]. These efforts are more operational than ours. A compilation with the same intent as ours is described in [8]. The compilation scheme is different. This effort does not claim to execute only needed steps, though it shortcuts some of them. Shortcutting is obtained by defining *ad-hoc* functions whereas we present a formal systematic way through specializations of the *head* function.

## 10 Conclusion

Our work addresses rewriting computations for the implementation of functional logic languages. We presented two major results.

The first result is a compilation scheme for inductively sequential graph rewriting systems. The object code generated by our scheme has very desirable properties: it is simple consisting of only two functions that take arguments by value, it is theoretically efficient by only executing needed steps, and it is complete in that it produces the value, when it exists, of any expression. The two functions of the object code are easily generated from the signature of the rewrite system and a traversal of the definitional trees of its operations.

The second result is a transformation of the object code that shortcuts some rewrite steps. Shortcutting avoids partial or total construction of the contractum of a step by composing one function of the object code with one operation symbol of the rewrite system signature. This avoids the construction of a node and in some cases and its subsequent pattern matching. Benchmarks show that the savings in node allocation and matching can be substantial.

Future work will rigorously investigate the extension of our compilation technique to rewrite systems with the *choice* operation and extra variables, as discussed in Sect. 8, as well as systematic opportunities to shortcut needed steps in situations similar to that discussed in Sect. 7.

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