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Circuit Modeling of Switched Linear Networks

Fardin Ansari
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THESIS APPROVAL

The abstract and thesis of Fardin Ansari for the Master of Science in Electrical and Computer Engineering were presented September 12, 1994, and accepted by the thesis committee and the department.

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ABSTRACT

An abstract of the thesis of Fardin Ansari for the Master of Science in Electrical and Computer Engineering presented September 12, 1994.

Title: Circuit Modeling of Switched Linear Networks.

The use of switched linear networks is prevalent in power processing systems such as dc-to-dc power converters. These converters provide regulated dc output power and are widely used in the power supplies for computers and peripherals.

As with most systems where high performance is a criterion, the power converter is usually enclosed in a feedback loop. Feedback design usually entails the use of small signal (linear) models with linear feedback design techniques.

There exists a large number of small signal models, but most of them are ad hoc. A describing function approach has previously been presented which determines the exact small signal frequency response of the converters. This method is now used to systematically derive circuit models for power converters. The aim of the work presented in this thesis is to model the nonlinear part of the switching converters which are simply the switches. This study is focused on the Pulse Width Modulation (PWM) class of converters. Majority of available models are not very accurate because they are averaged models. The aim of the new approach is to obtain accurate and simple model. A partial realization of this aim has been achieved; the new model is accurate over a very wide frequency range, but presently the parameters are rather complicated.

CIRCUIT MODELING OF SWITCHED LINEAR NETWORKS

by
FARDIN ANSARI

A thesis submitted in partial fulfillment of the
requirements for the degree of

MASTER OF SCIENCE
in
ELECTRICAL AND COMPUTER ENGINEERING

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1994

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Portland, Oregon

September 1994

Fardin Ansari

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CHAPTER 1

INTRODUCTION

In recent years, small-signal modeling of dynamic behaviors of open-loop dc-dc power converter has received notable amount of attention. This popularity is due to the fact that modeling of the converter by linear circuits is usually the basis by which feedback design is undertaken for designing reliable high performance regulators.

As with most systems, where high performance is a criterion, the power converter is enclosed in a feedback loop. The purpose of the feedback loop is to keep the performance of the system close to the desired operating conditions. In a power converter system, a feedback loop is designed to counteract the disturbances in the line voltage, v_g , and the load current, i_o , and subsequently regulate the output voltage, v_o . These disturbances can be assumed to be of small signal variations. Therefore, a linearized small-signal model is constructed to examine the dynamic behaviors of the converter. The open loop transfer functions $\hat{v}_o(s)/\hat{v}_g(s)$ and $\hat{v}_o(s)/\hat{i}_o(s)$ are derived from the small-signal model and plotted, e.g. Bode, for the frequency response analysis. If necessary, the system is compensated to meet the desired nominal operating conditions.

The importance of small-signal modeling in the design of power stages cannot be overstated. In power electronics, there are many different approaches to small-signal modeling. The majority of models work with only one specific control scheme, e.g. Pulse Width Modulation (PWM) duty-ratio or PWM current programming control. In the duty-ratio control, the switch ON-time is controlled by comparing a sawtooth

ramp with the controller voltage, v_c . In the current mode programming, the switch ON-time is determined by comparing a ramp proportional to the inductor current with the control voltage, v_c . So, in the current mode programming, both the duty ratio and inductor current control the length of the switch ON time. In this section, all the models are separated into three general modeling methods; the exact small-signal analysis techniques [1] – [4], the averaged models [5]–[10], and the sampled-data representation models [11]–[12].

The easiest and the most widely used method involves averaging. Averaged models are simple to derive but give inaccurate high frequency results. The circuit y-parameter can be constructed using the state space averaging approach [5], [6]. This model can be used to determine the various impedances and transfer functions of the system. It can also give the designer additional insight into the physical properties of the circuit. Another circuit oriented modeling technique involves the construction of the averaged models for the switch in the converter circuit [7]–[10]. This switch is called the PWM switch [8]. The PWM switch is a single pole double throw switch [8] responsible for the switching of the converter from one circuit configuration to another during each period. This modeling approach is similar to transistor modeling. The derivation of switch models are simpler than the converter models [9]. The limitation is that the PWM switch arrangement does not appear in all different converter topologies. The averaging techniques are generally used for the PWM duty-ratio programmed analysis.

The small-signal modeling of current mode programming is more complicated. The averaged models [5]–[10] cannot predict the response of the current mode programmed converters. Several attempts have been made to incorporate the current mode programming in the existing averaged models [13]–[16]. The price has always been complexity or inaccuracy of the augmented model.

Exact small–signal mathematical models [1] – [4] are very accurate. This method can be applied to any periodic, time–varying, piecewise, linear system. The price for the exactness is the complexity of the matrix manipulations. This modeling technique is automated in a power electronic circuit simulator, named PECS [3]. PECS can be used to perform analysis of higher order power electronics systems whose complicated behavior cannot be quickly analyzed by hand [2].

The sampled data models can be derived to obtain accurate response for current mode control [11]. The basic idea is to form a linear difference equation which describes propagation of a point on a converter waveform from one cycle to another. The analysis of the sampled data models is simpler than the exact small–signal models. However, the sampling theorem applies to the sampled data models which restricts the upper limit of the frequency range to half the switching frequency.

In general, the majority of models work with only one specific control scheme (be it the duty ratio [6], current mode control [11], etc.). From the above, it can be inferred that there is a need for a unified model applicable to various regulating schemes.

It is the purpose of this thesis to present a unified small–signal modeling approach applicable to any power converter system representable by two–port networks. This method can be used to represent the converter, the switch, or other three terminal components in the circuit by a two–port network and obtain the linearized small–signal model. Modeling the PWM switch is less complicated than modeling the whole circuit; therefore, in this thesis, the nonlinear PWM switch is replaced by its linearized small signal two–port model. The expressions for the model parameters are obtained using the small–signal describing functions [1], [3]. This model is very accurate up to the switching frequency. The new modeling approach can be used with any set of transfer functions such as state–space transfer functions to obtain expressions for the parameters.

In chapter 2, a review of hybrid parameter two–port model, state space averaging analysis, the PWM switch model, and an introduction to the new modeling approach is given. In section 2.1, the three terminal PWM switch is replaced by an augmented two–port hybrid parameter model. The proposed modeling approach is first used to obtain the state–space averaging model by applying it to a boost converter. A boost converter is one of the three basic converter types with PWM switch implementation. Using the new approach the PWM switch in the boost converter is replaced by a hybrid parameter switch model. Then, the hybrid parameters are evaluated using state–space control–to–output and input–to–output transfer functions. Next, the resulting parameter expressions are compared with the expressions obtained through the circuit averaging technique.

In chapter 3, a review of exact analysis of switching converters is presented followed by a section on the application of the time varying transfer function. In the latter, the exact small–signal analysis for PWM duty ratio programmed converters and PWM current programmed converters operating in continuous conduction mode (CCM) are presented.

In chapter 4, numeric expressions for the switch parameters are obtained using exact small–signal describing functions. First, the switch parameters for the boost converter are analyzed. Then, the control–to–output, $\hat{v}_o(s)/\hat{v}_c(s)$, and the input–to–output, $\hat{v}_o(s)/\hat{v}_g(s)$ responses of the model are plotted against the exact results. The control–to–output transfer function is part of the loop gain; therefore, its response is important for stability analysis of the regulator. The input–to–output response is however, considered less important when designing the regulator. This model fails to predict exact control–to–output frequency response. Therefore, a second, improved approach is proposed in chapter 5, which can produce exact control–to–output results. In order to obtain simple parameter expressions, the output capacitor is replaced

by a constant voltage source. This assumption reduces the number of states to one, namely the inductor current state, i_L . The reduced order model is then shown to be inaccurate for the input-to-output response analysis by testing the model on the buck and buckboost converters. The reason for this inaccuracy is discussed in chapter 6, where the third approach is presented. The small-signal control-to-output, and input-to-output frequency response of this model are compared with those of the exact transfer functions. It will be seen that the this model predicts the exact results at least up to the switching frequency. Next, the third model is used to analyze the buck and buckboost converters. The resulting plots show an excellent agreement with the exact small-signal control-to-output and input-to-output responses. The third modeling approach holds valid for both PWM duty-ratio and PWM current mode programming.

The price for the exactness of the third model is, however, the complexity of the parameter expressions. This model is an excellent choice for computer implementation. The current programmed model can be used to obtain the PWM duty ratio model by setting a parameter to zero. State space transfer functions can be used with this model which enables the designers to perform stability analysis, i.e. determine transfer functions, poles and zeros of the system.

CHAPTER 2

ANALYSIS PRELIMINARIES

2.1 Formulation of Equations

In this section, a unified small signal switch model based on two-port network analysis is proposed. A review of hybrid parameters, state space averaging analysis, and the PWM switch model will be given first. Next, the new hybrid parameter switch model will be introduced using state space averaging transfer functions. In the following chapters, this model is further developed using exact small signal transfer functions proposed in [1] and later extended in [3].

2.1.1 Two Port Networks

It is a common practice in circuit analysis to model three terminal devices in terms of their terminal characteristics using two-port networks. A two-port network is composed of linear elements such as R , L , C , and dependent sources. It contains no independent sources except at the ports. Figure 2 – 1 sketches the two-port network.

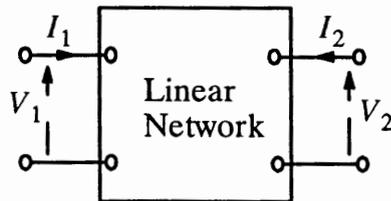


Figure 2 – 1 Two-port network.

Taking I_1 and V_2 to be the excitation variables (independent sources), it is possible to determine the other two response variables (dependent sources) through parameters known as *hybrid parameters*. As the network is assumed to be linear, it is possible to utilize the principle of superposition. By application of this principle, the values for the variables V_1 and I_2 can be determined. V_1 consists of two parts, one due to the input current (I_1) and another due to the output voltage (V_2). I_2 can be found in the same manner.

In general, two-port equations using the hybrid parameters are;

$$V_1 = h_{11}I_1 + h_{12}V_2 \quad (2 - 1)$$

$$I_2 = h_{21}I_1 + h_{22}V_2 \quad (2 - 2)$$

The hybrid equivalent circuit model representing equations (2 - 1) and (2 - 2) is shown in Figure 2 - 2.

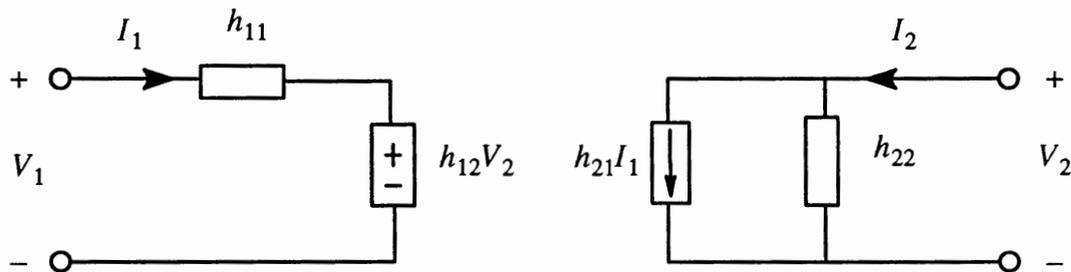


Figure 2 - 2 Equivalent circuit for two-port hybrid model.

The hybrid parameters are determined by the following equations:

$$\begin{aligned}
 h_{11} &= \left. \frac{V_1}{I_1} \right|_{V_2 = 0} \\
 h_{12} &= \left. \frac{V_1}{V_2} \right|_{I_1 = 0} \\
 h_{21} &= \left. \frac{I_2}{I_1} \right|_{V_2 = 0} \\
 h_{22} &= \left. \frac{I_2}{V_2} \right|_{I_1 = 0}
 \end{aligned} \tag{2-3}$$

where

h_{11} is the short circuit input impedance, h_{12} is the open-circuit reverse voltage gain, h_{21} is the short-circuit forward current gain, and h_{22} is the open-circuit output admittance.

2.1.2 State Space Analysis

Switching converters are nonlinear systems which can be described by the following general state and output equation:

$$\dot{x}(t) = A(t)x(t) + B(t)u(t) \tag{2-4}$$

$$y(t) = C(t)x(t) + E(t)u(t) \tag{2-5}$$

where $\{A(t), B(t), C(t), E(t)\}$ are the state, the input, the output, and the transmission matrices, respectively. The system is controlled by the complementary switching functions $q(t)$ and $q'(t)$ as shown below:

$$A(t) = A_1q(t) + A_2q'(t) \tag{2-6}$$

$$B(t) = B_1q(t) + B_2q'(t) \tag{2-7}$$

$$C(t) = C_1q(t) + C_2q'(t) \tag{2-8}$$

$$E(t) = E_1 q(t) + E_2 q'(t) \quad (2 - 9)$$

where $\{A_1, B_1, C_1, E_1\}$ and $\{A_2, B_2, C_2, E_2\}$ consist of linear electrical elements.

Since the converter switches between two subintervals then equations (2 – 4) may not be continuous over the entire switching period. This can be explained in terms of the energy storing elements such as capacitors and inductors in the converter. Inductor current, capacitor voltage, and their derivatives appearing as state variables in equations (2 – 4) must be continuous in time in order for the system to be considered continuous. In the converter circuits, the inductor current waveform is continuous, but its derivative which is the voltage across the inductor is discontinuous. Therefore, to perform circuit analysis techniques the discontinuous nonlinear system must be approximated to continuous linear system.

State space averaging is used to represent switching converters as linear continuous systems. In the sequel, two approximations have been used; 1) low frequency approximation 2) small-signal approximation.

2.1.2.a Low Frequency Approximation

As explained earlier, equations (2 – 4) and (2 – 5) are difficult to analyze over the entire frequency range. The main reason for this is the discontinuity of the switching times, $q(t)$ and $q'(t)$. Therefore, using low frequency approximation, $q(t)$ and $q'(t)$ are replaced by their low frequency components, d and d' , respectively. Hence,

$$A = A_1 d + A_2 d' \quad (2 - 10)$$

$$B = B_1 d + B_2 d' \quad (2 - 11)$$

$$C = C_1 d + C_2 d' \quad (2 - 12)$$

$$E = E_1 d + E_2 d' \quad (2 - 13)$$

The quadruples $\{A, B, C, E\}$ are functions of time. Equations (2 – 4) and (2 – 5)

can be rewritten in the following form:

$$\dot{x} = Ax + Bu \quad (2 - 14)$$

$$y = Cx + Eu \quad (2 - 15)$$

The validity of this approximation is based on the assumption that natural frequencies of the converter, regulator, and line variations are at least one decade lower than the switching frequency.

2.1.2.b Small Signal Approximation

Using state space averaging, equations (2 – 4) and (2 – 5) were replaced by continuous averaged equations (2 – 14) and (2 – 15). Now, the averaged equations are still nonlinear and need to be linearized. Therefore, all the variables in equations (2 – 14) and (2 – 15) are replaced by their steady state, and a perturbation term.

Perturbation in the duty ratio will result in

$$d = D + \hat{d} \quad (2 - 16)$$

and,

$$d' = 1 - d = 1 - D - \hat{d} = D' - \hat{d} \quad (2 - 17)$$

where D is the steady state value of the duty ratio and, \hat{d} is the perturbed duty ratio quantity.

Perturbing (2 – 14) and (2 – 15) gives:

$$\dot{X} + \hat{\dot{x}} = A(X + \hat{x}) + B(U + \hat{u}) \quad (2 - 18)$$

$$Y + \hat{y} = C(X + \hat{x}) + E(U + \hat{u}) \quad (2 - 19)$$

where d in A , B , C , and E is also perturbed as shown below:

$$A = A_1 (D + \hat{d}) + A_2 (D' - \hat{d}) \quad (2 - 20)$$

$$B = B_1(D + \hat{d}) + B_2 (D' - \hat{d}) \quad (2 - 21)$$

$$C = C_1(D + \hat{d}) + C_2(D' - \hat{d}) \quad (2 - 22)$$

$$E = E_1(D + \hat{d}) + E_2(D' - \hat{d}) \quad (2 - 23)$$

Substituting equations (2 - 20) and (2 - 21) into equation (2 - 18) gives:

$$\begin{aligned} \dot{X} + \hat{x} = & \left[A_1(D + \hat{d}) + A_2(D' - \hat{d}) \right] (X + \hat{x}) \\ & + \left[B_1(D + \hat{d}) + B_2(D' - \hat{d}) \right] (U + \hat{u}) \end{aligned} \quad (2 - 24)$$

Separating all the ac perturbation terms from (2 - 24) one gets:

$$\begin{aligned} \dot{\hat{x}} = & \left[A_1(D + \hat{d}) + A_2(D' - \hat{d}) \right] \hat{x} + (A_1\hat{d} - A_2\hat{d})X \\ & + \left[B_1(D + \hat{d}) + B_2(D' - \hat{d}) \right] \hat{u} + (B_1\hat{d} - B_2\hat{d})X \end{aligned} \quad (2 - 25)$$

which simplifies to:

$$\begin{aligned} \dot{\hat{x}} = & (A_1D + A_2D') \hat{x} + (A_1 - A_2)(X\hat{d}) + (A_1 - A_2)(\hat{x} \hat{d}) \\ & + (B_1D + B_2D') \hat{u} + (B_1 - B_2)(U\hat{d}) + (B_1 - B_2)(\hat{u} \hat{d}) \end{aligned} \quad (2 - 26)$$

Equations (2 - 26) contains nonlinear terms which are products of two perturbation terms.

Similarly, the output equation (2 - 19) can also be expanded using equations (2 - 22) and (2 - 23) to give the following ac result:

$$\begin{aligned} \dot{\hat{y}} = & (C_1D + C_2D') \hat{x} + (C_1 - C_2)(X\hat{d}) + (C_1 - C_2)(\hat{x} \hat{d}) \\ & + (E_1D + E_2D') \hat{u} + (E_1 - E_2)(U\hat{d}) + (E_1 - E_2)(\hat{u} \hat{d}) \end{aligned} \quad (2 - 27)$$

To remove the nonlinear terms from equations (2 - 26) and (2 - 27), the ac perturbations are assumed to be very small compared to the steady state operating values:

$$\|\hat{x}\| \ll \|X\|$$

$$\|\hat{d}\| \ll \|D\|$$

$$\|\hat{u}\| \ll \|U\|$$

$$\| \hat{y} \| \ll \| Y \|$$

where $\| \cdot \|$ denotes a matrix norm.

Dropping the product of ac terms in equations (2 – 26) and (2 – 27) and rearranging gives the following ac small signal result:

$$\hat{x} = A\hat{x} + B\hat{u} + B_d\hat{d} \quad (2 - 28)$$

$$y = C\hat{x} + E\hat{u} + E_d\hat{d} \quad (2 - 29)$$

$$A = DA_1 + D'A_2 \quad (2 - 30)$$

$$B = DB_1 + D'B_2 \quad (2 - 31)$$

$$C = DC_1 + D'C_2 \quad (2 - 32)$$

$$E = DE_1 + D'E_2 \quad (2 - 33)$$

$$B_d = (A_1 - A_2)X + (B_1 - B_2)U \quad (2 - 34)$$

$$E_d = (C_1 - C_2)X + (E_1 - E_2)U \quad (2 - 35)$$

The small-signal state space frequency response of an ideal dc-to-dc converter described by equation (2 – 29) is given by equation (2 – 36).

$$\hat{y}(j\omega) = \left(C(j\omega I - A)^{-1}B + E \right) \hat{u}(j\omega) + \left(C(j\omega I - A)^{-1}B_d + E_d \right) \hat{d}(j\omega) \quad (2 - 36)$$

2.1.3 PWM Switch model

The basic PWM converters are the buck, boost, and buckboost which are shown in Figure 2 – 3.

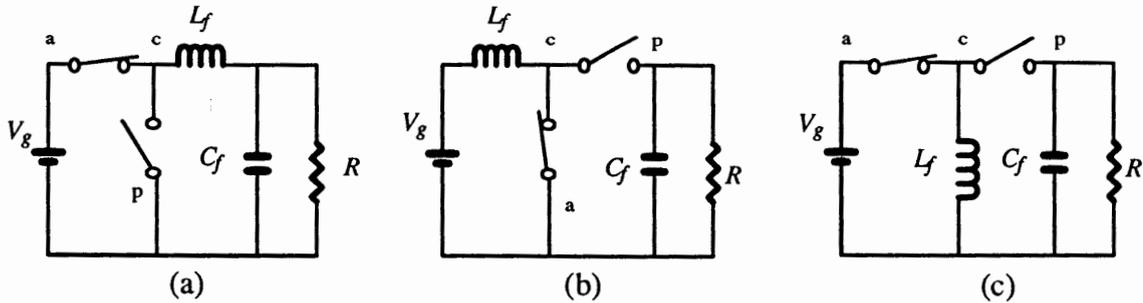


Figure 2 – 3 The three basic PWM converters. (a) Buck. (b) Boost. (c) Buck–boost.

The switch in these converters can be represented as a single–pole double–throw (SPDT) switch called the PWM switch. This implementation satisfies the condition that the switch pairs cannot both be ON or OFF at the same time. A PWM switch is a three–terminal device that may be depicted as in Figure 2 – 4, where a, p, and c refer to active, passive and common terminals. The active–common switch is usually a transistor, and the passive–common is a diode.

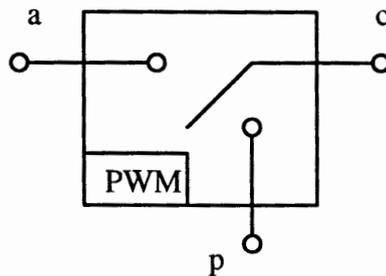


Figure 2 – 4 Symbol for PWM Switch.

The PWM switch can be modeled as a two–port network. The output of each port in the low frequency small signal sense is described by the frequency response relationship given in equation (2 – 36). The hybrid parameter model in Figure 2 – 2 can be augmented to incorporate the additional controller input terms as shown in Figure 2 – 5.

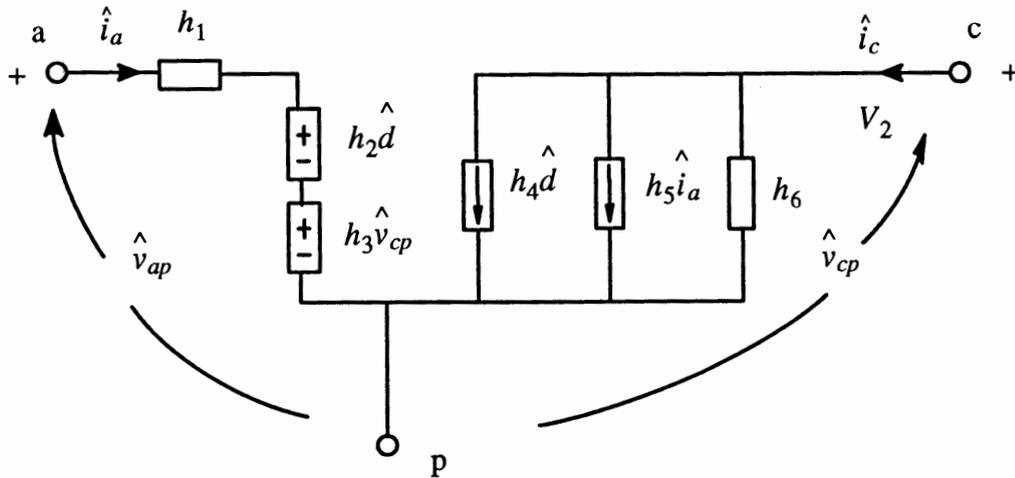


Figure 2 – 5 Hybrid parameter common passive switch model.

Mathematical description of Figure 2 – 5 is given below.

$$\hat{v}_{ap} = h_1 \hat{i}_a + h_2 \hat{d} + h_3 \hat{v}_{cp} \quad (2 - 37)$$

$$\hat{i}_c = h_4 \hat{i}_a + h_5 \hat{d} + h_6 \hat{v}_{cp} \quad (2 - 38)$$

Each port described by equations (2 – 37) and (2 – 38) has one output and three inputs. Furthermore, equations (2 – 37) and (2 – 38) can be expressed in terms of the small signal frequency response given in equation (2 – 36).

$$\begin{aligned} \hat{v}_{ap}(j\omega) = & \left(C(j\omega I - A)^{-1}B + E \right) \hat{i}_a(j\omega) + \left(C(j\omega I - A)^{-1}B_d + E_d \right) \hat{d}(j\omega) \\ & + \left(C(j\omega I - A)^{-1}B + E \right) \hat{v}_{cp}(j\omega) \end{aligned} \quad (2 - 39)$$

$$\begin{aligned} \hat{i}_c(j\omega) = & \left(C(j\omega I - A)^{-1}B + E \right) \hat{i}_a(j\omega) + \left(C(j\omega I - A)^{-1}B_d + E_d \right) \hat{d}(j\omega) \\ & + \left(C(j\omega I - A)^{-1}B + E \right) \hat{v}_{cp}(j\omega) \end{aligned} \quad (2 - 40)$$

To find the transfer function from one of the inputs to the output, the other two inputs must be set to zero. The model depicted in Figure 2 – 5 is a common passive hybrid parameter switch model. Other switch models can be found using the same technique.

In the following section, the hybrid parameter switch model for the PWM boost converter will be introduced. The switch in the boost converter is in the common active configuration as shown in Figure 2 – 3(b). In the chapters to follow, this switch model will be used to construct small signal equivalent circuits for other converters.

2.1.4 Hybrid Parameter Boost Switch Model

In this section, the parameters for the PWM boost converter switch model will be introduced. The switch configuration of the boost converter of Figure 2 – 6 will be represented by the hybrid parameter switch model shown in Figure 2 – 7.

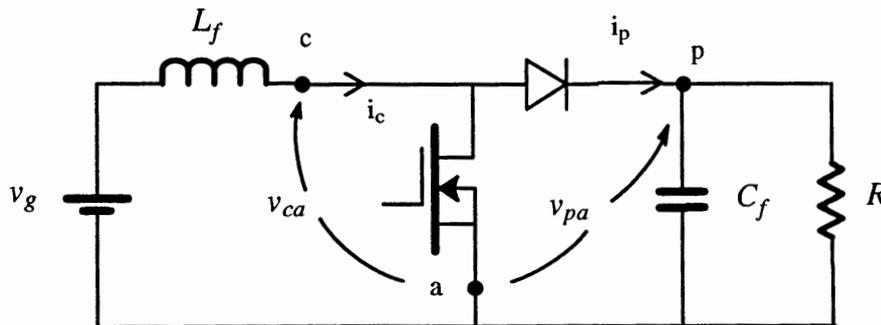


Figure 2 – 6 PWM boost converter.

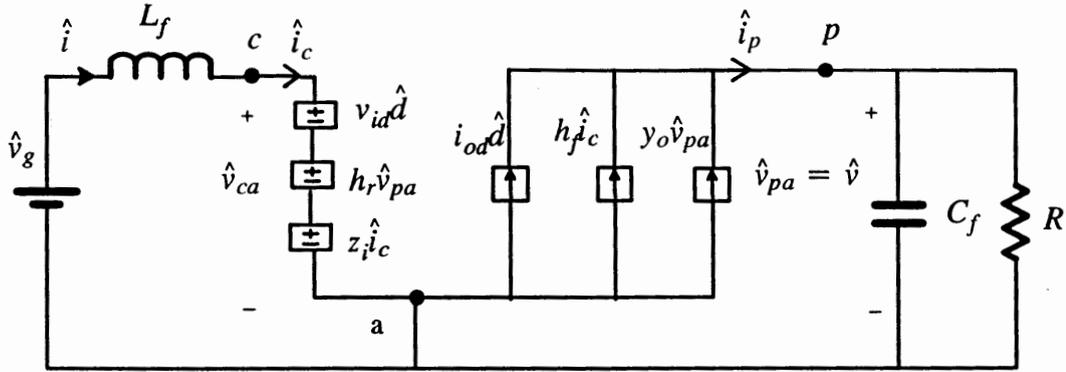


Figure 2 – 7 Small signal hybrid parameter model of the boost converter of Figure 2 – 6.

In Figure 2 – 7, the input port equation is

$$\hat{v}_{ca} = v_{id}\hat{d} + h_r\hat{v}_{pa} + z_i\hat{i}_c \quad (2 - 41)$$

and the output port equation is

$$\hat{i}_p = i_{od}\hat{d} + h_f\hat{i}_c + y_o\hat{v}_{pa} \quad (2 - 42)$$

The above set of equations describes the common active hybrid parameter switch model. The hybrid parameters are obtained by the superposition property of linear networks. In other words, each parameter is found by deactivating the effect of the other two sources in the port equation as shown below:

$$v_{id} = \left. \frac{\hat{v}_{ca}}{\hat{d}} \right|_{\substack{\hat{v}_{pa} = 0 \\ \hat{i}_c = 0}} \quad (2 - 43)$$

$$i_{od} = \left. \frac{\hat{i}_p}{\hat{d}} \right|_{\substack{\hat{v}_{pa} = 0 \\ \hat{i}_c = 0}} \quad (2 - 44)$$

$$h_r = \left. \frac{\hat{v}_{ca}}{\hat{v}_{pa}} \right|_{\substack{\hat{d} = 0 \\ \hat{i}_c = 0}} \quad (2 - 45)$$

$$h_f = \left. \frac{\hat{i}_p}{\hat{i}_c} \right|_{\substack{\hat{d} = 0 \\ \hat{v}_{pa} = 0}} \quad (2 - 46)$$

$$z_i = \left. \frac{\hat{v}_{ca}}{\hat{i}_c} \right|_{\substack{\hat{d} = 0 \\ \hat{v}_{pa} = 0}} \quad (2 - 47)$$

$$y_o = \left. \frac{\hat{i}_p}{\hat{v}_{pa}} \right|_{\substack{\hat{d} = 0 \\ \hat{i}_c = 0}} \quad (2 - 48)$$

Next, the state–space realization of the small–signal transfer functions has been used to obtain a state–space averaged model and the result has been verified using the circuit averaging method. The state–space analysis is performed to show the simplicity of the new method in obtaining equivalent circuit models.

2.2 Derivation of Equivalent State Space PWM Boost Model

In this section, the new hybrid parameter switch model is used to obtain a small signal state–space averaging model. Next, the same model is found using circuit averaging techniques. It is the purpose of this section to show, in its simplest form, how this modeling approach works.

An analytical expression for each parameter of equations (2 – 41) and (2 – 42) can be found by evaluating the appropriate state space transfer functions given in (2 – 49) and (2 – 50).

$$\frac{\hat{y}}{\hat{u}}(j\omega) = C(j\omega I - A)^{-1}B + E \quad (2 - 49)$$

$$\frac{\hat{y}}{\hat{d}}(j\omega) = C(j\omega I - A)^{-1}B_d + E_d \quad (2 - 50)$$

Consider the boost converter of Figure 2 – 6. For this converter to operate in continuous conduction mode, the state equations have to be obtained for both DT_s and $D'T_s$ subintervals. As shown in Figure 2 – 8, it is possible to find state and output matrices for each subinterval.

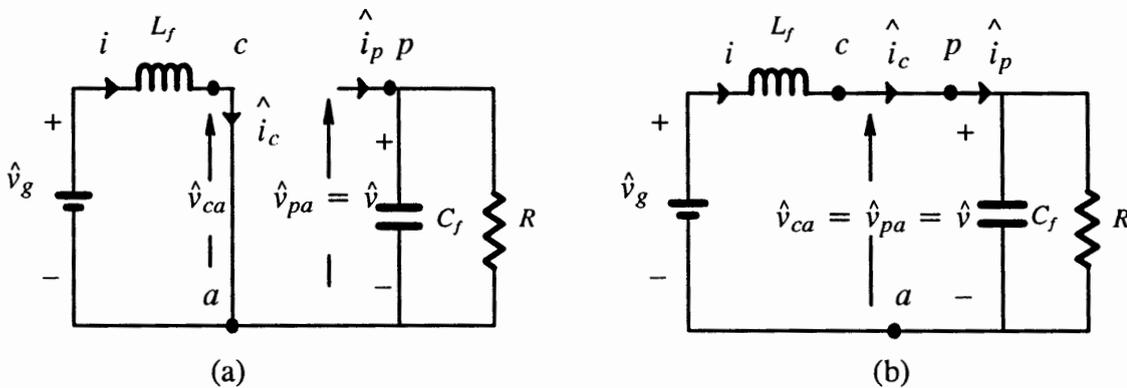


Figure 2 – 8 Boost converter of Figure 2 – 6 in; a) DT_s subinterval b) $D'T_s$ subinterval.

State equations are dependent solely on the states $x = [i_c \ v_{pa}]^T$ and input $u = Vg$ of the system. Each subinterval is described by a set of state equations.

The behavior of the converter in DT_s is described by

$$\dot{x} = A_1x + B_1u \quad (2 - 51)$$

and the state matrices for this subinterval are

$$A_1 = \begin{bmatrix} 0 & 0 \\ 0 & -1/RC_f \end{bmatrix}$$

and

$$B_1 = \begin{bmatrix} 1/L_f \\ 0 \end{bmatrix}$$

Similarly the behavior of the converter in $D'T_s$ is described by

$$\dot{x} = A_2x + B_2u \quad (2 - 52)$$

where

$$A_2 = \begin{bmatrix} 0 & -1/L_f \\ 1/C_f & -1/RC_f \end{bmatrix}$$

and

$$B_2 = \begin{bmatrix} 1/L_f \\ 0 \end{bmatrix}$$

Using equations (2 - 30), (2 - 31), and (2 - 34) one has:

$$A = \begin{bmatrix} 0 & -\frac{D'}{L_f} \\ \frac{D'}{C_f} & -\frac{1}{RC_f} \end{bmatrix} \quad B = \begin{bmatrix} 1/L_f \\ 0 \end{bmatrix} \quad B_d = \begin{bmatrix} \frac{V_{pa}}{L_f} \\ -\frac{I_c}{C_f} \end{bmatrix}$$

The C and E matrices are obtained using equations (2 – 32) and (2 – 33), respectively:

When $y = \hat{v}_{ca}$:

$$\begin{aligned} C_1 &= [0 \ 0] & E_1 &= 0 \\ C_2 &= [0 \ 1] & E_2 &= 0 \\ C &= [0 \ D'] \quad \text{and} \quad E &= 0 & (2 - 53) \end{aligned}$$

When $y = \hat{i}_p$:

$$\begin{aligned} C_1 &= [0 \ 0] & E_1 &= 0 \\ C_2 &= [1 \ 0] & E_2 &= 0 \\ C &= [D' \ 0] \quad \text{and} \quad E &= 0 & (2 - 54) \end{aligned}$$

When $y = \hat{i}_c$:

$$\begin{aligned} C_1 &= [1 \ 0] & E_1 &= 0 \\ C_2 &= [1 \ 0] & E_2 &= 0 \\ C &= [1 \ 0] \quad \text{and} \quad E &= 0 & (2 - 55) \end{aligned}$$

When $y = \hat{v}_{pa}$:

$$\begin{aligned} C_1 &= [0 \ 1] & E_1 &= 0 \\ C_2 &= [0 \ 1] & E_2 &= 0 \\ C &= [0 \ 1] \quad \text{and} \quad E &= 0 & (2 - 56) \end{aligned}$$

From now deactivating the effect of particular terms of an equation will be referred to as *nulling*. As evident from equations (2 – 41) and (2 – 42), two of the independent sources of the hybrid parameter model are the states of the system; therefore, any of the states can be nulled by setting the corresponding column of the C vector to zero.

First, the control parameter v_{id} can be solved for:

$$v_{id} = \frac{\hat{v}_{ca}}{\hat{d}} \left| \begin{array}{l} \hat{v}_{pa} = 0 \\ \hat{i}_c = 0 \end{array} \right.$$

Using (2 – 50) one has

$$v_{id} = C(j\omega I - A)^{-1} B_d + E_d$$

where

$$E_d = (C_1 - C_2)X + (E_1 - E_2)U$$

As a constraint imposed on equation (2 – 43), both states of equation (2 – 50) need to be nulled. Therefore, C in equation (2 – 53) will be forced to give:

$$C = [0 \ 0]$$

and consequently

$$v_{id} = E_d = -V_{pa} \quad (2 - 57)$$

Next an expression for the i_{od} parameter will be found using equation (2 – 44). Nulling both states of the corresponding C matrix will reduce equation (2 – 50) to :

$$i_{od} = E_d = -I_c \quad (2 - 58)$$

In general, nulling both states of equation (2 – 50) will give:

$$\frac{\hat{y}}{\hat{d}}(j\omega) = E_d \quad (2 - 59)$$

Solving for the h_r parameter of equation (2 – 45) is tricky. The h_r parameter is an output–to–output transfer function which cannot be solved unless it is put in a standard input–to–output form. This is done by dividing both numerator and denominator of equation (2 – 45) by the input \hat{v}_g . Equation (2 – 49) can now be used to obtain

an expression for the h_r parameter.

$$h_r = \frac{\hat{v}_{ca}/\hat{v}_g \Big|_{\substack{\hat{d} = 0 \\ \hat{i}_c = 0}}}{\hat{v}_{pa}/\hat{v}_g \Big|_{\substack{\hat{d} = 0 \\ \hat{i}_c = 0}}} \quad (2 - 60)$$

Since E equals zero for the numerator as well as the denominator of equation (2 - 45), then equation (2 - 49) reduces to:

$$h_r = C(j\omega I - A)^{-1}B \quad (2 - 61)$$

The numerator of the h_r is :

$$\hat{v}_{ca}/\hat{v}_g \Big|_{\substack{\hat{d} = 0 \\ \hat{i}_c = 0}} = [0 \ D'] \frac{1}{\Delta} \begin{bmatrix} s + \frac{1}{RC_f} & -\frac{D'}{L_f} \\ \frac{D'}{C_f} & s \end{bmatrix} \begin{bmatrix} \frac{1}{L_f} \\ 0 \end{bmatrix} = \frac{1}{\Delta} \frac{D'^2}{L_f C_f} \quad (2 - 62)$$

and the denominator is:

$$\hat{v}_{pa}/\hat{v}_g \Big|_{\substack{\hat{d} = 0 \\ \hat{i}_c = 0}} = [0 \ 1] \frac{1}{\Delta} \begin{bmatrix} s + \frac{1}{RC_f} & -\frac{D'}{L_f} \\ \frac{D'}{C_f} & s \end{bmatrix} \begin{bmatrix} \frac{1}{L_f} \\ 0 \end{bmatrix} = \frac{1}{\Delta} \frac{D'}{L_f C_f} \quad (2 - 63)$$

Therefore, the overall result is:

$$h_r = \frac{D'^2/L_f C_f}{D'/L_f C_f} = D' \quad (2 - 64)$$

As it is evident from C matrices of equations (2 - 62) and (2 - 63), the effect of the \hat{i}_c state is already zero. This could be explained better using the time domain output equation (2 - 29):

$$y = C\hat{x} + E\hat{u} + E_d\hat{d}$$

First, the controller term is nulled which reduces the above equation to

$$y = C\hat{x} + E\hat{u} \quad (2 - 65)$$

Using (2 - 53) in (2 - 65) gives

$$\hat{v}_{ca} = D'\hat{v}_{pa} \quad (2 - 66)$$

From equation (2 - 66) it is evident that \hat{i}_c has no effect on this output. The other parameters are found in the same manner. It would be rather tedious to go through all of the parameters in detail. Therefore, the solutions have been given below:

$$z_i = 0 \quad (2 - 67)$$

$$h_f = D' \quad (2 - 68)$$

$$y_o = 0 \quad (2 - 69)$$

The model can then be reduced to the one shown in Figure 2 - 9.

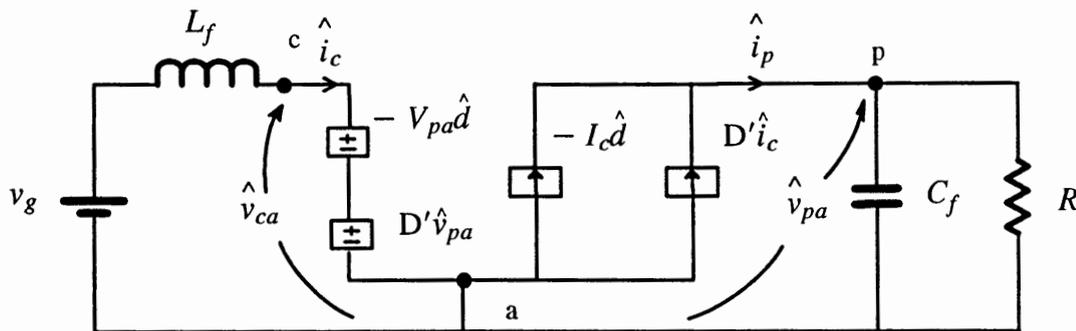


Figure 2 - 9 Hybrid parameter boost model using state space averaging results.

To verify the state space averaging results, the circuit averaging technique will be used to obtain the same model as depicted in Figure 2 - 9. In the circuit averaging method, the current and the voltage waveforms are averaged over one switching period T_s . The waveforms of the PWM switch of the boost converter of Figure 2 - 10 have

been depicted in Figure 2 – 11 .

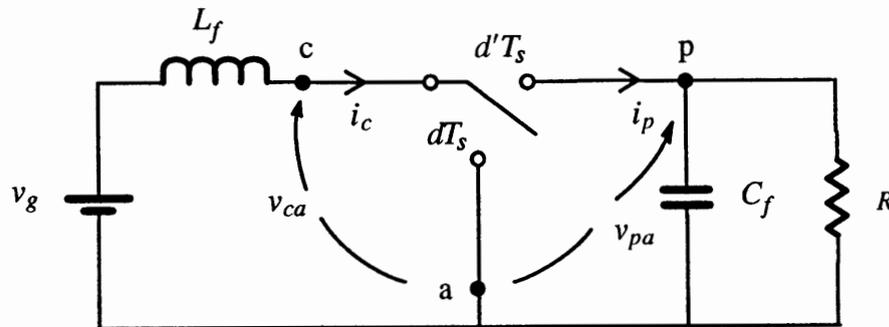


Figure 2 – 10 Typical PWM boost converter.

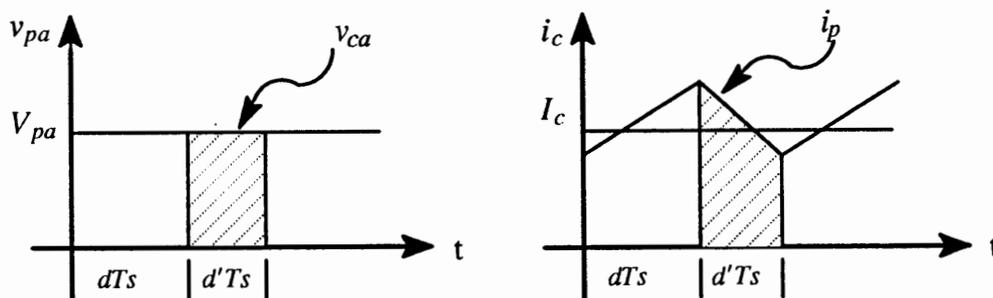


Figure 2 – 11 The terminal voltages and currents of the switch implementation of Figure 2 – 10.

The relationships between the average terminal currents and voltages of the switch are given by

$$v_{ca} = v_{pa} d' \quad (2 - 70)$$

and

$$i_p = i_c d' \quad (2 - 71)$$

Figure 2 – 10 is thus electrically equivalent to Figure 2 – 12.

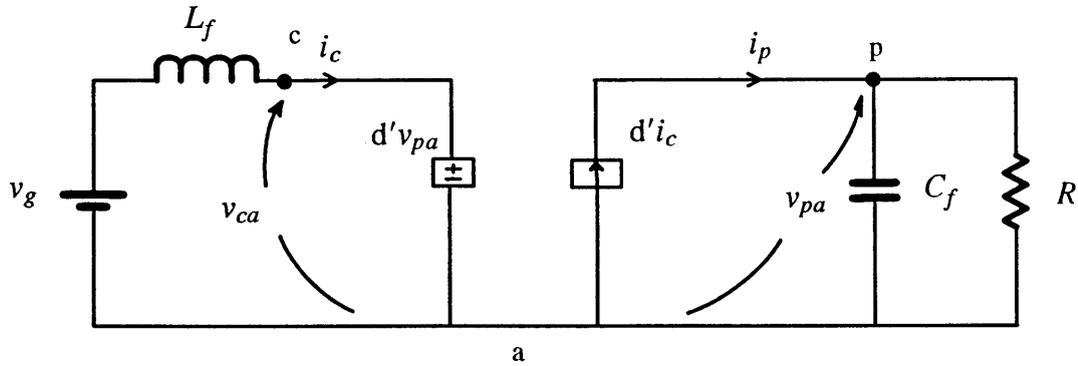


Figure 2 – 12 Averaged model of Figure 2 – 10.

Perturbing and linearizing equations (2 – 70) and (2 – 71) gives

$$v_{ca} = (V_{pa} + \hat{v}_{pa}) (D' - \hat{d}) = D'V_{pa} - \hat{d} V_{pa} \tag{2 - 72}$$

$$i_p = (I_c + \hat{i}_c) (D' - \hat{d}) = D'I_c - \hat{d} I_c \tag{2 - 73}$$

The final circuit averaging model is depicted in Figure 2 – 13. It can be observed that this model is identical to the hybrid parameter switch model presented earlier in Figure 2 – 9.

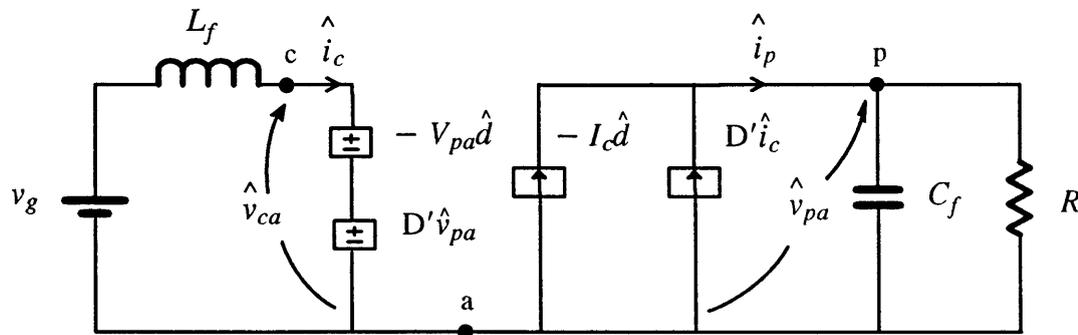


Figure 2 – 13 Small signal equivalent circuit model for the boost converter of Figure 2 – 10.

In the following chapters, different approaches are taken to accurately obtain expressions for the parameters of the hybrid parameter switch model.

CHAPTER 3

EXACT SMALL SIGNAL ANALYSIS OF PWM CONVERTERS

It was mentioned earlier that an exact small–signal switch model can be developed using the exact small–signal frequency analysis technique [1]–[3]. In section 3.1, the general analysis of switching converters is reviewed. In the next section, a general expression for the exact small–signal response of any piecewise linear network is given. This expression is further simplified for two specific cases:

- a. Duty ratio programmed PWM converters operating in the continuous conduction mode (CCM)
- b. Current programmed PWM converters operating in CCM.

3.1 Analysis of Switching Converters in Continuous Conduction Mode

A dc–to–dc converter operating in Continuous Conduction Mode (CCM) cycles through two switched topologies during each switching period. Since an ideal dc–to–dc converter is periodic and piecewise linear, it can be described by the following Linear Time Invariant (LTI) state equations:

$$\dot{x}(t) = A_i x(t) + B_i u(t) \tag{3 – 1}$$

$$y(t) = C_i^T x(t) + E_i u(t) \tag{3 – 2}$$

$t_i < t < t_{i+1}$ $i \in (1, 2)$ where t_i and t_{i+1} are the beginning and the ending times of each subinterval. The switching period is:

$$T_s = \sum_{i=1}^2 (t_{i+1} - t_i) \quad (3-3)$$

Dc-to-dc converters are controlled by changing the switching times, t_i and t_{i+1} , such that

$$t_i = T_i + \hat{t}_i \quad (3-4)$$

and

$$t_{i+1} = T_{i+1} + \hat{t}_{i+1}. \quad (3-5)$$

The complete solution of the state equation (3-1) at the end of each subinterval is:

$$x(t_{i+1}) = e^{A_i(t_{i+1}-t_i)} x(t_i) + \int_{t_i}^{t_{i+1}} e^{A_i(t_{i+1}-\tau)} B_i u(\tau) d\tau \quad (3-6)$$

For small signal analysis, the fluctuating line voltage $u(t)$ needs to be separated into the steady state component and the small signal perturbations.

$$u(t) = U + \hat{u}_k(t) = U + \hat{u} e^{j\omega t} \quad (3-7)$$

Substituting (3-7) in equation (3-6) gives

$$x(t_{i+1}) = e^{A_i(t_{i+1}-t_i)} x(t_i) + \int_{t_i}^{t_{i+1}} e^{A_i(t_{i+1}-\tau)} B_i U d\tau + \int_{t_i}^{t_{i+1}} e^{A_i(t_{i+1}-\tau)} B_i \hat{u} e^{j\omega \tau} d\tau \quad (3-8)$$

Evaluating the integral in equation (3-8) will give

$$\begin{aligned} x(t_{i+1}) = & e^{A_i(t_{i+1}-t_i)} x(t_i) + A_i^{-1} \left(e^{A_i(t_{i+1}-t_i)} - I \right) B_i U \\ & + (j\omega I - A_i)^{-1} e^{j\omega t_{i+1}} \left(I - e^{-(j\omega I - A_i)(t_{i+1}-t_i)} \right) B_i \hat{u} \end{aligned} \quad (3-9)$$

The state $x(t)$ in each subinterval can be expressed as

$$x(t) = X(t) + \hat{x}(t) \quad (3-10)$$

In the above equation the steady state solution $X(t)$ is periodic within each switching period (T_s). The perturbation term $\hat{x}(t)$ is due to the perturbations in the input, \hat{u} and switching times, \hat{t}_i and \hat{t}_{i+1} . Figure 3 – 1 illustrates one possible way to show states and switching times.

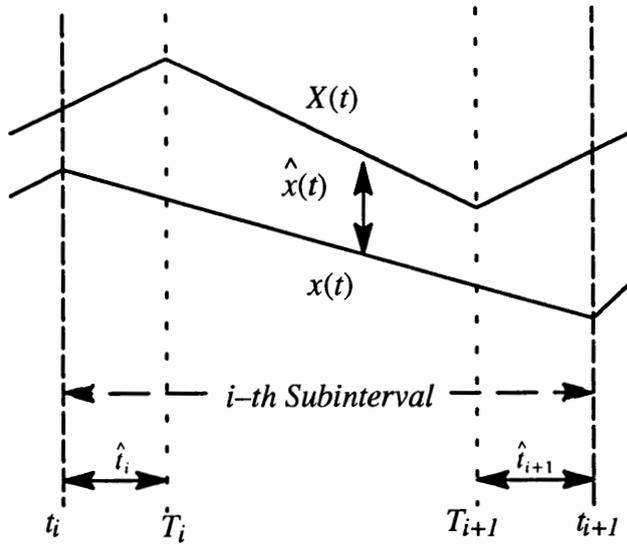


Figure 3 – 1 State and Switching times of the i -th subinterval.

Substituting (3 – 4) into (3 – 10) results in:

$$x(t_i) = X(T_i + \hat{t}_i) + \hat{x}(T_i + \hat{t}_i) \quad (3 - 11)$$

Taylor expansions of $x(t_i)$ at T_i is

$$x(t_i) = X(T_i) + \dot{X}(T_i)\hat{t}_i + \hat{x}(T_i) + \hat{\dot{x}}(T_i)\hat{t}_i \quad (3 - 12)$$

where $X(T_i)$ is the steady state term, $\dot{X}(T_i)\hat{t}_i + \hat{x}(T_i)$ are the first order perturbations, and $\hat{\dot{x}}(T_i)\hat{t}_i$ is the product of perturbations.

Using $A_i X(T_i) + B_i U$ in place of $\dot{X}(T_i)$ in equation (3 – 12) and ignoring the high order perturbation terms, results in:

$$x(t_i) = X(T_i) + [A_i X(T_i) + B_i U]\hat{t}_i + \hat{x}(T_i) \quad (3 - 13)$$

Substituting (3 – 13) in (3 – 9) yields

$$x(t_{i+1}) = e^{A_i(t_{i+1}-t_i)} \left[X(T_i) + [A_i X(T_i) + B_i U] \hat{t}_i + \hat{x}(T_i) \right] + A_i^{-1} \left(e^{A_i(t_{i+1}-t_i)} - I \right) B_i U \\ + (j\omega I - A_i)^{-1} e^{j\omega t_{i+1}} \left(I - e^{-(j\omega I - A_i)(t_{i+1}-t_i)} \right) B_i \hat{u} \quad (3 - 14)$$

where $e^{A_i(t_{i+1}-t_i)}$ can be expanded into the steady state and perturbation terms:

$$e^{A_i(t_{i+1}-t_i)} = e^{A_i(T_{i+1}-T_i)} e^{A_i(\hat{t}_{i+1}-\hat{t}_i)} \quad (3 - 15)$$

Linearizing the exponential perturbation component in expression (3 – 15), using a first order taylor series expansion, gives:

$$e^{A_i(t_{i+1}-t_i)} = e^{A_i(T_{i+1}-T_i)} \left[I + A_i(\hat{t}_{i+1} - \hat{t}_i) \right] \quad (3 - 16)$$

Using the result of (3 – 16) in equation (3 – 14) and rearranging it further gives

$$x(t_{i+1}) = e^{A_i(T_{i+1}-T_i)} \left[I + A_i(\hat{t}_{i+1} - \hat{t}_i) \right] \left[X(T_i) + [A_i X(T_i) + B_i U] \hat{t}_i + \hat{x}(T_i) \right] \\ + A_i^{-1} \left(e^{A_i(T_{i+1}-T_i)} \left[I + A_i(\hat{t}_{i+1} - \hat{t}_i) \right] - I \right) B_i U \\ + (j\omega I - A_i)^{-1} e^{j\omega T_{i+1}} (1 + j\omega \hat{t}_{i+1}). \\ \left(I - e^{-(j\omega I - A_i)(T_{i+1}-T_i)} \left[I - (j\omega I - A_i)(\hat{t}_{i+1} - \hat{t}_i) \right] \right) B_i \hat{u} \quad (3 - 17)$$

Changing the subscripts in equation (3 – 13) to $i+1$ gives

$$x(t_{i+1}) = X(T_{i+1}) + [A_{i+1} X(T_{i+1}) + B_{i+1} U] \hat{t}_{i+1} + \hat{x}(T_{i+1}) \quad (3 - 18)$$

and substituting equation (3 – 18) in the left hand side of equation (3 – 17) will give:

$$X(T_{i+1}) + [A_{i+1} X(T_{i+1}) + B_{i+1} U] \hat{t}_{i+1} + \hat{x}(T_{i+1}) = \\ e^{A_i(T_{i+1}-T_i)} \left[I + A_i(\hat{t}_{i+1} - \hat{t}_i) \right] \left[X(T_i) + [A_i X(T_i) + B_i U] \hat{t}_i + \hat{x}(T_i) \right] \\ + A_i^{-1} \left(e^{A_i(T_{i+1}-T_i)} \left[I + A_i(\hat{t}_{i+1} - \hat{t}_i) \right] - I \right) B_i U \\ + (j\omega I - A_i)^{-1} e^{j\omega T_{i+1}} (1 + j\omega \hat{t}_{i+1}). \\ \left(I - e^{-(j\omega I - A_i)(T_{i+1}-T_i)} \left[I - (j\omega I - A_i)(\hat{t}_{i+1} - \hat{t}_i) \right] \right) B_i \hat{u} \quad (3 - 19)$$

Only the zeroth and first order terms of this equation are of interest. Therefore, the steady state and the first order term of the state at the end of the i -th subinterval can be obtained by separating the appropriate terms and simplifying the results. From equation (3 - 19) the zeroth order term is:

$$x(T_{i+1}) = e^{A_i(T_{i+1}-T_i)}X(T_i) + A_i^{-1}(e^{A_i(T_{i+1}-T_i)} - I)B_iU \quad (3 - 20)$$

provided $\det A_i \neq 0$

and first order term is:

$$\begin{aligned} \hat{x}(T_{i+1}) &= e^{A_i(T_{i+1}-T_i)}\hat{x}(T_i) + e^{A_i(T_{i+1}-T_i)}[A_iX(T_i) + B_iU]\hat{t}_{i+1} \\ &- [A_{i+1}X(T_{i+1}) + B_{i+1}U]\hat{t}_{i+1} \\ &+ (j\omega I - A_i)^{-1}e^{j\omega T_{i+1}}(I - e^{-(j\omega I - A_i)(T_{i+1}-T_i)})B_i\hat{u} \end{aligned} \quad (3 - 21)$$

The second term on the right hand side of equation (3 - 21) can be written in terms of $X(T_{i+1})$. Premultiplying equation (3 - 20) by $e^{-A_i(T_{i+1}-T_i)}$ gives:

$$x(T_i) = e^{-A_i(T_{i+1}-T_i)}X(T_{i+1}) + e^{-A_i(T_{i+1}-T_i)}A_i^{-1}(e^{A_i(T_{i+1}-T_i)} - I)B_iU \quad (3 - 22)$$

where the commutativity property of a matrix and its exponential has been used.

Substituting equation (3 - 22) in (3 - 21) will give:

$$\begin{aligned} \hat{x}(T_{i+1}) &= e^{A_i(T_{i+1}-T_i)}\hat{x}(T_i) + [(A_i - A_{i+1})X(T_{i+1}) + (B_i - B_{i+1})U]\hat{t}_{i+1} \\ &+ (j\omega I - A_i)^{-1}e^{j\omega T_{i+1}}(I - e^{-(j\omega I - A_i)(T_{i+1}-T_i)})B_i\hat{u} \end{aligned} \quad (3 - 23)$$

Equation (3 - 23) can be written as:

$$\hat{x}(T_{i+1}) = \Phi_i \hat{x}(T_i) + \xi_i \hat{t}_{i+1} + \eta_i \hat{u}_k(T_{i+1}) \quad (3 - 24)$$

where

$$\Phi_i = e^{A_i(T_{i+1}-T_i)} \quad (3 - 25)$$

$$\xi_i = [(A_i - A_{i+1})X(T_{i+1}) + (B_i - B_{i+1})U] \quad (3 - 26)$$

$$\eta_i = (j\omega I - A_i)^{-1} \left(I - e^{-(j\omega I - A_i)(T_{i+1} - T_i)} \right) B_i \quad (3 - 27)$$

$$\hat{u}_k(T_{i+1}) = \hat{u} e^{j\omega T_{i+1}} \quad (3 - 28)$$

For small signal analysis, the controller signal is expressed as:

$$r(t_i) = R + \hat{r}_k(t_i) = R + \hat{r}_p e^{j\omega t} \quad (3 - 29)$$

The difference equation (3 - 24) has a time perturbation term $\xi_i \hat{t}_{i+1}$ which makes the system time varying. This term must be evaluated and replaced by appropriate linear combination of states and inputs. For example, for a current mode programming PWM converter operating in CCM, the control input equals a linear combination of states, $h_i^T x(\hat{t}_{i+1})$, plus a ramp with slope m_i .

$$\hat{r}(t_{i+1}) = h_i^T \hat{x}(t_{i+1}) + m_i \hat{t}_{i+1} \quad (3 - 30)$$

Next, equation (3 - 24) is written in terms of $x(\hat{t}_{i+1})$. Using only the perturbation terms of equation (3 - 18) gives

$$\hat{x}(t_{i+1}) = \Phi_i \hat{x}(T_i) + \gamma_i \hat{t}_{i+1} + \eta_i \hat{u}_k(T_{i+1}) \quad (3 - 31)$$

where $\gamma_i = A_i X(T_{i+1}) + B_i U$.

Equations (3 - 30) and (3 - 31) are then used together to give the desired state difference equations. The derivations for these equations are given in [3].

3.2 Application of Time Varying Transfer Function

Using the above analysis, it can be shown [3] that the exact small signal frequency response of any piecewise linear network can be obtained in the general form given as:

$$H_o(j\omega) = \frac{1}{NT_s} \sum_{k=0}^{N-1} \sum_{i=1}^{N_s} e^{-j\omega T_i} \left[\alpha_i \hat{x}(T_i) + \kappa_i \hat{t}_i + \lambda_i \hat{t}_{i+1} + \rho_i \hat{u} \right] \quad (3 - 32)$$

where

$$\alpha_i = C_i^T (j\omega I - A_i)^{-1} \left[I - e^{-(j\omega I - A_i)(T_{i+1} - T_i)} \right] \quad (3 - 33)$$

$$\kappa_i = - [C_i^T X(T_i) + E_i U] \quad (3 - 34)$$

$$\lambda_i = [C_i^T X(T_i) + E_i U] e^{-j\omega(T_{i+1} - T_i)} \quad (3 - 35)$$

$$\rho_i = e^{j\omega T_i} \left\{ C_i^T (j\omega I - A_i)^{-1} [(T_{i+1} - T_i) B_i - \eta_i] + E_i (T_{i+1} - T_i) \right\} \quad (3 - 36)$$

where N is the number of cycles of the switching waveform and N_s is the number of topologies per switching period.

Expression (3 – 32) can be used to analyze any piecewise linear network. In the following, the exact small–signal transfer functions for PWM converters in CCM under duty ratio, and current mode programming are shown.

3.2.1 Duty Ratio Programmed PWM Converter in CCM

For duty ratio programming, the control voltage v_c is compared with a positive sawtooth ramp v_r . The duty ratio of the k th cycle is defined as:

$$d_k = D + \hat{d}_k \quad (3 - 37)$$

The control signal is given in the following form

$$v_c = V_c + \hat{v}_c \quad (3 - 38)$$

$$\hat{v}_c = v_{cp} \cos [(k + \delta)\omega T_s] \quad (3 - 39)$$

where

$$\delta = d_k; \quad \text{for natural sampling}$$

$$\delta = 0; \quad \text{for uniform sampling}$$

The sawtooth ramp has a peak–to–peak value V_r and minimum value V_o . The intersection of the ramp with the control signal determines the width of the duty ratio. Duty ratio directly turns the active switch ON. The duty ratio of the k th cycle is determined as:

$$\frac{V_r}{T_s} (d_k T_s) + V_o = v_c \quad (3 - 40)$$

This arrangement is shown in Figure 3 – 2.

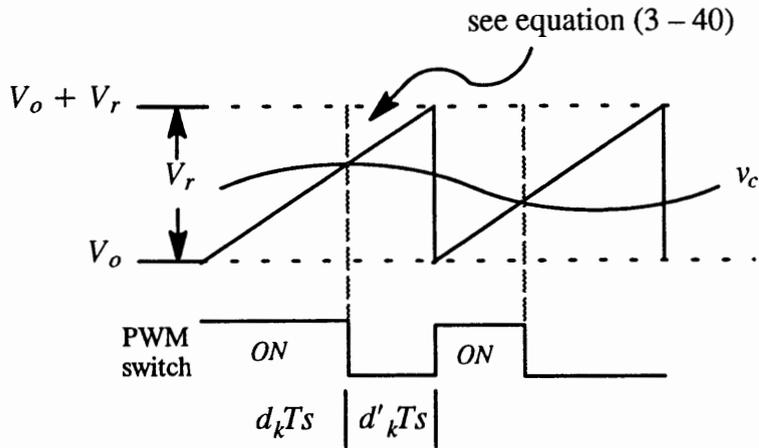


Figure 3 – 2 Waveforms showing typical signals associated with duty ratio determination.

Equation (3 – 40) can be divided into dc and first order perturbation. The dc term of the above relationship is found as:

$$D = \frac{V_c - V_o}{V_r} \quad (3 - 41)$$

The the small signal duty ratio is:

$$\hat{d}_k = \frac{\hat{v}_c}{V_r} \quad (3 - 42)$$

Next, the exact small signal frequency response transfer functions are given. In the following chapter, these equations are used to obtain exact expressions for the PWM duty ratio programmed hybrid parameter switch model.

$$\frac{\hat{y}}{\hat{x}}(j\omega) = [a_1 \beta_2 + a_2] (I - \beta_1 \beta_2)^{-1} \xi + \zeta \quad (3 - 43)$$

$$\begin{aligned}
\frac{\hat{y}}{\hat{u}}(j\omega) = & \frac{1}{T_s} \left\{ [\alpha_1 \beta_2 + \alpha_2] (I - \beta_1 \beta_2)^{-1} (\beta_2 \eta_1 + \eta_2) \right. \\
& + \alpha_1 \eta_2 + E_1 (T_2 - T_1) + E_2 (T_3 - T_2) \\
& + C_1 (j\omega I - A_1)^{-1} [(T_2 - T_1) B_1 - \eta_1] \\
& \left. + C_2 (j\omega I - A_2)^{-1} [(T_3 - T_2) B_2 - \eta_2] \right\} \quad (3 - 44)
\end{aligned}$$

where

$$\alpha_i = C_i (j\omega I - A_i)^{-1} [I - e^{-(j\omega I - A_i)(T_{i+1} - T_i)}], \quad i = 1, 2 \quad (3 - 45)$$

$$\beta_i = e^{-(j\omega I - A_i)(T_{i+1} - T_i)}, \quad i = 1, 2 \quad (3 - 46)$$

$$\eta_i = (j\omega I - A_i)^{-1} [I - e^{-(j\omega I - A_i)(T_{i+1} - T_i)}] B_i, \quad i = 1, 2 \quad (3 - 47)$$

$$\xi = (A_1 - A_2) X + (B_1 - B_2) U \quad (3 - 48)$$

$$\zeta = (C_1 - C_2) X + (E_1 - E_2) U \quad (3 - 49)$$

$$\begin{aligned}
X = & [I - e^{A_1 D T_s} e^{A_2 D' T_s}]^{-1} \{ e^{A_1 D T_s} [e^{A_2 D' T_s} - I] \\
& \cdot A_2^{-1} B_2 U + [e^{A_1 D T_s} - I] A_1^{-1} B_1 U \} \quad (3 - 50)
\end{aligned}$$

3.2.2 Current Programmed Converter in CCM

In current programming, the switch is turned ON when the voltage ramp $R_i i_i$ reaches the control signal v_c . The above voltage ramp is proportional to the instantaneous sensed inductor current ramp. The current to voltage transformation gain R_i is used to show this proportionality. Therefore, switch turn ON is dependent on the control signal and the sensed current. This is shown in Figure 3 – 3:

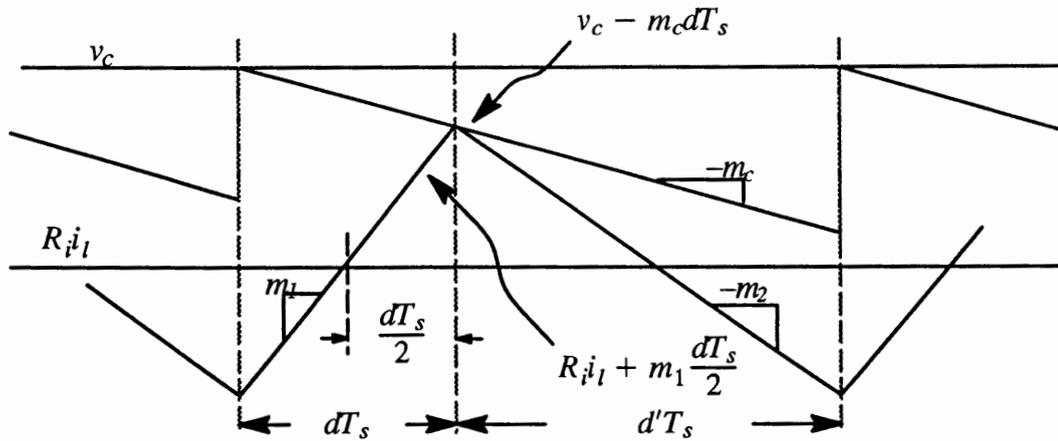


Figure 3 – 3 Waveforms showing the intersection of voltage ramps that cause switching to take place.

The exact small–signal describing functions given in [3] can be used to obtain the control–to–output and the input–to–output transfer functions for converters operating in continuous conduction mode under current mode programming.

The control–to–output transfer function given in terms of control voltage is:

$$\frac{\hat{y}}{\hat{v}_c}(j\omega) = \frac{\mu}{T_s} \left\{ \left[\alpha_1 \beta_2 + \alpha_2 - \zeta h^T \mu \beta_1 \beta_2 \right] (I - \Gamma \beta_1 \beta_2)^{-1} \xi + \zeta \right\} \quad (3 - 51)$$

where $\frac{\mu}{T_s}$ is the modulator gain.

The input–to–output transfer function is given in the following form:

$$\begin{aligned} \frac{\hat{y}}{\hat{u}}(j\omega) = & \frac{1}{T_s} \left\{ \left(\left[\alpha_1 \beta_2 + \alpha_2 - \zeta h^T \mu \beta_1 \beta_2 \right] (I - \Gamma \beta_1 \beta_2)^{-1} \Gamma - \zeta h^T \mu \right) \right. \\ & \cdot (\beta_2 \eta_1 + \eta_2) + \alpha_1 \eta_2 + E_1(T_2 - T_1) + E_2(T_3 - T_2) \\ & + C_1 (j\omega I - A_1)^{-1} [(T_2 - T_1)B_1 - \eta_1] \\ & \left. + C_2 (j\omega I - A_2)^{-1} [(T_3 - T_2)B_2 - \eta_2] \right\} \quad (3 - 52) \end{aligned}$$

where

$$\mu = \frac{1}{h^T[A_1X + B_1U] + m_c} \quad (3 - 53)$$

$$\Gamma = I - \xi h^T \mu \quad (3 - 54)$$

and $\{\alpha_i, \beta_i, \eta_i, \xi, \zeta, X\}$ are the same as those given for PWM CCM analysis.

CHAPTER 4

FIRST MODEL

4.1 PWM Duty Ratio Programmed Model

In chapter 3, the general form of the hybrid parameter switch model was introduced. Next, the state–space averaged control–to–output and the input–to–output transfer functions were used to obtain expressions for the parameters of the model. The subsequent model was found to agree with the circuit averaging model. In the sequel, the exact expressions for the parameters will be sought for using the exact small signal transfer functions given in equations (3 – 43) and (3 – 44).

In the first step, the small–signal state equation of the boost model of Figure 2 – 7 will be obtained. The two independent sources of this model are actually the states of the system. Therefore,

$$\hat{i}_c = \hat{i} \quad (4 - 1)$$

and

$$\hat{v}_{pa} = \hat{v} \quad (4 - 2)$$

Using Kirchoff's voltage law (KVL) one gets:

$$-\hat{v}_g + \frac{d\hat{i}}{dt}L + v_{id}\hat{d} + h_r\hat{v}_{pa} + z_i\hat{i}_c = 0 \quad (4 - 3)$$

Substituting equations (4 – 1) and (4 – 2) in (4 – 3) and rearranging gives:

$$\frac{d\hat{i}}{dt} = -\frac{v_{id}}{L_f}\hat{d} - \frac{z_i}{L_f}\hat{i} + \frac{1 - h_r}{L_f}\hat{v}_g \quad (4 - 4)$$

Applying Kirchoff's current law (KCL) to the p terminal of Figure 2 – 7 will give:

$$\frac{d\hat{v}}{dt}C_f + \frac{\hat{v}_{pa}}{R} - \hat{i}_p = 0 \quad (4 - 5)$$

Using KCL again gives:

$$\hat{i}_p = i_{od}\hat{d} + h_f\hat{i}_c + y_o\hat{v}_{pa} \quad (4 - 6)$$

Substituting equations (4 – 1), (4 – 2) in equation (4 – 6) and substituting the results in equation (4 – 5) will give:

$$\frac{d\hat{v}}{dt} = \frac{i_{od}}{C_f}\hat{d} + \frac{h_f}{C_f}\hat{i} + \left(\frac{y_o - \frac{1}{R}}{C_f}\right)\hat{v} \quad (4 - 7)$$

Equations (4 – 4) and (4 – 7) together form the small–signal state equation of the system:

$$\frac{d}{dt} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} = \begin{bmatrix} -\frac{z_i}{L_f} & -\frac{h_r}{L_f} \\ \frac{h_f}{C_f} & \frac{y_o}{C_f} - \frac{1}{RC_f} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} -\frac{v_{id}}{L_f} \\ \frac{i_{od}}{C_f} \end{bmatrix} \hat{d} + \begin{bmatrix} \frac{1}{L_f} \\ 0 \end{bmatrix} \hat{v}_g \quad (4 - 8)$$

In order to obtain the frequency response of equation (4 – 8), one may simply use state space averaging transfer functions given in equations (2 – 49) and (2 – 50).

To verify this modeling approach, the parameters are numerically evaluated at each discrete frequency point using equations (3 – 43) and (3 – 44). Then the overall response is formulated using equations (2 – 49) and (2 – 50). The plots obtained from the switch model are then compared against the exact solutions of equations (3 – 43) and (3 – 44). These plots are shown in Figure 4 – 1 and Figure 4 – 2. For the given specifications the ripple percentages are calculated to be:

$$\frac{\Delta v/2}{V} \times 100 \approx 4.85\%$$

$$\frac{\Delta i/2}{I} \times 100 \approx 37.2\%$$

where Δv and Δi are the peak-to-peak capacitor voltage ripple the inductor current ripple, respectively. The quantities V and I are the steady state operating values for the capacitor voltage and inductor current. As it is apparent from Figure 4 – 1 this model does not reproduce the exact plot for the control-to-output frequency response.

From equations (2 – 41) and (2 – 42) it is evident that system states, \hat{i}_c and \hat{v}_{pa} are also the independent sources of the system. Therefore, nulling them would eliminate the dynamics of the system and reduces the exact controller parameters v_{id} and i_{od} to the state space averaging expressions. To clarify this point, the v_{id} parameter will be found next.

Using control-to-output transfer function given in equation (3 – 43) and also the constraints given in equation (2 – 43) v_{id} is found as follows:

$$v_{id} = \frac{\hat{v}_{ca}}{\hat{d}} = [\alpha_1 \beta_2 + \alpha_2] (I - \beta_1 \beta_2)^{-1} \xi + \xi \quad \left| \begin{array}{l} \hat{v}_{pa} = 0 \\ \hat{i}_c = 0 \end{array} \right.$$

where

$$\alpha_1 = C_1 (j\omega I - A_1)^{-1} [I - e^{-(j\omega I - A_1) (T_2 - T_1)}] \quad (4 - 9)$$

$$\alpha_2 = C_2 (j\omega I - A_2)^{-1} [I - e^{-(j\omega I - A_2) (T_3 - T_2)}] \quad (4 - 10)$$

$$\beta_1 = e^{-(j\omega I - A_1) (T_2 - T_1)} \quad (4 - 11)$$

$$\beta_2 = e^{-(j\omega I - A_2) (T_3 - T_2)} \quad (4 - 12)$$

Using (2 – 53), α_1 and α_2 for v_{id} parameter will be:

$$\alpha_1 = [0 \ 0] (j\omega I - A_1)^{-1} [I - e^{-(j\omega I - A_1) (T_2 - T_1)}]$$

$$\alpha_2 = [0 \ 1] (j\omega I - A_2)^{-1} [I - e^{-(j\omega I - A_2) (T_3 - T_2)}] \quad (4 - 13)$$

Nulling both states will force C_1 and C_2 vectors to zero. This is shown below:

$$\begin{aligned} C_1 &= [0 \ 0] \\ C_2 &= [0 \ 0] \end{aligned} \tag{4 - 14}$$

Using (4 - 14) in (4 - 13) results in:

$$\begin{aligned} \alpha_1 &= [0 \ 0] (j\omega I - A_1)^{-1} \left[I - e^{-(j\omega I - A_1) (T_2 - T_1)} \right] = 0 \\ \alpha_2 &= [0 \ 0] (j\omega I - A_2)^{-1} \left[I - e^{-(j\omega I - A_2) (T_3 - T_2)} \right] = 0 \end{aligned} \tag{4 - 15}$$

Therefore, when both states are nulled, equation (3 - 43) reduces to:

$$\frac{\hat{y}}{\hat{d}}(j\omega) = \zeta \tag{4 - 16}$$

Since ζ is equivalent to E_d of the state space averaging method, it can be seen that equation (4 - 16) is identical to equation (2 - 59). As a result of this reduction, all of the components contributing to the exactness of the solution will be lost. Therefore, this technique fails to produce exact expressions for the control-to-output parameters of the PWM duty ratio controlled boost converter operating in CCM. As shown in Figure 4 - 2, the input-to-output response of the model matches that of the exact results.

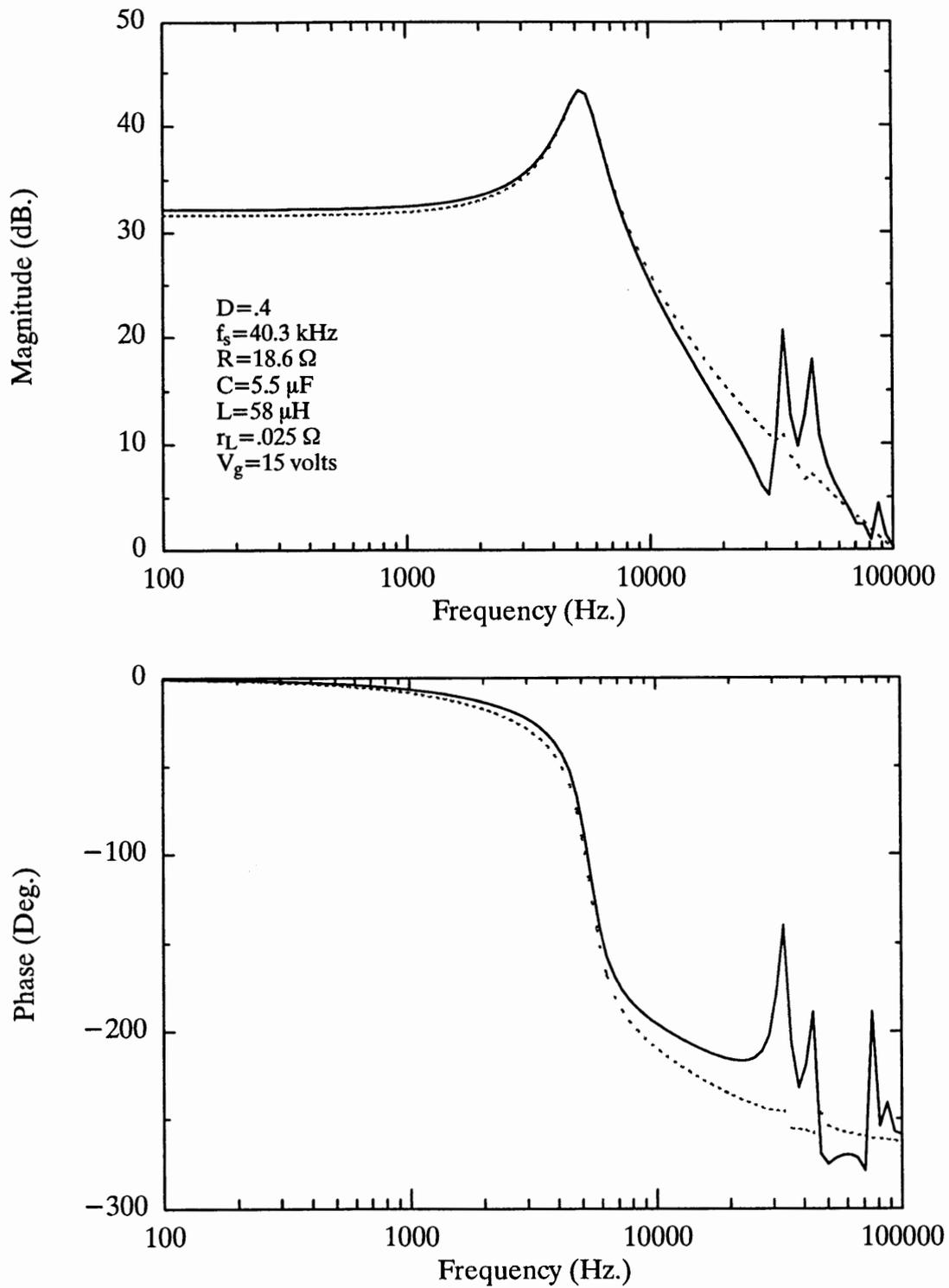


Figure 4 - 1 The control-to-output response of the PWM boost converter operating in CCM; exact (solid line) versus the first model (dashed line).

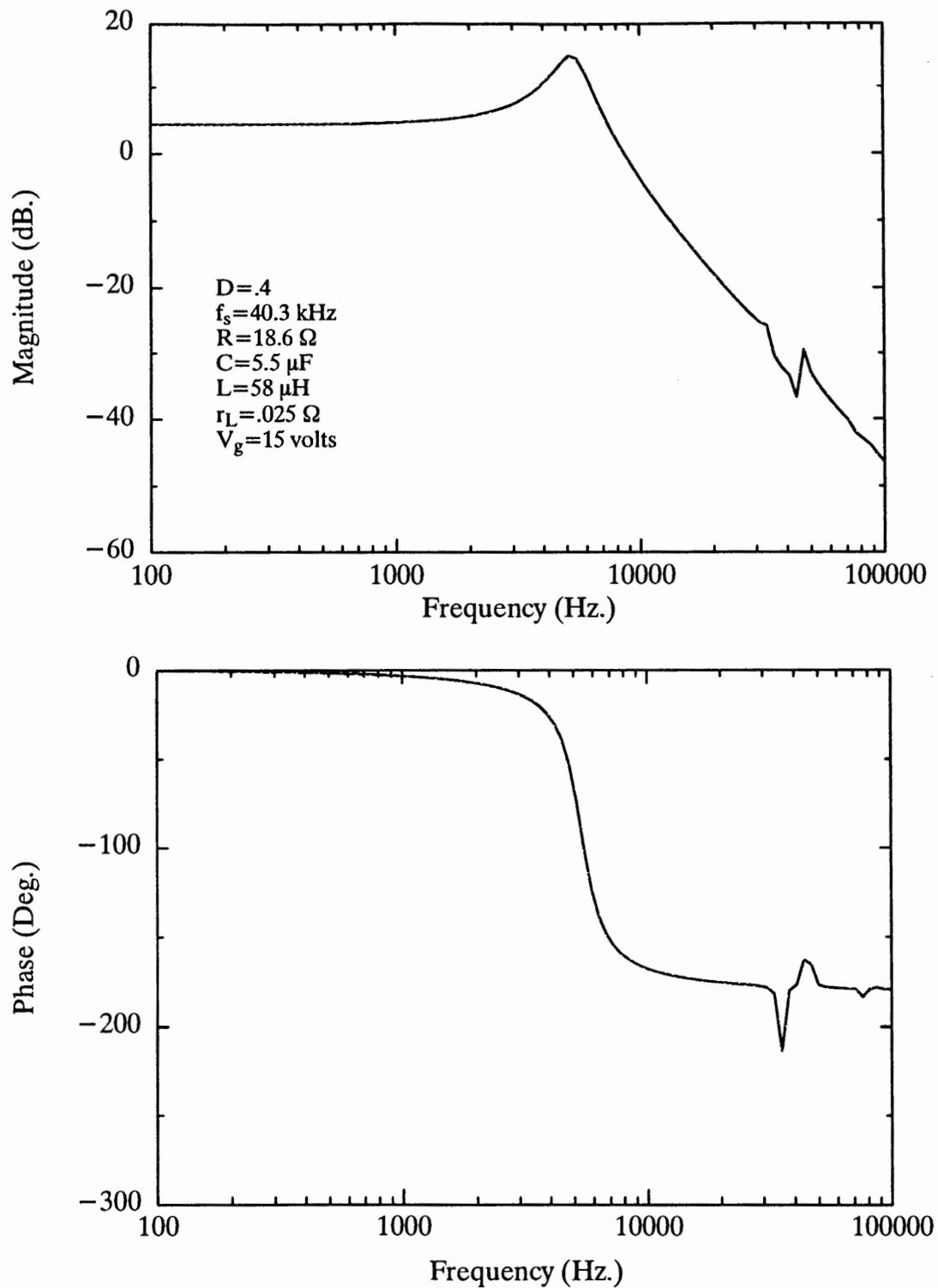


Figure 4 – 2 The input-to-output response of the PWM boost converter operating in CCM; exact (solid line) versus the first model (dashed line).

4.2 PWM Current Programmed Model

It is apparent from the control-to-output transfer function of the current-programmed converters in equation (3 – 51) that this method does not work for the current mode programming either. Nulling both states would reduce equation (3 – 52) to:

$$\frac{\hat{y}}{\hat{v}_c}(j\omega) = \frac{\mu}{T_s} \zeta \quad (4 - 17)$$

where μ is given by equation (3 – 53). In terms of duty ratio-to-output transfer function, equation (4 – 17) reduces to:

$$\frac{\hat{y}}{\hat{d}}(j\omega) = \zeta \quad (4 - 18)$$

which is equivalent to the state space averaging solution given by equation (2 – 59).

Generally, the current mode transfer functions given in equations (3 – 51) and (3 – 52) are more complex and involve more terms due to the current sensing phenomena. The state space averaging results fail to predict the behavior of the converters under current mode control. The easiest way to verify the results would be through comparing Matlab frequency response plots of the exact small-signal results versus those of the proposed model. These plots are shown in Figure 4 – 3 and Figure 4 – 4. The following ripple percentages are used in the above simulation:

$$\frac{\Delta v/2}{V} \times 100 \approx 3\%$$

$$\frac{\Delta i/2}{I} \times 100 \approx 35\%$$

In Figure 4 – 3, the control-to-output response of the model is plotted against the exact results. As shown in the above figure, the magnitude response of the proposed model has an overall level shift of around +20 dB up to half the switching frequency. The phase plot of the model shows around 180° deg. phase lag at low frequencies.

Obviously, this modeling technique does not predict the exact results. In the next chapter, a new method is proposed which is believed to overcome the inaccuracies associated with this model.

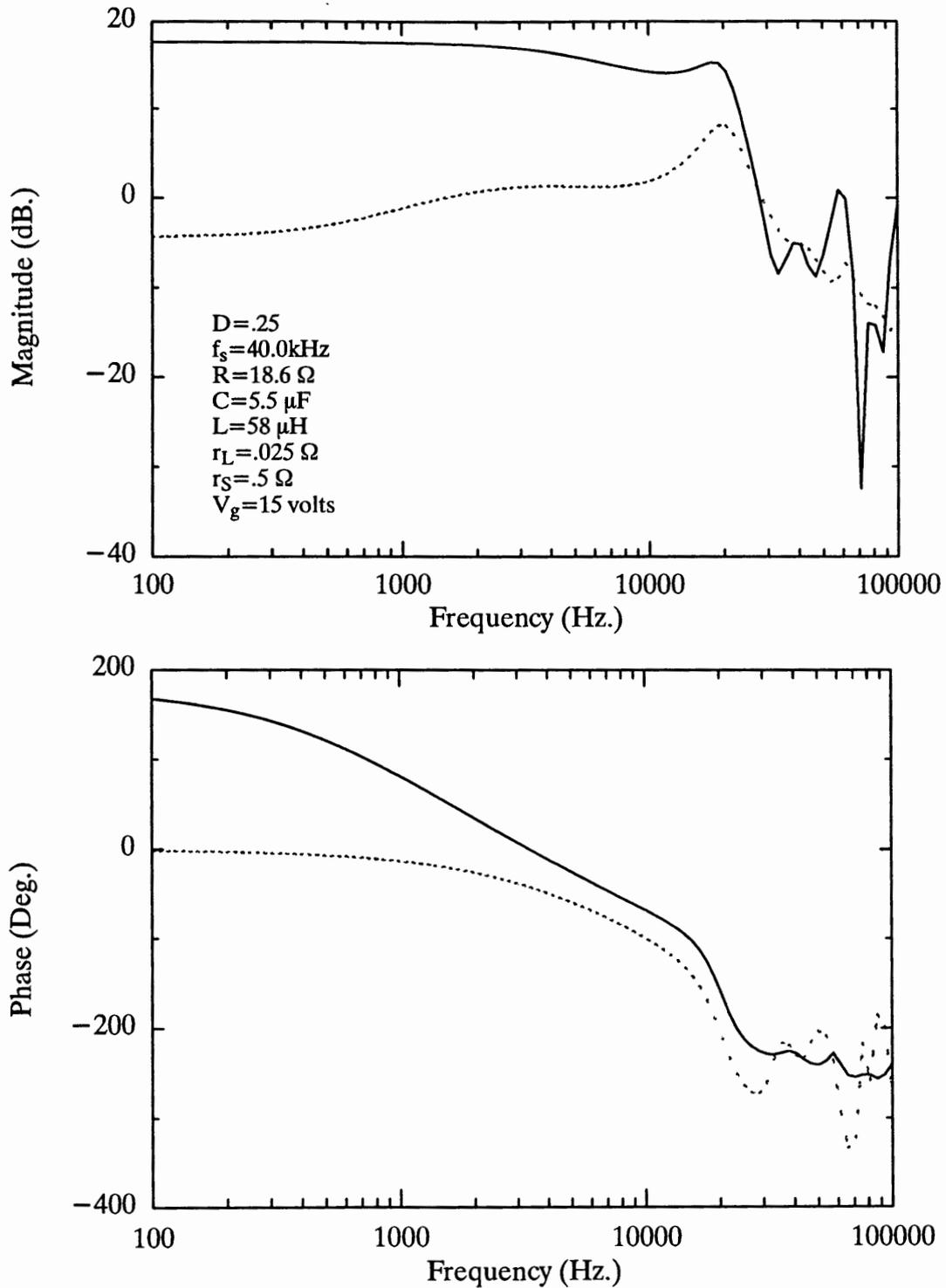


Figure 4 – 3 The control–to–output response of the PWM boost converter operating in CCM under current mode programming; exact (solid line) versus the first model (dashed line).

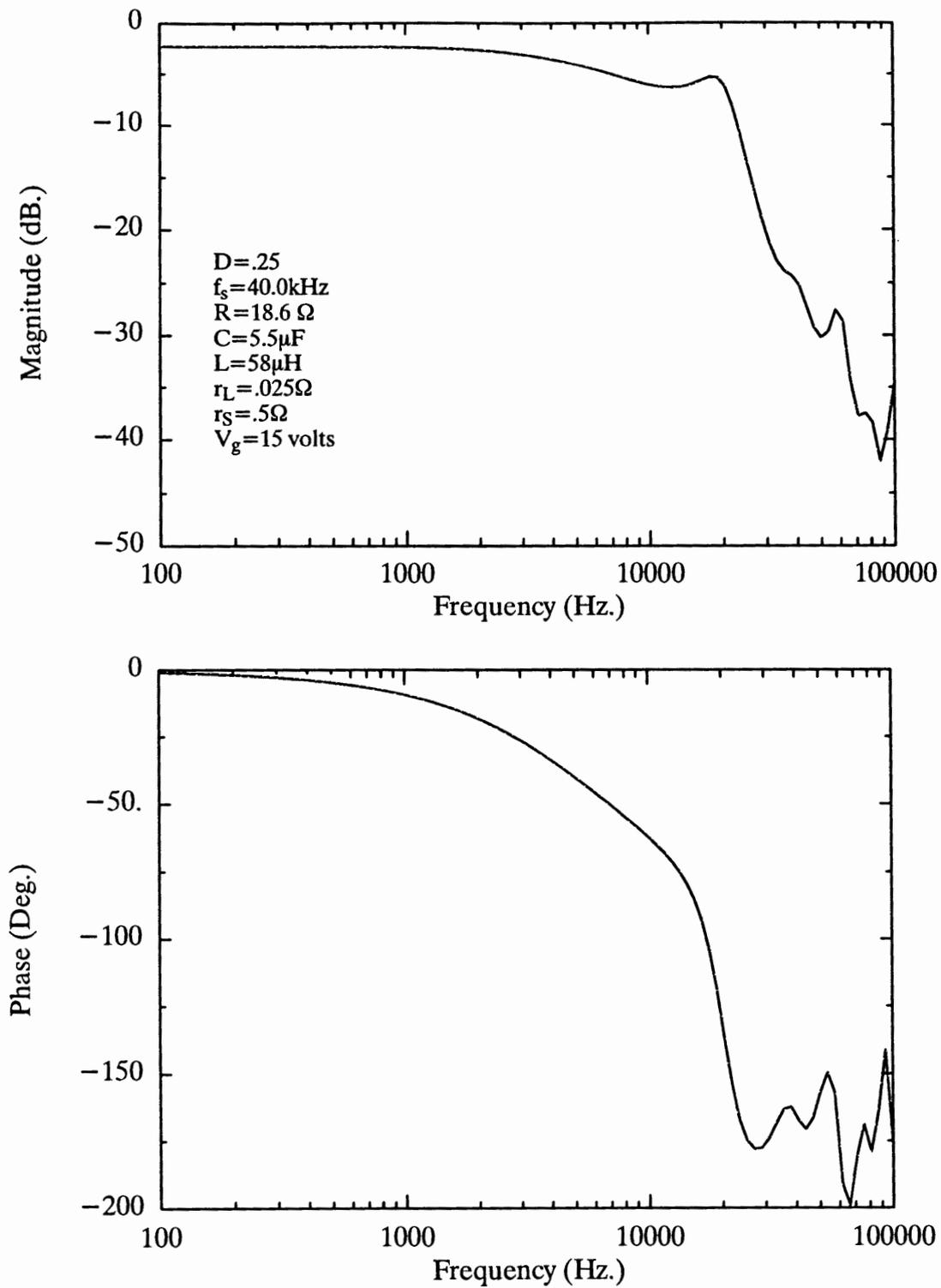


Figure 4 - 4 The input-to-output response of the PWM boost converter operating in CCM under current mode programming; exact (solid line) versus the first model (dashed line).

CHAPTER 5

SECOND MODEL: NEW METHOD FOR OBTAINING CONTROLLER PARAMETERS

5.1 General Analysis of the New Model

Since the original modeling approach given in the last chapter is not satisfactory, alternative approaches need be investigated to avoid the problem of nulling both states of the control-to-output transfer function.

In section 2.1.4 the boost derived hybrid parameter switch model was introduced. Equations (2 – 41) and (2 – 42) which are given below were derived from this model.

$$\hat{v}_{ca} = v_{id}\hat{d} + h_r\hat{v}_{pa} + z_i\hat{i}_c$$

$$\hat{i}_p = i_{od}\hat{d} + h_f\hat{i}_c + y_o\hat{v}_{pa}$$

In the above set of equations, there exist six unknown parameters. Four of these parameters, h_r , z_i , h_f , and y_o , are found using the input-to-output transfer function. The other two parameters, v_{id} and i_{od} , are found by employing the control-to-output transfer function.

In equation (2 – 41) there are two known parameters, h_r and z_i , and one unknown parameter, v_{id} . Therefore, one can solve for the unknown. In this case, the parameter

v_{id} can be evaluated by dividing the variables in equation (2 – 41) by \hat{d} :

$$\frac{\hat{v}_{ca}}{\hat{d}} = v_{id} + h_r\frac{\hat{v}_{pa}}{\hat{d}} + z_i\frac{\hat{i}_c}{\hat{d}}$$

Rearrangement will give:

$$v_{id} = \frac{\hat{v}_{ca}}{\hat{d}} - h_r \frac{\hat{v}_{pa}}{\hat{d}} - z_i \frac{\hat{i}_c}{\hat{d}} \quad (5 - 1)$$

and i_{od} is similarly found by applying the same technique to equation (2 - 42).

$$i_{od} = \frac{\hat{i}_p}{\hat{d}} - y_o \frac{\hat{v}_{pa}}{\hat{d}} - h_f \frac{\hat{i}_c}{\hat{d}} \quad (5 - 2)$$

Equations (5 - 1) and (5 - 2) can be given in the following form:

$$v_{id} = v_{id}' - h_r v_{od} - z_i i_{id} \quad (5 - 3)$$

$$i_{od} = i_{od}' - y_o v_{od} - h_f i_{id} \quad (5 - 4)$$

where

$$v_{id}' = \frac{\hat{v}_{ca}}{\hat{d}} \quad (5 - 5)$$

$$i_{od}' = \frac{\hat{i}_p}{\hat{d}} \quad (5 - 6)$$

$$v_{od} = \frac{\hat{v}_{pa}}{\hat{d}} \quad (5 - 7)$$

$$i_{id} = \frac{\hat{i}_c}{\hat{d}} \quad (5 - 8)$$

Equations (5 - 3) and (5 - 4) are expanded below to show the existing constraints on each parameter:

$$v_{id} = \frac{\hat{v}_{ca}}{\hat{d}} - \frac{\hat{v}_{ca}}{\hat{v}_{pa}} \left. \frac{\hat{v}_{ca}}{\hat{d}} \right|_{\hat{i}_c = 0} = 0 \cdot \frac{\hat{v}_{pa}}{\hat{d}} - \frac{\hat{v}_{ca}}{\hat{i}_c} \left. \frac{\hat{v}_{ca}}{\hat{d}} \right|_{\hat{v}_{pa} = 0} = 0 \cdot \frac{\hat{i}_c}{\hat{d}} \quad (5 - 9)$$

$$i_{od} = \frac{\hat{i}_p}{\hat{d}} - \frac{\hat{i}_p}{\hat{v}_{pa}} \left| \begin{array}{c} \hat{d} = 0 \\ \hat{i}_c = 0 \end{array} \right. = 0 \cdot \frac{\hat{v}_{pa}}{\hat{d}} - \frac{\hat{i}_p}{\hat{i}_c} \left| \begin{array}{c} \hat{d} = 0 \\ \hat{v}_{pa} = 0 \end{array} \right. \cdot \frac{\hat{i}_c}{\hat{d}} \quad (5 - 10)$$

Therefore, this method will be valid only when the nulling according to equations (5 - 9) and (5 - 10) is performed.

5.1.1 PWM Duty Ratio Programmed Model

In this section, the equivalent boost model given in Figure 2 - 7 is simulated using the second hybrid parameter switch model. The state equation for this model is the same as equation (4 - 8). Then, the control-to-output and input-to-output frequency responses of the second modeling approach are compared against the exact results.

As shown in Figure 5 - 1 the small-signal control-to-output frequency response of the PWM boost hybrid parameter switch model matches that of the exact small-signal control-to-output transfer function given in equation (3 - 43). The input-to-output response of the above model is shown in Figure 5 - 2. Also, the exact small-signal input-to-output transfer function (3 - 44) is plotted on the same graph. The agreement is excellent.

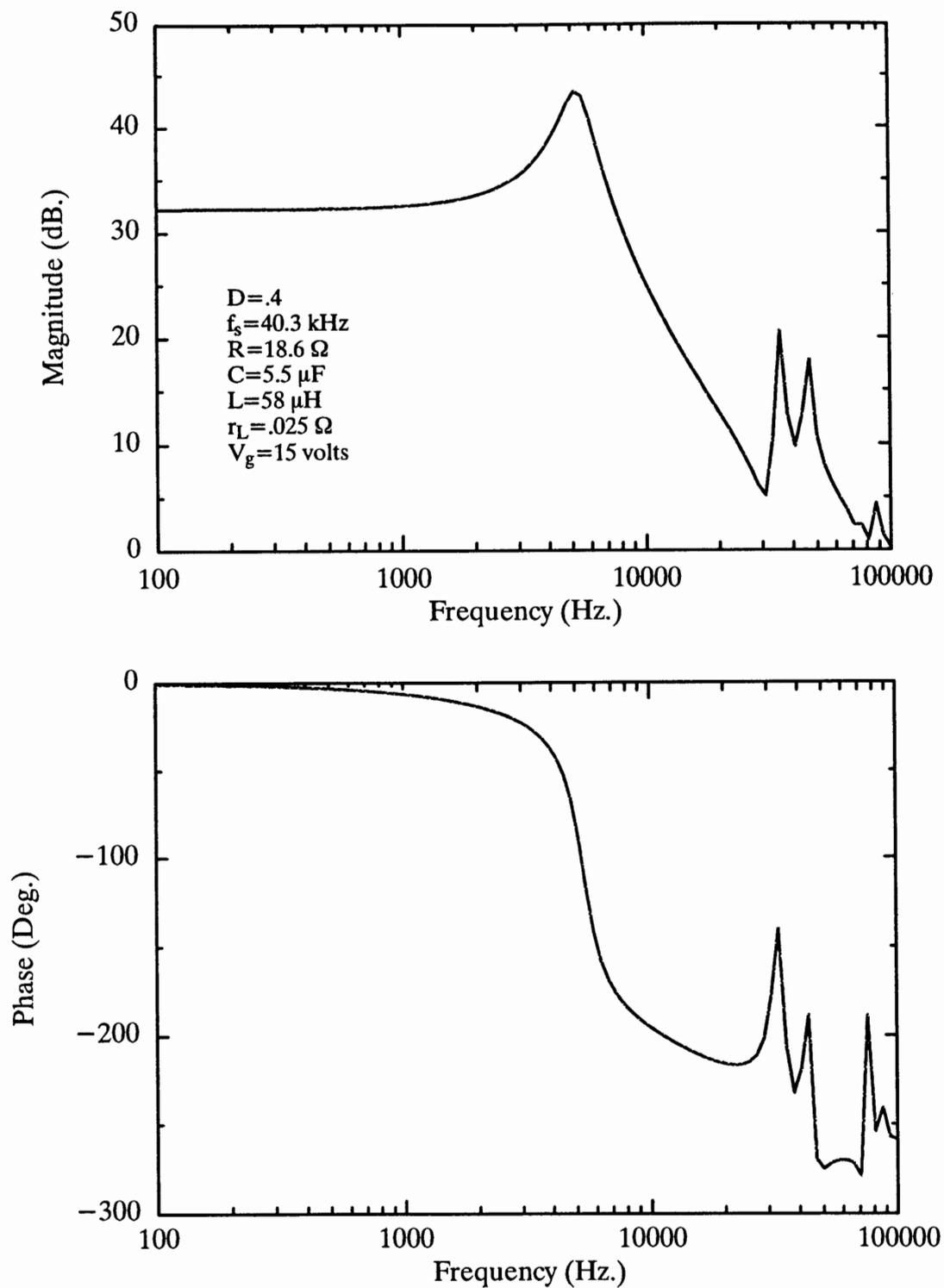


Figure 5 - 1 The control-to-output response of the PWM boost converter operating in CCM; exact (solid line) versus the second model (dashed line).

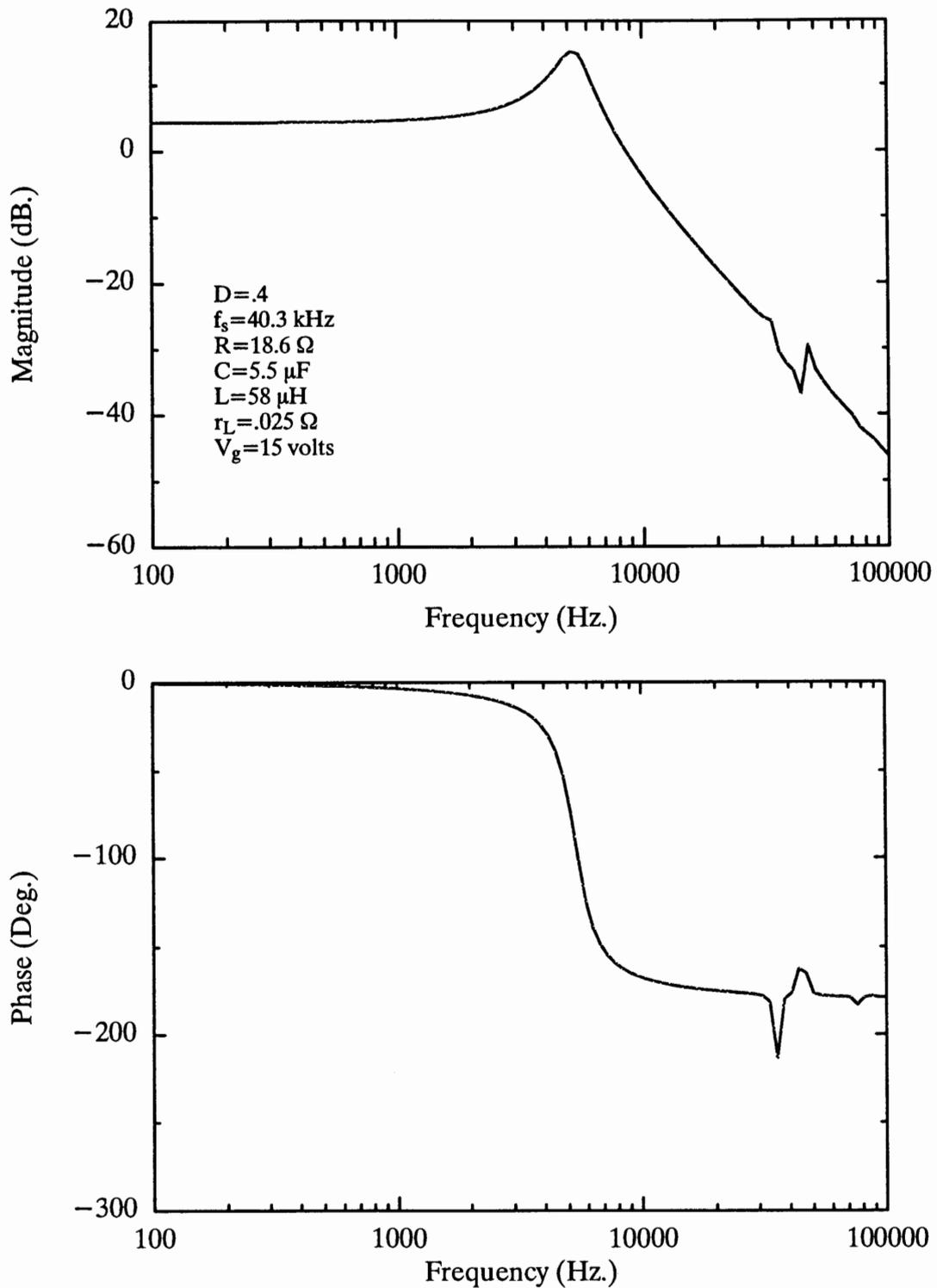


Figure 5 – 2 The input-to-output response of the PWM boost converter operating in CCM; exact (solid line) versus the second model (dashed line).

The next step is to obtain the expressions for the switch parameters. The goal is to find simple expressions. The solutions of equations (3 – 45), (3 – 46), and (3 – 47) all will be in exponential matrix form. Therefore, using the exact small-signal transfer functions of (3 – 43) and (3 – 44) will result in complex, lengthy expressions that are considered to be in an undesired form. As a result, we make the following assumption:

In a well designed converter, the output capacitor voltage ripple will be small; therefore, the output capacitor can be modeled by a constant voltage source as shown in Figure 5 – 3.

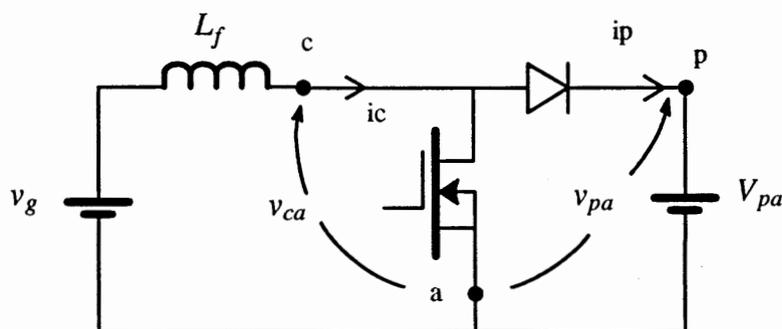


Figure 5 – 3 Boost converter: capacitor is replaced by a constant voltage source V_{pa} .

This approximation will reduce the number of states to one, i.e. the inductor current state, and consequently equations (3 – 43) and (3 – 44) will become scalar. In order to obtain the parameter expressions, the reduced order converter of Figure 5 – 3 must be analyzed.

Figure 5 – 4 shows the reduced order boost converter in DT_s and $D'T_s$ subintervals.

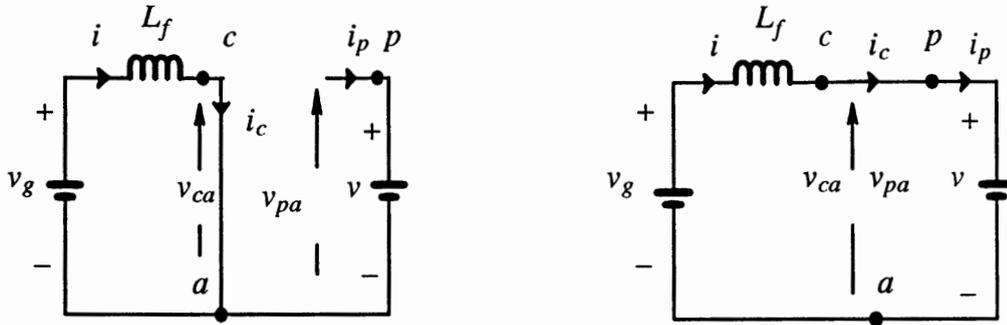


Figure 5 – 4 Reduced order boost converter of Figure 5 – 3 in;
a) DT_s subinterval b) $D'T_s$ subinterval.

The state vector is now reduced to a single state:

$$x = i$$

and the capacitor voltage is considered to be the second input, hence

$$u = \begin{bmatrix} v_g \\ v \end{bmatrix}$$

i , v_g , and v contain both the steady–state values and the perturbation quantities.

$$i = I + \hat{i} \quad (5 - 11)$$

$$v_g = V_g + \hat{v}_g \quad (5 - 12)$$

$$v = V + \hat{v} \quad (5 - 13)$$

The steady state solution of Figure 5 – 4 can be easily calculated. State equation of the system for DT_s subinterval is:

$$-V_g + \frac{di}{dt}L_f = 0 \quad (5 - 14)$$

and for $D'T_s$ subinterval is:

$$-V_g + \frac{di}{dt}L_f + V = 0 \quad (5 - 15)$$

From equations (5 – 14) and (5 – 15), the reduced boost converter is described by:

$$A_1 = 0 \qquad A_2 = 0$$

$$B_1 = \begin{bmatrix} \frac{1}{L_f} & 0 \end{bmatrix} \qquad B_2 = \begin{bmatrix} \frac{1}{L_f} & -\frac{1}{L_f} \end{bmatrix}$$

The output and the transmission matrices for the reduced model are given below:

When $y = \hat{v}_{ca}$

$$C_1 = 0 \qquad E_1 = [0 \ 0]$$

$$C_2 = 0 \qquad E_2 = [0 \ 1] \qquad (5 - 16)$$

When $y = \hat{i}_p$

$$C_1 = 0 \qquad E_1 = [0 \ 0]$$

$$C_2 = 1 \qquad E_2 = [0 \ 0] \qquad (5 - 17)$$

When $y = \hat{i}_c$

$$C_1 = 1 \qquad E_1 = [0 \ 0]$$

$$C_2 = 1 \qquad E_2 = [0 \ 0] \qquad (5 - 18)$$

The reduction in the number of states will effect the overall frequency characteristics of the model.

To obtain the numeric solutions for the parameters of the reduced order model, two converter systems are set up simultaneously. The first system is the reduced order system. The state and output matrices for this system are passed on to appropriate functions to obtain parameter values. The second system is the full order system used in the exact small–signal analysis method. The DC operating points of this system are passed to the functions along with the reduced order system’s state and output matrices. The returned value of the function calls are the discrete values for the parameters.

After performing a frequency sweep using the above algorithm, the state and output matrices of the model are obtained. Next, the above findings are compared against the exact results by plotting their control-to-output and input-to-output responses.

The control-to-output responses of the reduced order model are plotted in Figure 5 – 5. The magnitude plot shows that the DC gain of the model is by about 0.5 dB. lower than the exact results. Otherwise, the control-to-output magnitude and phase responses are excellent up to half the switching frequency, $f_s/2$.

The input-to-output magnitude and phase responses of the reduced model are shown in Figure 5 – 6. These input-to-output results are accurate everywhere except at f_s .

NOTE: The high inductor current ripple contributes to the inaccuracies occurring at switching frequency and its higher harmonics. It can be shown that reducing the inductor ripple improves the accuracy of the model.

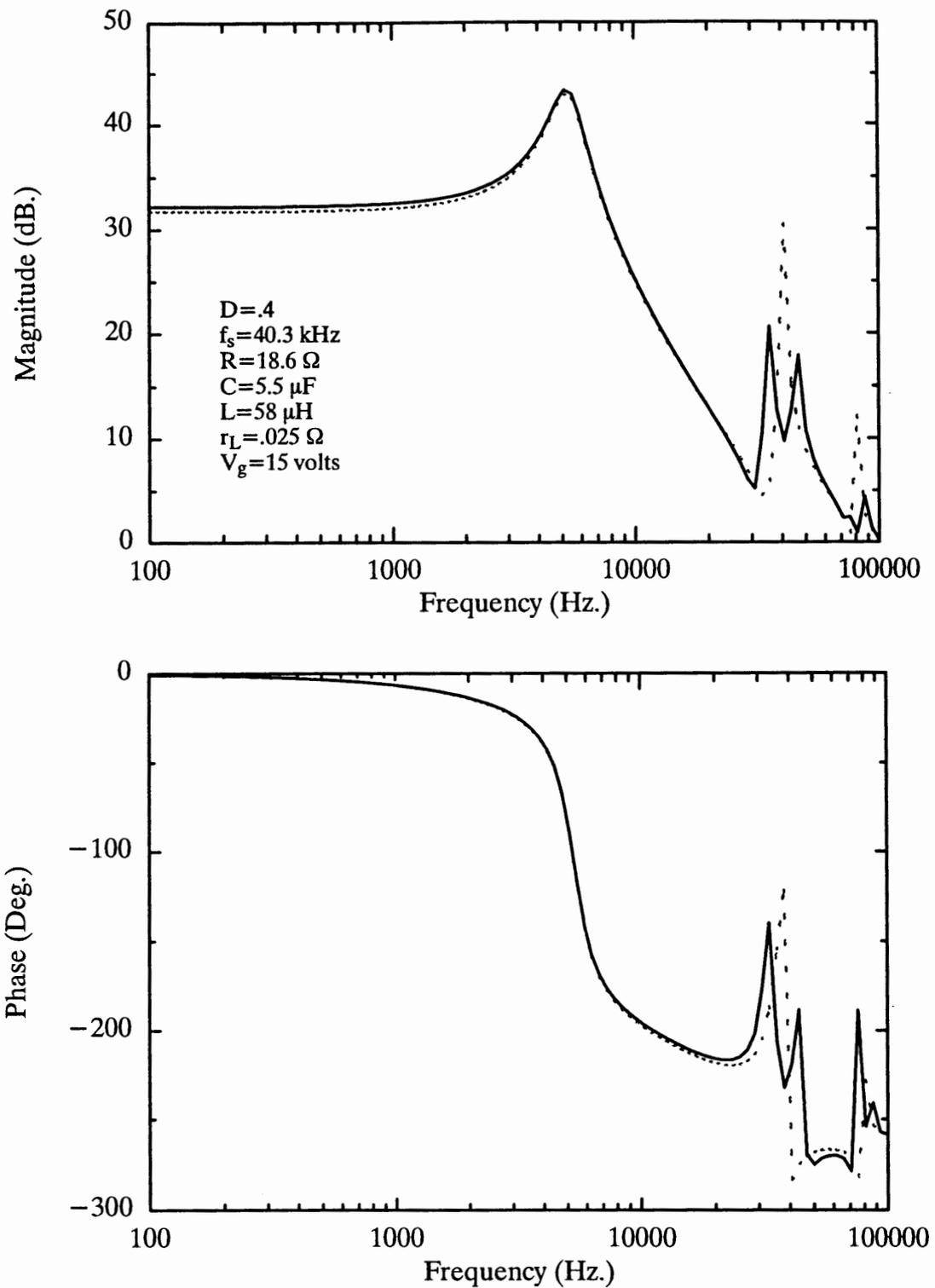


Figure 5 – 5 The control-to-output response of the PWM boost converter operating in CCM; exact (solid line) versus the reduced order second model (dashed line).

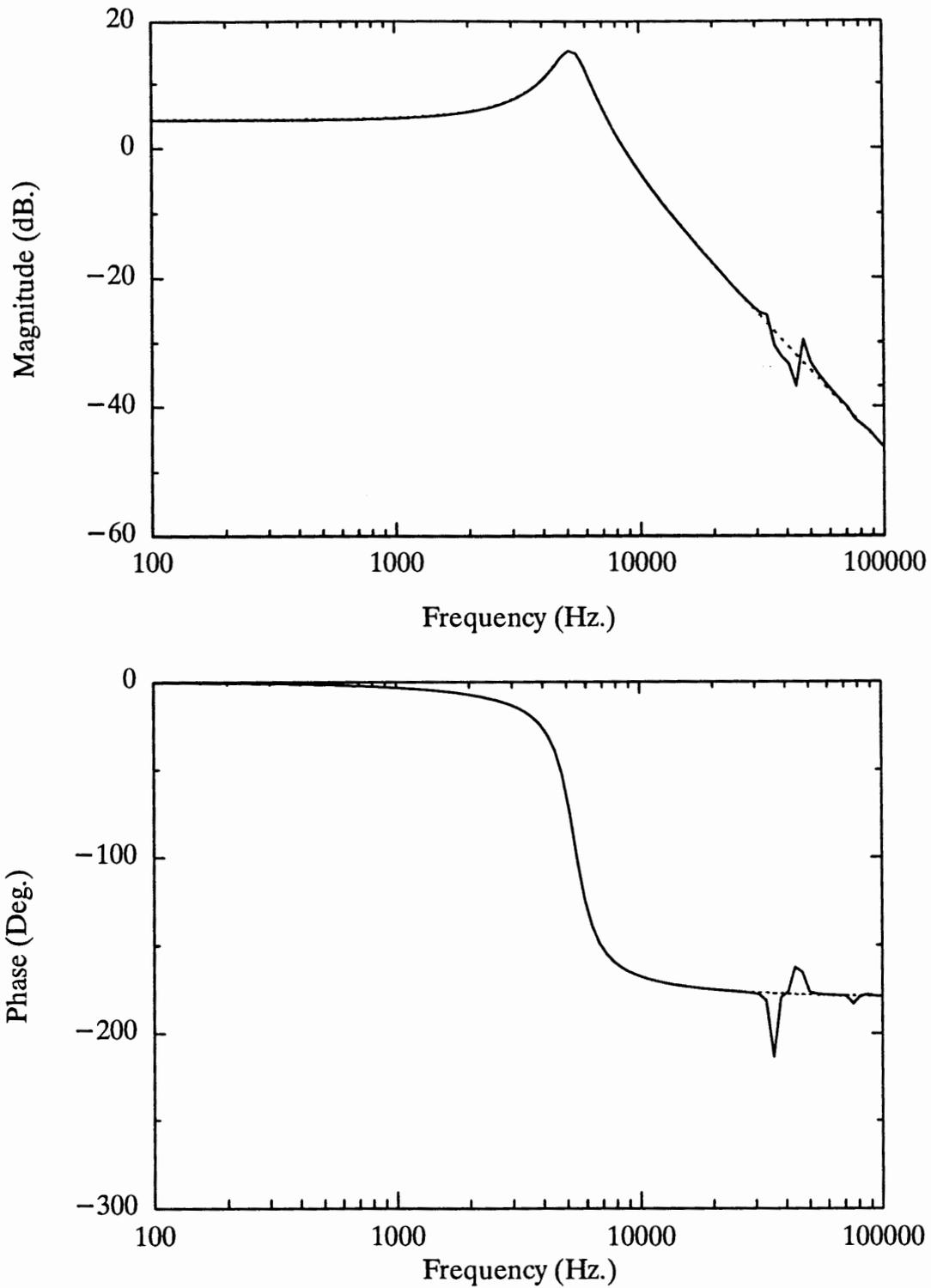


Figure 5 – 6 The input-to-output response of the PWM boost converter operating in CCM; exact (solid line) versus the reduced second model (dashed line).

Maple V has been used to obtain expressions for the proposed reduced model. Since solving these expressions will take much time, only the following parameters are listed:

$$v_{id}' = -V_{pa} \quad (5 - 19)$$

$$h_r = D' \quad (5 - 20)$$

$$z_i = 0 \quad (5 - 21)$$

$$i_{od}' = \frac{-jV_{pa}(1 - e^{-j\omega D'T_s}) - I_c \omega L(1 - e^{-j\omega T_s})}{\omega L(1 - e^{-j\omega T_s})} \quad (5 - 22)$$

$$h_f = D' \quad (5 - 23)$$

$$y_o = 0 \quad (5 - 24)$$

$$i_{id} = \frac{-jV_{pa}}{\omega L} \quad (5 - 25)$$

$$v_{od} = 0 \quad (5 - 26)$$

Substituting the above results in equations (5 - 3) and (5 - 4) will give:

$$v_{id} = -V_{pa} \quad (5 - 27)$$

and

$$i_{od} = \frac{-jV_{pa}(1 - e^{-j\omega D'T_s}) - I_c \omega L(1 - e^{-j\omega T_s})}{\omega L(1 - e^{-j\omega T_s})} + \frac{jD'V_{pa}}{\omega L} \quad (5 - 28)$$

For the exact small-signal transfer functions, the instantaneous inductor current and capacitor voltage are used. The model parameters have expressions in terms of I_c and V_{pa} which are steady state values. In order for the model to produce frequency responses comparable to the exact results, the instantaneous values must be used. For a boost converter, according to equations (4 - 1) and (4 - 2), I_c and V_{pa} are equal to the states of the system. The instantaneous quantities can be given by:

$$I_{c\Delta} = I_c + \frac{\Delta i_c}{2} \quad (5 - 29)$$

and

$$V_{pa\Delta} = V_{pa} + \frac{\Delta v_{pa}}{2} \quad (5 - 30)$$

Since z_i and v_{od} were zero, v_{id} is unchanged and only the i_{od} parameter has been effected by this method. Therefore, for the PWM duty ratio converters, the new model reduces to Figure 5 – 7.

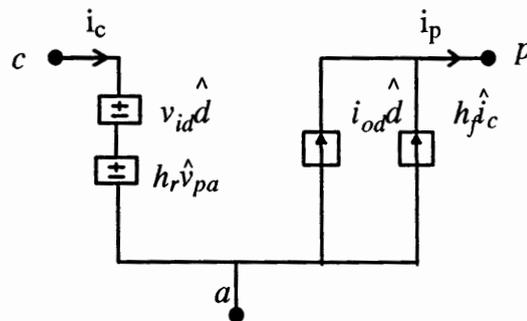


Figure 5 – 7 Hybrid parameter switch model for PWM duty ratio programming.

5.1.2 PWM Current Programmed Model

In the previous modeling approach presented in chapter 4, the control–to–output response of the current programmed model completely failed to predict the exact results. In this section, the current programmed parameters expressions are obtained numerically. Then, they are used to plot the control–to–output and input–to–output responses of the system. Next, these responses are compared against the exact results.

From the control–to–output and input–to–output plots, shown in Figure 5 – 8 and Figure 5 – 9, the accuracy of this model can be verified. All the plots match the exact plots. The next step would be to reduce the order of the model to simplify the results.

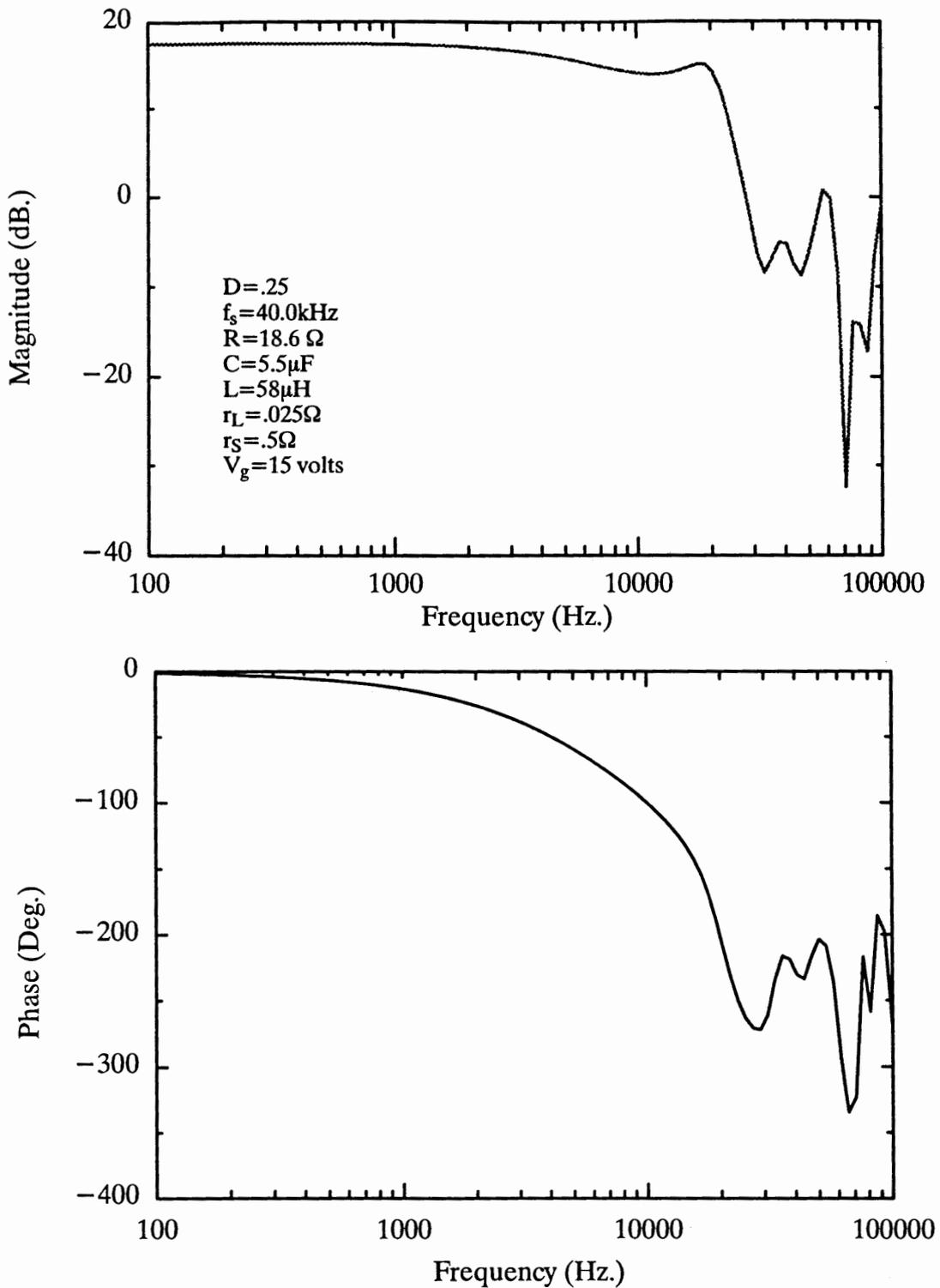


Figure 5 – 8 The control-to-output response of the PWM boost converter operating in CCM under current mode programming; exact (solid line) versus the second model (dashed line).

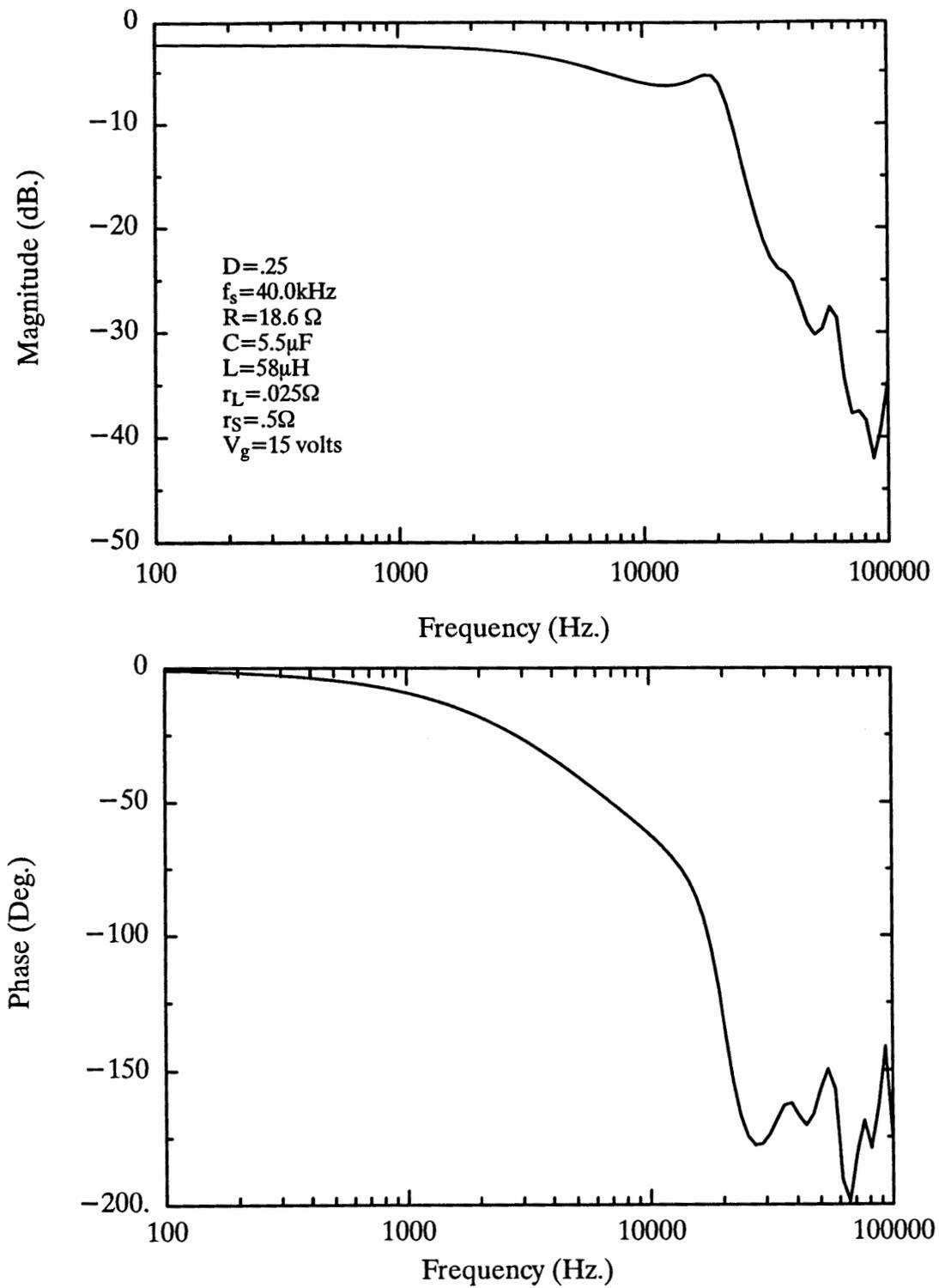


Figure 5 – 9 The input-to-output response of the PWM boost converter operating in CCM under current mode programming ; exact (solid line) versus the second model (dashed line).

To obtain current mode parameters, the order of the system has to be reduced using the same technique as the one used in the PWM duty ratio case. It is assumed that the output capacitor voltage ripple is very small; hence it is replaced by a constant voltage source.

The analysis done in earlier is used now to plot the responses of the new model. The magnitude response of the control-to-output transfer function for the case of reduced order current mode model deviates from the exact small-signal transfer function results by around 3 dB as shown in Figure 5 – 11. This is a good result, considering the approximation initially made by using an independent voltage source in place of the capacitor. It should be mentioned that the model is tested on a circuit with high inductor current ripple. The control-to-output phase of the reduced model barely has any phase difference of the exact frequency analysis before reaching $f_s/2$. Figure 5 – 12 shows the input-to-output frequency response for the current mode boost converter model. The same conclusions can be derived for the magnitude response of the input-to-output as those for the control-to-output response. The phase is shifted by about 5 degrees.

The next step is to derive the symbolic expressions for the parameters. Despite the efforts to reduce the complexity of the expressions, few parameters are found to be lengthy and complex. The expressions for the current mode hybrid parameter switch parameters are listed below.

$$v_{id}' = - \frac{V_{pa} (1 - e^{-j\omega T_s})}{(1 - \Gamma e^{-j\omega T_s})} \quad (5 - 31)$$

$$i_{id} = \frac{V_{pa}}{j\omega L} \frac{(1 - e^{-j\omega T_s})}{(1 - \Gamma e^{-j\omega T_s})} \quad (5 - 32)$$

$$i_{od}' = - \left(\frac{I_c(1 - e^{-j\omega T_s})}{(1 - \Gamma e^{-j\omega T_s})} + \frac{jV_{pa}(1 - e^{-j\omega D'T_s})}{\omega L(1 - \Gamma e^{-j\omega T_s})} \right) \quad (5 - 33)$$

$$h_r = D' \quad (5 - 34)$$

$$h_f = \frac{1 - j\omega D'T_s - e^{-j\omega D'T_s} - \Gamma(1 - e^{-j\omega D'T_s} - j\omega D'T_s e^{-j\omega T_s})}{1 - \Gamma - j\omega T_s - (1 - \Gamma - j\omega T_s \Gamma)e^{-j\omega T_s}} - \frac{j\omega \mu h^T I_c (1 - e^{-j\omega T_s})}{1 - \Gamma - j\omega T_s - (1 - \Gamma - j\omega T_s \Gamma)e^{-j\omega T_s}} \quad (5 - 35)$$

$$z_i = \frac{-j\omega V_{pa} \mu h^T (1 - e^{-j\omega T_s})}{1 - \Gamma - j\omega T_s - (1 - \Gamma - j\omega T_s \Gamma)e^{-j\omega T_s}} \quad (5 - 36)$$

$$y_o = 0 \quad (5 - 37)$$

Next, equations (5 - 3) and (5 - 4) will be used to find the new v_{id} and i_{od} parameters. Since $v_{od} = 0$, these equations will be reduced to the following;

$$v_{id} = v_{id}' - z_i i_{id} \quad (5 - 38)$$

$$i_{od} = i_{od}' - h_f i_{id} \quad (5 - 39)$$

Expanding equations (5 - 38) and (5 - 39) lead to complex expressions which are not listed here.

Based on the above parameters and results, the new hybrid parameter switch model for current mode programming is shown in Figure 5 - 10.

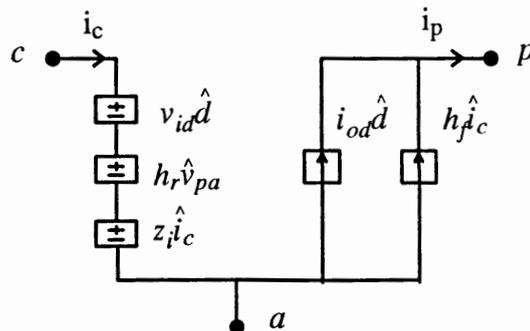


Figure 5 - 10 Hybrid parameter switch model for current mode programming.

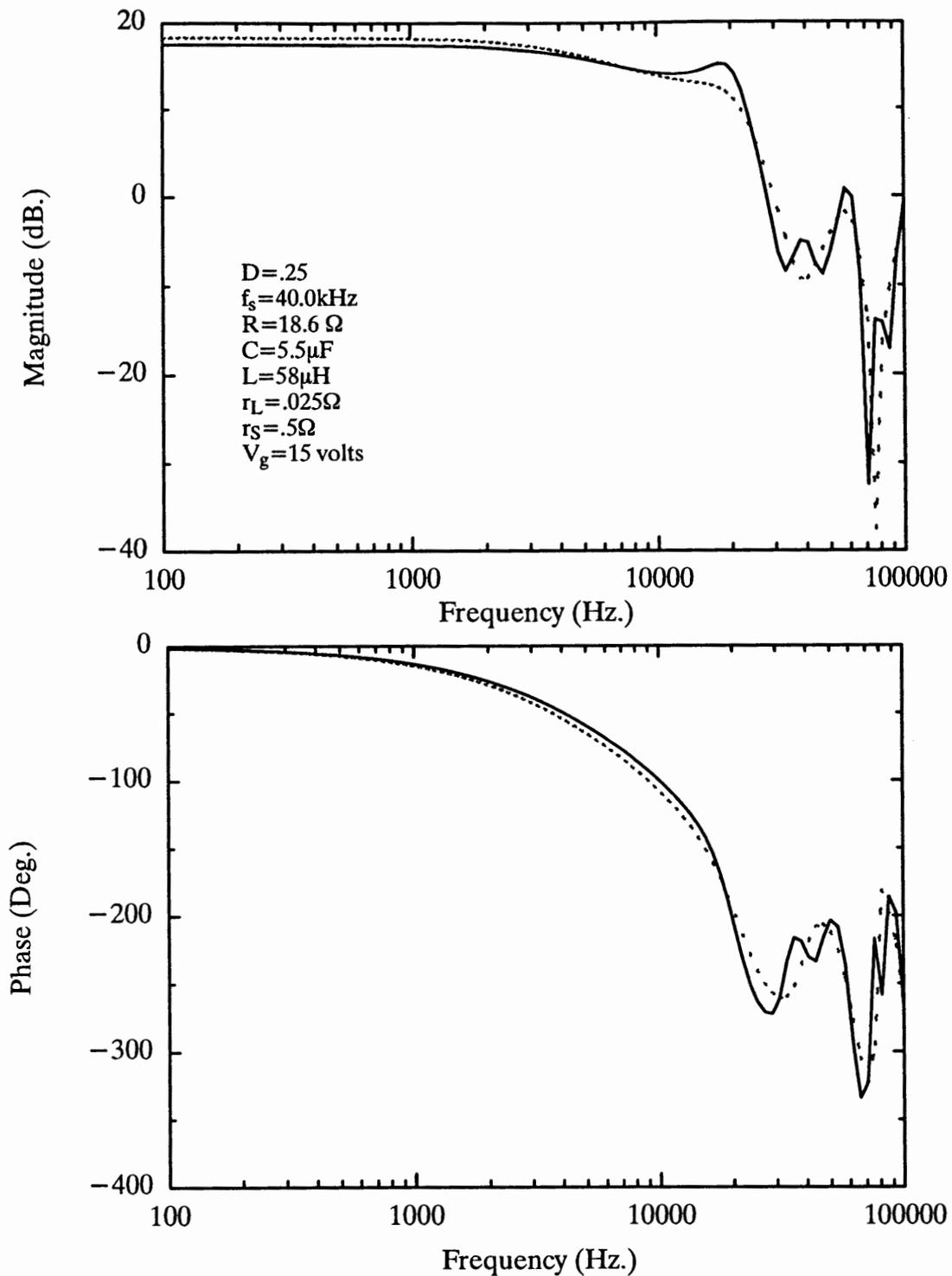


Figure 5 - 11 The control-to-output response of the PWM boost converter operating in CCM under current mode programming; exact (solid line) versus the reduced order second model (dashed line).

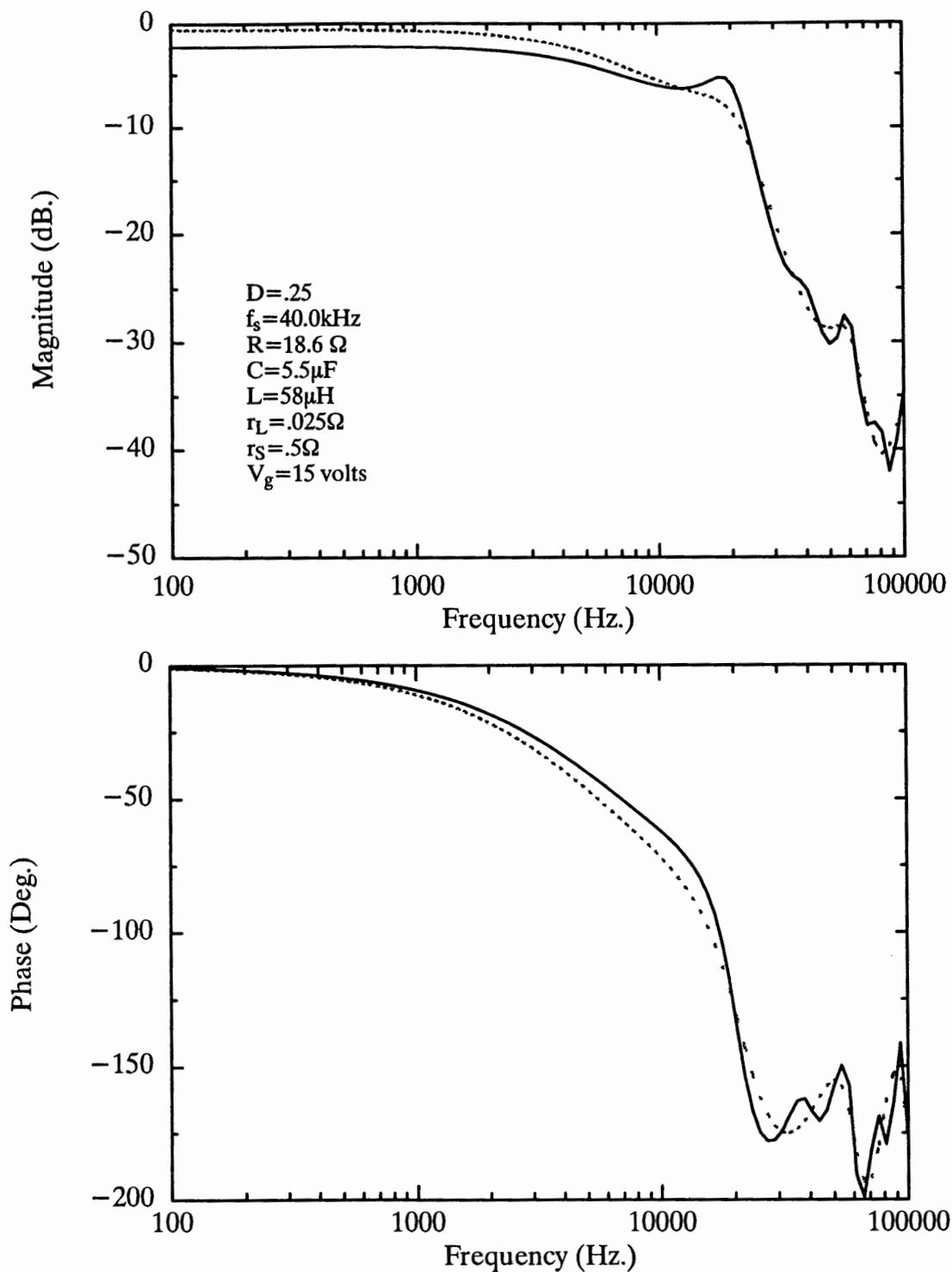


Figure 5 - 12 The input-to-output response of the PWM boost converter operating in CCM under current mode programming; exact (solid line) versus the reduced order second model (dashed line).

5.2 Buck Converter Analysis

In this section, the accuracy and usefulness of the model will be examined by analyzing the small-signal response of the buck converter. This analysis is conducted on both control schemes; duty-ratio and current-mode programming.

In order for the new, boost derived, hybrid parameter switch model to work with other converters, one must first solve for the dc operating point quantities; V_{pa} , and I_c . This is necessary because the model parameters have expressions that in some way contain the above dc terms. For example, equation (5 - 27) gives:

$$v_{id} = -V_{pa}$$

and from (5 - 28) it is known that

$$i_{od} = \frac{-jV_{pa}(1 - e^{-j\omega D'T_s}) - I_c\omega L(1 - e^{-j\omega T_s})}{\omega L(1 - e^{-j\omega T_s})} + \frac{jD'V_{pa}}{\omega L}$$

5.2.1 PWM Duty Ratio Programmed Buck Converter

A schematic of a basic PWM buck converter is shown in Figure 5 - 13. The orientation of the internal switch devices is not important for small-signal analysis. It is important, however, to appropriately indicate the direction of the independent voltage source (v_{pa}) and the current source (i_c) as shown in Figure 5 - 13.

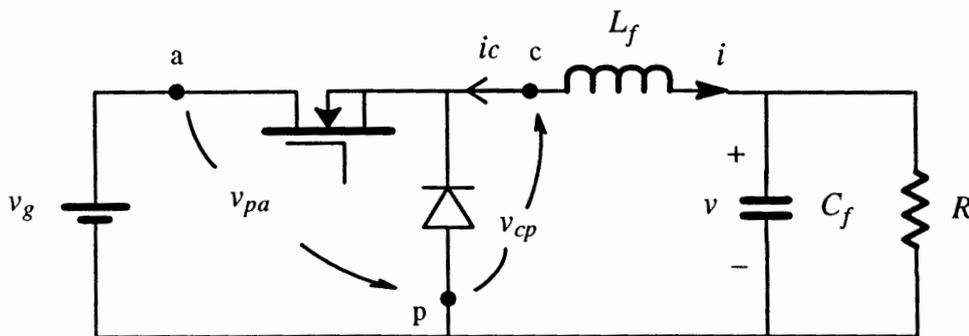


Figure 5 - 13 Circuit schematics for PWM buck converter.

Inspection of Figure 5 – 13 shows that

$$v_{pa} = - v_g \quad (5 - 40)$$

$$i_c = - i \quad (5 - 41)$$

The above set of equations contain both steady–state values and perturbation quantities. Therefore, both sides of above equations can be written in the following manner:

$$\hat{v}_{pa} + V_{pa} = - (\hat{v}_g + V_g) \quad (5 - 42)$$

$$\hat{i}_c + I_c = - (\hat{i} + I) \quad (5 - 43)$$

First the dc quantities I_c and V_{pa} of the buck converter of Figure 5 – 13 need be solved. Separating the steady state term of equations (5 – 42) and (5 – 43) will give:

$$V_{pa} = - V_g \quad (5 - 44)$$

$$I_c = - I \quad (5 - 45)$$

The small–signal terms are:

$$\hat{v}_{pa} = - \hat{v}_g \quad (5 - 46)$$

$$\hat{i}_c = - \hat{i} \quad (5 - 47)$$

From Figure 5 – 13 the following dc operating point is found:

$$I = \frac{V_g D}{R} \quad (5 - 48)$$

Substituting (5 – 48) into (5 – 45) will give:

$$I_c = - \frac{V_g D}{R} \quad (5 - 49)$$

The equivalent small–signal circuit model of the buck converter of Figure 5 – 13 is depicted in Figure 5 – 14.

Rearranging equations (5 – 50) and (5 – 51) will give :

$$\frac{\hat{d}i}{dt} = -\frac{1}{L_f}\hat{v} + \frac{v_{id}}{L_f}\hat{d} \quad (5 - 52)$$

$$\frac{d\hat{v}}{dt} = \frac{1}{C_f}\hat{i} - \frac{1}{RC_f}\hat{v} \quad (5 - 53)$$

From equations (5 – 52) and (5 – 53) the control-to-output state equation can be set up:

$$\frac{d}{dt} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} = \begin{bmatrix} 0 & -\frac{1}{L_f} \\ \frac{1}{C_f} & -\frac{1}{RC_f} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} \frac{v_{id}}{L_f} \\ 0 \end{bmatrix} \hat{d} \quad (5 - 54)$$

Next, the input-to-output equivalent model of Figure 5 – 14 is obtained by deactivating the controller terms. This model is shown in Figure 5 – 16.

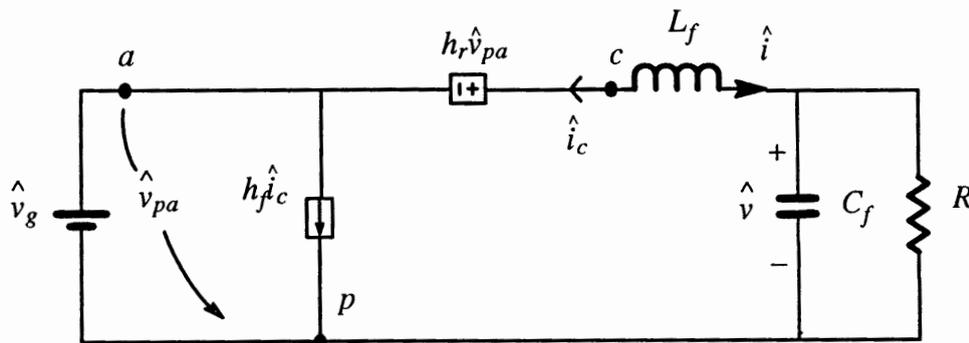


Figure 5 – 16 Equivalent input-to-output small-signal hybrid parameter switch model for the PWM buck converter of Figure 5 – 13.

Again using KVL:

$$-\hat{v}_g - h_r\hat{v}_{pa} + \frac{d\hat{i}}{dt}L_f + \hat{v} = 0 \quad (5 - 55)$$

and KCL:

$$\hat{i} - \frac{d\hat{v}}{dt}C_f - \frac{\hat{v}}{R} = 0 \quad (5 - 56)$$

Using equations (5 - 46) in (5 - 55) gives:

$$-\hat{v}_g + h_r\hat{v}_g + \frac{d\hat{i}}{dt}L_f + \hat{v} = 0 \quad (5 - 57)$$

while rearranging equations (5 - 56) and (5 - 57) gives:

$$\frac{d\hat{v}}{dt} = \frac{1}{C_f}\hat{i} - \frac{1}{RC_f}\hat{v} \quad (5 - 58)$$

$$\frac{d\hat{i}}{dt} = -\frac{1}{L_f}\hat{v} + \frac{(1 - h_r)}{L_f}\hat{v}_g \quad (5 - 59)$$

Solution of (5 - 58) and (5 - 59) may be given in state equation form :

$$\frac{d}{dt} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} = \begin{bmatrix} 0 & -\frac{1}{L_f} \\ \frac{1}{C_f} & -\frac{1}{RC_f} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} \frac{1 - h_r}{L_f} \\ 0 \end{bmatrix} \hat{v}_g \quad (5 - 60)$$

One could use equations (2 - 49) and (2 - 50) to find the desired transfer functions. In Figure 5 - 17, the control-to-output plot of the PWM buck described by (5 - 60) is compared with that of the exact small signal results. The ripple percentages for this system are:

$$\frac{\Delta v/2}{V} \times 100 \approx 12\%$$

$$\frac{\Delta i/2}{I} \times 100 \approx 69\%$$

The response of the model completely matches the exact results. The input-to-output response of the above circuit matches the exact results as shown in Figure 5 - 18. From above results, it can be seen that the proposed model is valid for the case of PWM buck converter.

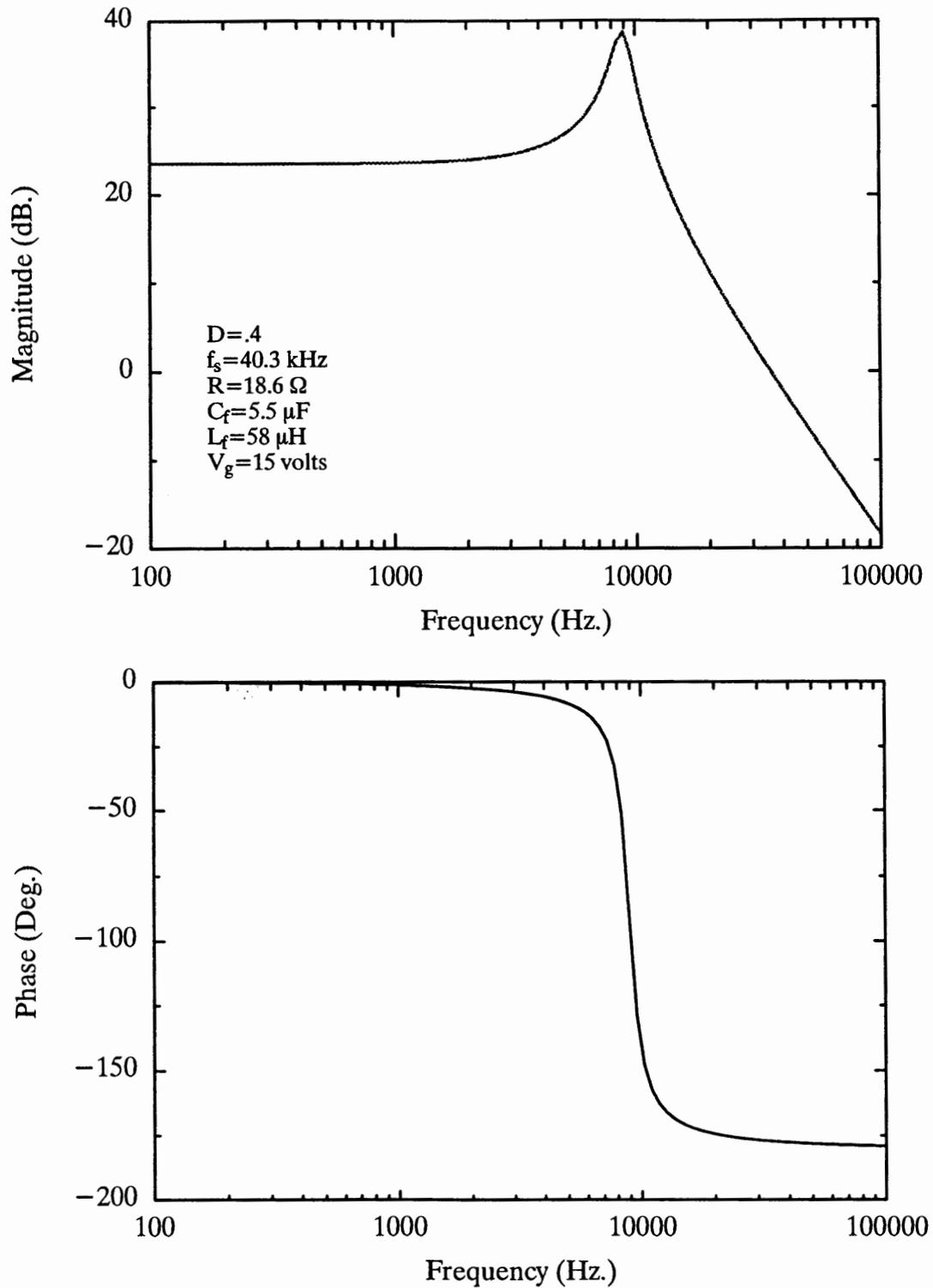


Figure 5 - 17 The control-to-output response of the PWM buck converter operating in CCM; exact (solid line) versus the second model (dashed line).

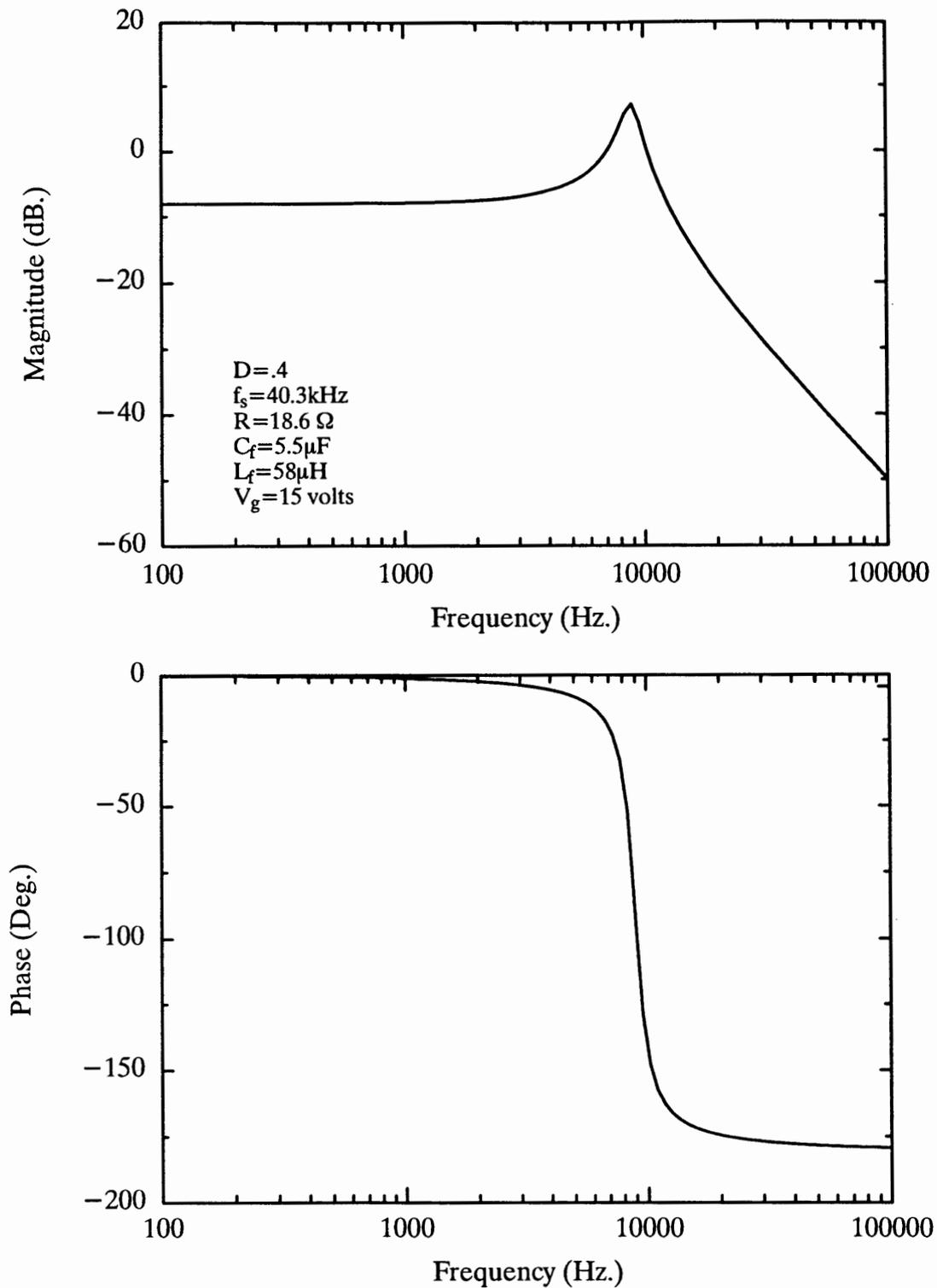


Figure 5 – 18 The input-to-output response of the PWM buck converter operating in CCM ; exact (solid line) versus the second model (dashed line).

5.2.2 PWM Current Programmed Buck Converter

To verify this modeling approach, the control-to-output and input-to-output responses of the current programmed buck converter of Figure 5 – 19 is determined. In converter topologies with a single PWM switch implementation, the positive slope of the inductor current is identical to the transistor current slope in the ON position. Therefore, the inductor current is sensed instead of the transistor current.

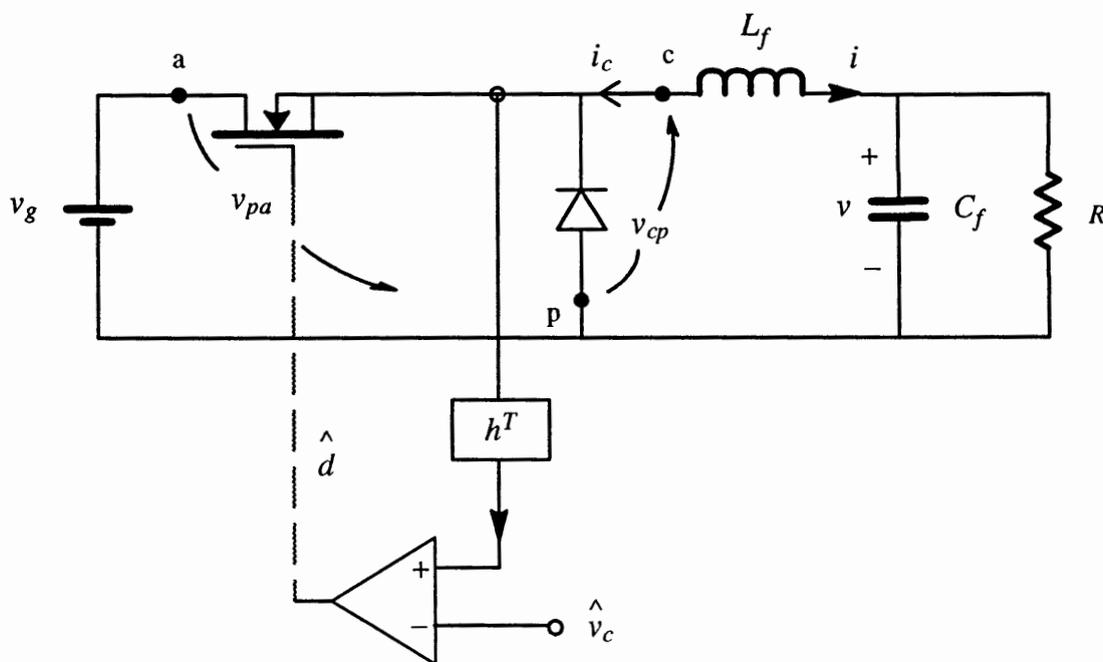


Figure 5 – 19 Circuit schematics for PWM buck converter.

Using equation (3 – 53) for a single inductor state gives:

$$\mu = \frac{1}{h^T m_1 + m_c} \quad (5 - 61)$$

where

h^T denotes the current to voltage transformation gain, m_1 is the slope of the rising portion of the sensed current and m_c is the compensation ramp.

Here the inductor current slope is of concern. Therefore, equation (3 – 54) will reduce to:

$$\Gamma = 1 - \xi h^T \mu \quad (5 - 62)$$

Since μ and Γ are contingent on the slope of the inductor current and stabilizing ramp, they are carried through parameters as symbols. Therefore, depending on the particular converter, μ and Γ will need to be expanded into equations (5 – 61) and (5 – 62), respectively, and the results will be incorporated into the parameters expressions.

It is important to change the sign of h^T when inductor current i is flowing in the opposite direction of common terminal current i_c . This is due to the fact that h^T is the gain associated with inductor current and must be consistent with the direction of the inductor current flow.

Next, equations (5 – 61) and (5 – 62) are solved for the buck converter of Figure 5 – 19. The positive slope of the inductor current is :

$$m_1 = \frac{V_g - V}{L_f} \quad (5 - 63)$$

Furthermore, from the dc conversation ratio:

$$V = V_g D \quad (5 - 64)$$

Substituting equation (5 – 64) into equation (5 – 63) and simplifying gives :

$$m_1 = \frac{V_g D'}{L_f} \quad (5 - 65)$$

Since no compensation ramp has been used in this example, equation (5 – 61) reduces to :

$$\mu = \frac{1}{h^T m_1} \quad (5 - 66)$$

and consequently equation (5 – 62) reduces to :

$$\Gamma = 1 - \frac{\xi}{m_1} \quad (5 - 67)$$

ξ can be found by solving equation (3 - 48). Therefore,

$$\xi = \frac{V_g}{L_f} \quad (5 - 68)$$

Substituting equation (5 - 68) into equation (5 - 67) and simplifying will give

$$\Gamma = -\frac{D}{D'} \quad (5 - 69)$$

For the buck converter,

$$i_c = -i$$

Therefore, the sign of the transformation gain h^T must be changed as well, so it would reflect the change in the direction of the inductor current. Hence,

$$h^T \rightarrow -h^T \quad (5 - 70)$$

The above dc operating conditions, along with those obtained for the PWM duty-ratio programmed buck converter, can be used to deduce the small signal response of the current programmed buck converter of Figure 5 - 19. First, the buck converter is modeled using the current programmed hybrid parameter switch model as shown in Figure 5 - 20.

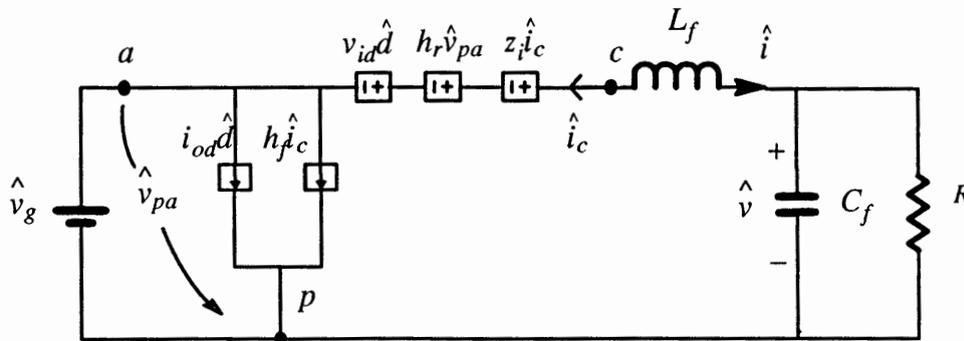


Figure 5 - 20 Equivalent small-signal hybrid parameter switch model for the PWM buck converter of Figure 5 - 19.

Analysis of Figure 5 – 20 is essentially the same as that of the PWM buck converter. The z_i parameter needs to be incorporated into the A matrix of equations (5 – 54) and (5 – 60). The control–to–output and input–to–output equations can be given in the state space representation form as shown in equation (5 – 71). The control–to–output transfer function can be obtained by deactivating the effect of the line input, v_g . The input–to–output transfer function can be found in the same manner, by setting the controller quantities to zero. The state space formulation for the current mode buck converter using the hybrid parameter switch model is:

$$\frac{d}{dt} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} = \begin{bmatrix} -\frac{z_i}{L_f} & -\frac{1}{L_f} \\ \frac{1}{C_f} & -\frac{1}{RC_f} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} \frac{1-h_r}{L_f} \\ 0 \end{bmatrix} \hat{v}_g + \begin{bmatrix} \frac{v_{id}}{L_f} \\ 0 \end{bmatrix} \hat{d} \quad (5 - 71)$$

Equation (5 – 71) is used to obtain the frequency response of the model. Only z_i , h_r and v_{id} parameters are used for the current mode buck analysis. Next, the response of the model is plotted against the exact small signal response of the system. This system has the following ripple percentages:

$$\frac{\Delta v/2}{V} \times 100 \approx 0.16\%$$

$$\frac{\Delta i/2}{I} \times 100 \approx 12.8\%$$

The control to output response of this model is accurate everywhere except at the switching frequency and its harmonics as shown in Figure 5 – 21. As it is apparent from Figure 5 – 22, this model fails for the input–to–output response of the current programmed buck converter. The magnitude and the phase response of this model are significantly different from the exact plots. There is an overall +12 dB. level shift in the magnitude response of the model at low frequencies up to $f_s/2$. The phase plot of the model has about 180° phase lag. Much of this error is due to the fact that the h_r parameter found by this method is equal to that of the PWM duty ratio control and con-

tains no information about the sensed inductor current. This is further explained in chapter 6.

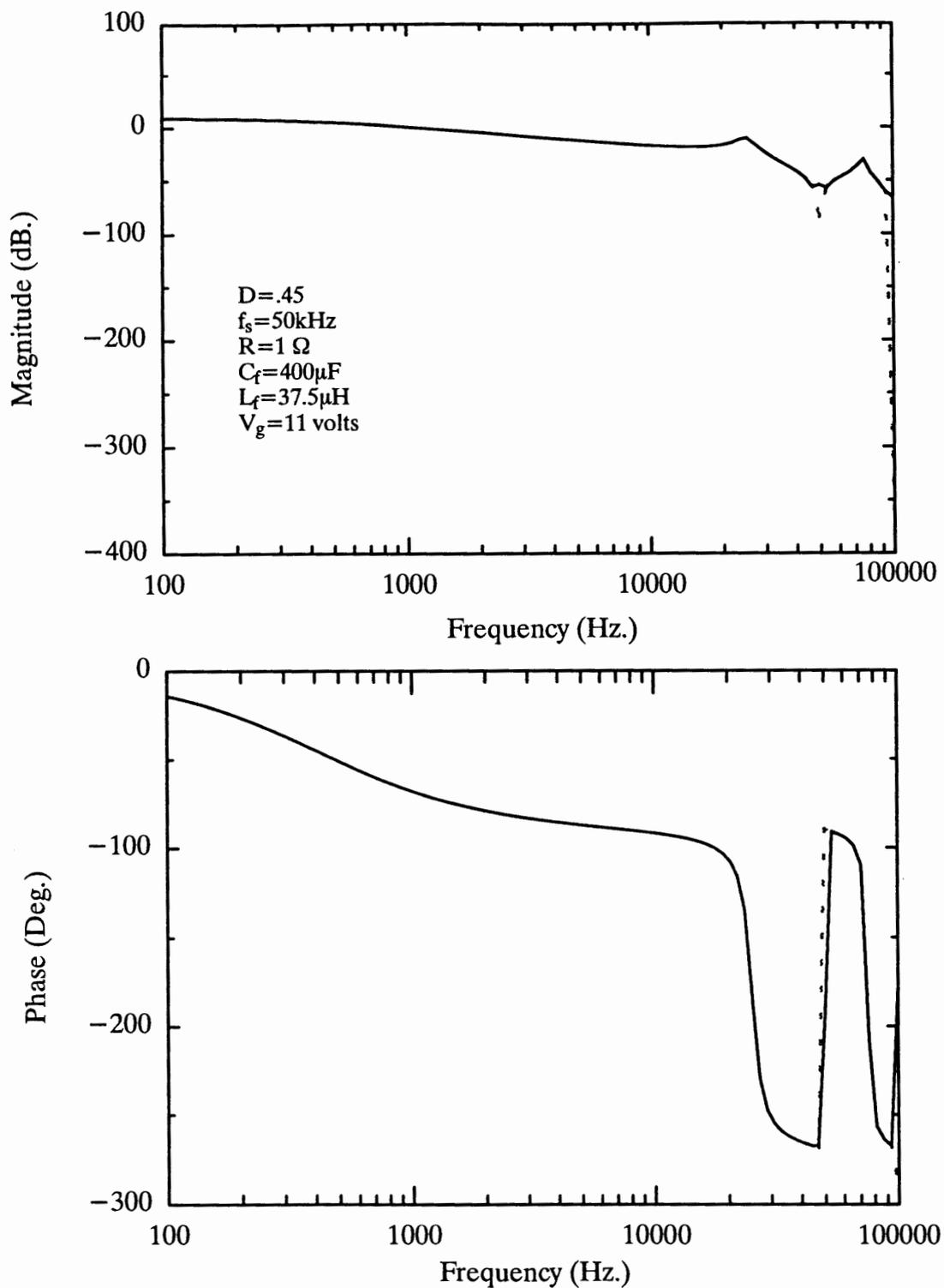


Figure 5 - 21 The control-to-output response of the PWM buck converter operating in CCM under current mode programming; exact (solid line) versus the second model (dashed line).

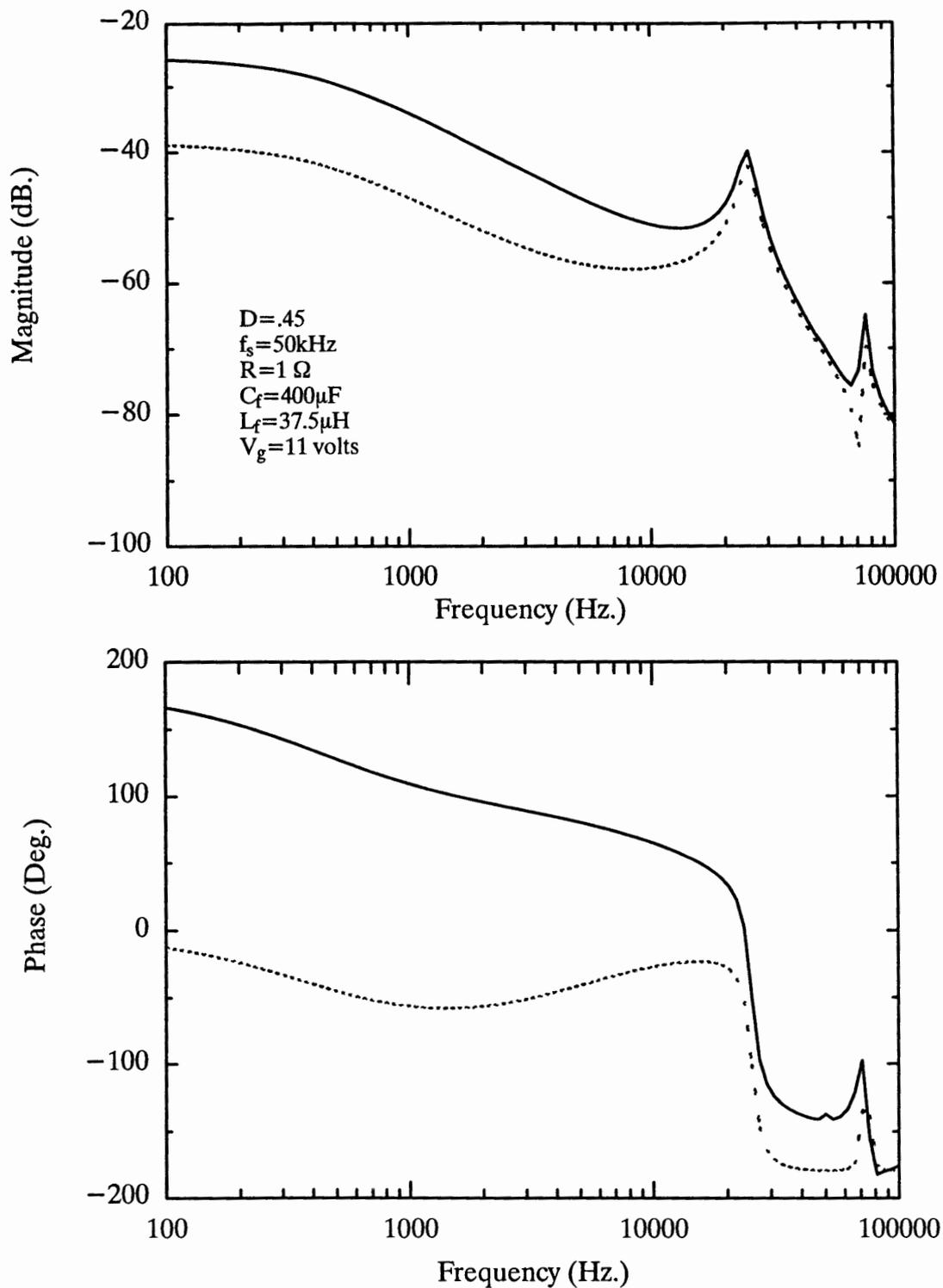


Figure 5 - 22 The input-to-output response of the PWM buck converter operating in CCM under current mode programming; exact (solid line) versus the second model (dashed line).

5.3 Buckboost Converter Analysis

In this section, the hybrid switch model is used to predict the small signal frequency response of a buckboost converter. The model results are compared against the exact ones. The analysis done here are for the duty ratio programmed and current programmed buckboost converters operating in CCM.

5.3.1 PWM Duty Ratio Programmed Buckboost Converter

Figure 5 – 23 shows a basic PWM buckboost converter. For small signal analysis of the converter, the PWM switch has been replaced by the hybrid parameter switch model as shown in Figure 5 – 24.

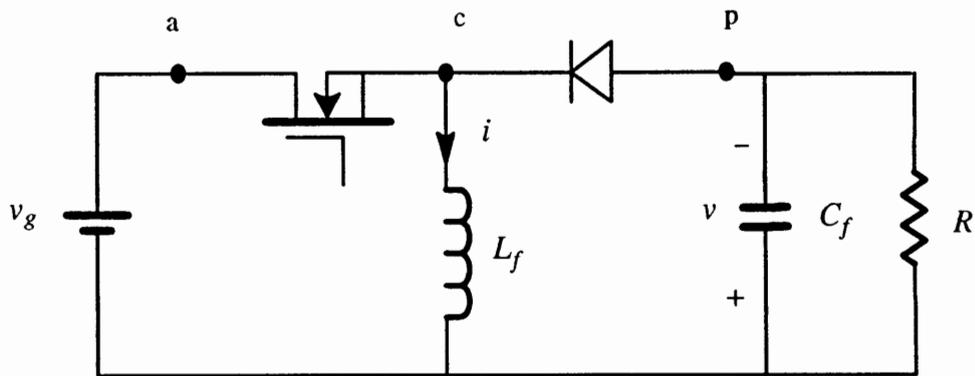


Figure 5 – 23 Circuit schematics for PWM buckboost converter.

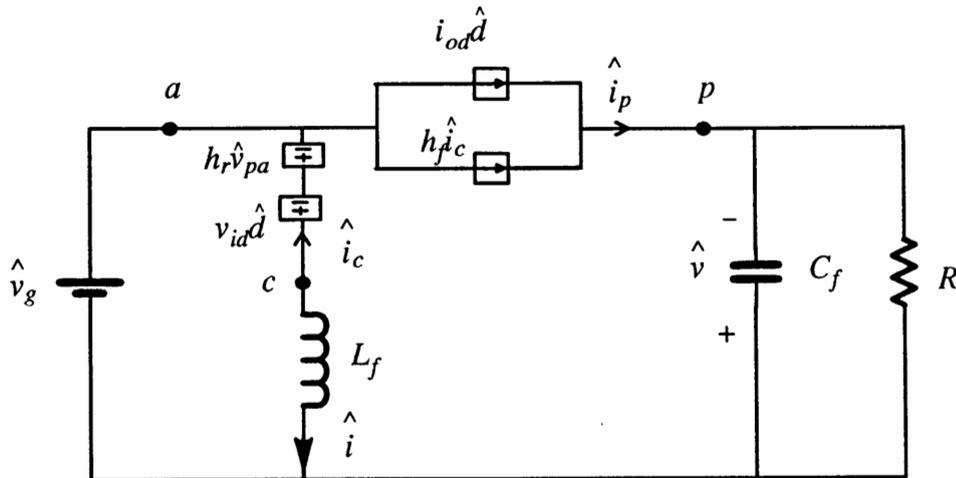


Figure 5 – 24 Equivalent small–signal hybrid parameter switch model for the PWM buckboost converter of Figure 5 – 23.

The dc operating point conditions are deduced from Figure 5 – 23. They are:

$$I_c = -I \quad (5 - 72)$$

$$V_{pa} = -(V_g + V) \quad (5 - 73)$$

The small signal perturbation terms found from Figure 5 – 24 are:

$$\hat{i}_c = -\hat{i} \quad (5 - 74)$$

and

$$\hat{v}_{pa} = -(\hat{v}_g + \hat{v}) \quad (5 - 75)$$

To obtain the small–signal control–to–output transfer function, all of the independent sources except \hat{d} need to be deactivated. Therefore, in Figure 5 – 24, \hat{v}_g will be shorted and as a result this model reduces to the one shown in Figure 5 – 25.

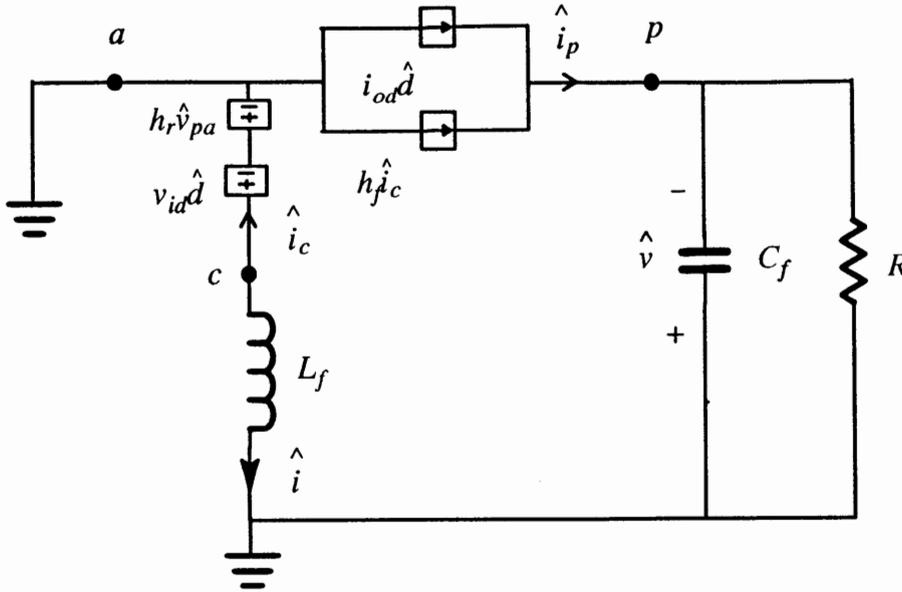


Figure 5 – 25 Equivalent small–signal control–to–output hybrid parameter switch model for PWM buckboost converter of Figure 5 – 23.

Hence, equation (5 – 75) will reduce to:

$$\hat{v}_{pa} = -\hat{v}, \quad (5 - 76)$$

Furthermore, using KVL one gets:

$$-h_r \hat{v}_{pa} - v_{id} \hat{d} + \frac{di}{dt} L_f = 0 \quad (5 - 77)$$

and substituting equation (5 – 76) into equation (5 – 77) and simplifying gives:

$$\frac{di}{dt} = -\frac{h_r}{L_f} \hat{v} + \frac{v_{id}}{L_f} \hat{d} \quad (5 - 78)$$

Employing KCL will give:

$$\hat{i}_p + \frac{d\hat{v}}{dt} C_f + \frac{\hat{v}}{R} = 0 \quad (5 - 79)$$

It is also known that

$$\hat{i}_p = i_{od}\hat{d} + h_f\hat{i}_c \quad (5 - 80)$$

Substituting equation (5 - 80) into equation (5 - 79) leads to :

$$i_{od}\hat{d} + h_f\hat{i}_c + \frac{d\hat{v}}{dt}C_f + \frac{\hat{v}}{R} = 0 \quad (5 - 81)$$

Substitution of equation (5 - 74) into equation (5 - 81) and rearranging gives :

$$\frac{d\hat{v}}{dt} = -\frac{i_{od}}{C_f}\hat{d} + \frac{h_f}{C_f}\hat{i} - \frac{1}{RC_f}\hat{v} \quad (5 - 82)$$

State equations (5 - 78) and (5 - 82) are rewritten in the state equation matrix form :

$$\frac{d}{dt} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} = \begin{bmatrix} 0 & -\frac{h_r}{L_f} \\ \frac{h_f}{C_f} & -\frac{1}{RC_f} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} \frac{v_{id}}{L_f} \\ -\frac{i_{od}}{C_f} \end{bmatrix} \hat{d} \quad (5 - 83)$$

The input to output equivalent small-signal model depicted in Figure 5 - 26 is obtained by deactivating the controller signal. The input-to-output transfer function of Figure 5 - 26 may also be found using state space analysis of the system.

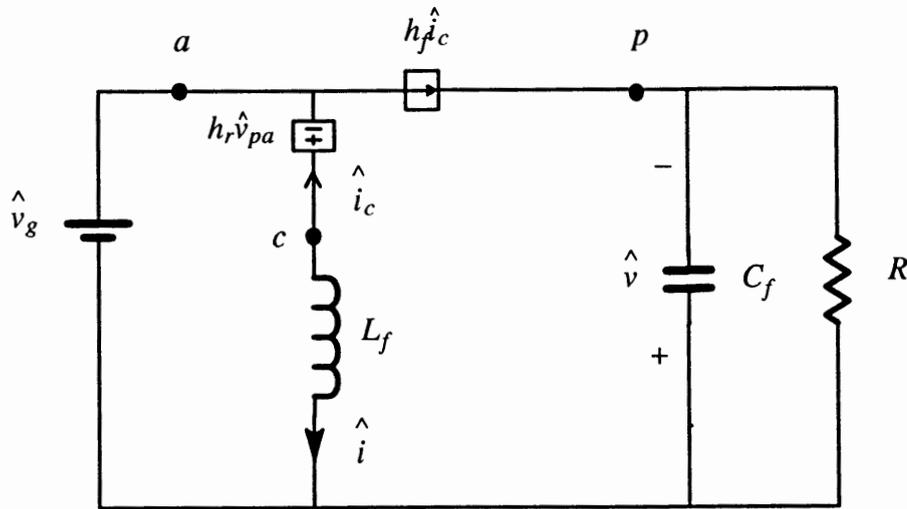


Figure 5 – 26 Equivalent input–to–output small–signal hybrid parameter switch model for the PWM buckboost converter of Figure 5 – 23.

Applying KVL will give :

$$-\hat{v}_g - h_r \hat{v}_{pa} + \frac{di}{dt} L_f = 0 \quad (5 - 84)$$

Substituting equation (5 – 75) into equation (5 – 84) and rearranging will give:

$$\frac{di}{dt} = -\frac{h_r \hat{v}}{L_f} + \frac{1 - h_r}{L_f} \hat{v}_g \quad (5 - 85)$$

Similarly, the second equation is formed using KCL :

$$\hat{i}_p + \frac{d\hat{v}}{dt} C_f + \frac{\hat{v}}{R} = 0 \quad (5 - 86)$$

Using

$$\hat{i}_p = h_f \hat{i}_c \quad (5 - 87)$$

The substitution of equation (5 – 87) into equation (5 – 86) gives :

$$h_f \hat{i}_c + \frac{d\hat{v}}{dt} C_f + \frac{\hat{v}}{R} = 0 \quad (5 - 88)$$

Using equation (5 – 74) to simplify equation (5 – 88) will give:

$$\frac{d\hat{v}}{dt} = \frac{d\hat{i}}{C_f} - \frac{1}{RC_f}\hat{v} \quad (5 - 89)$$

and input-to-output matrices are :

$$\frac{d}{dt} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} = \begin{bmatrix} 0 & -\frac{h_r}{L_f} \\ \frac{h_f}{C_f} & -\frac{1}{RC_f} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} \frac{1-h_r}{L_f} \\ 0 \end{bmatrix} \hat{v}_g \quad (5 - 90)$$

The accuracy of the above results is examined by comparing its frequency response against the exact plots, shown in Figure 5 – 27 and Figure 5 – 28. The percentage ripples are found to be:

$$\frac{\Delta v/2}{V} \times 100 \approx 1.3\%$$

$$\frac{\Delta i/2}{I} \times 100 \approx 9.5\%$$

From Figure 5 – 27 it can be observed that the control to output response is accurate even beyond half the switching frequency, $f_s/2$. It is evident from Figure 5 – 28 that the input to output magnitude response is very accurate up to $f_s/2$. The phase associated with the input to output response of the model is exact up to the switching frequency, f_s .

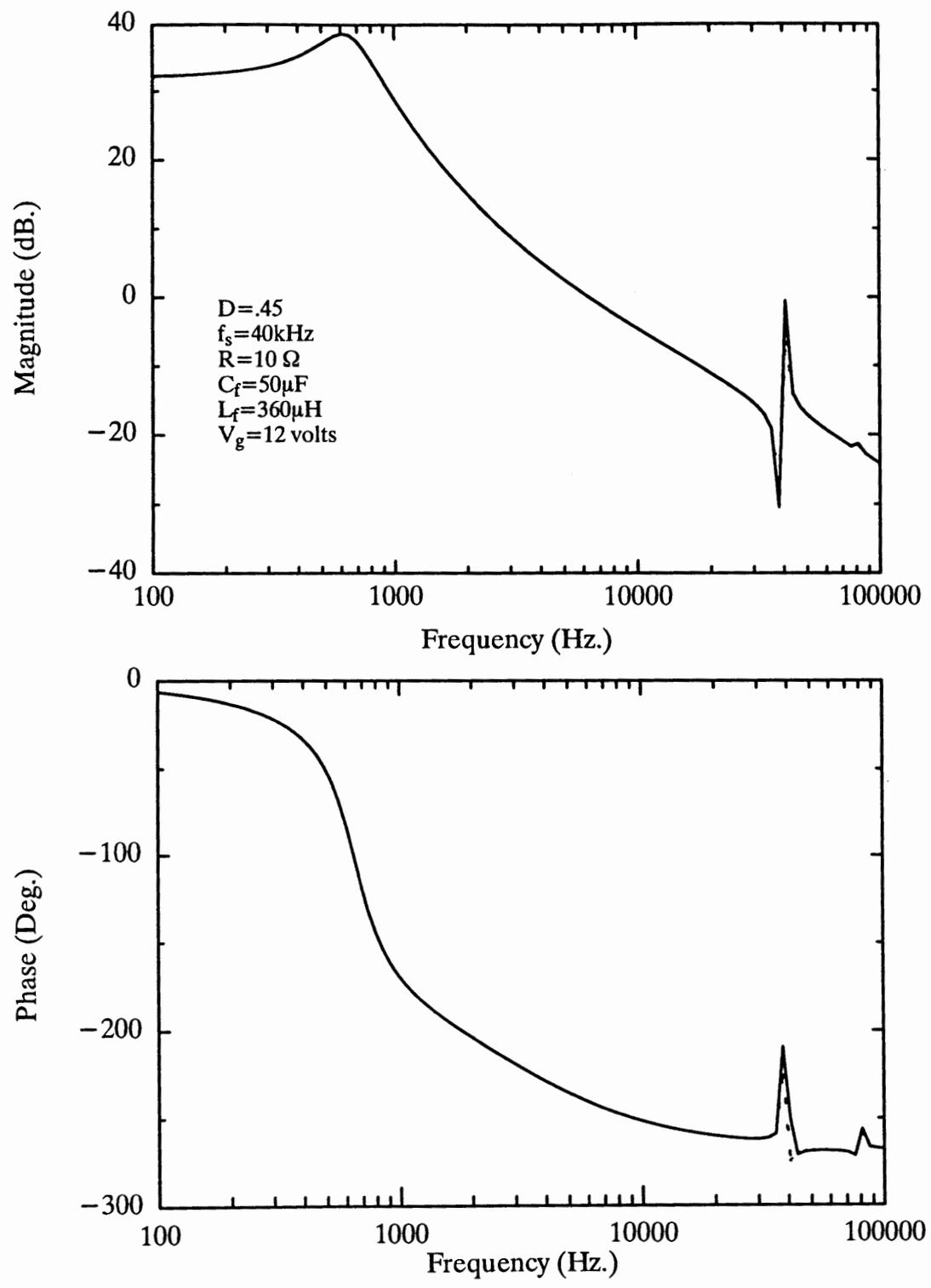


Figure 5 - 27 The control-to-output response of the PWM buck-boost converter operating in CCM; exact (solid line) versus the second model (dashed line).

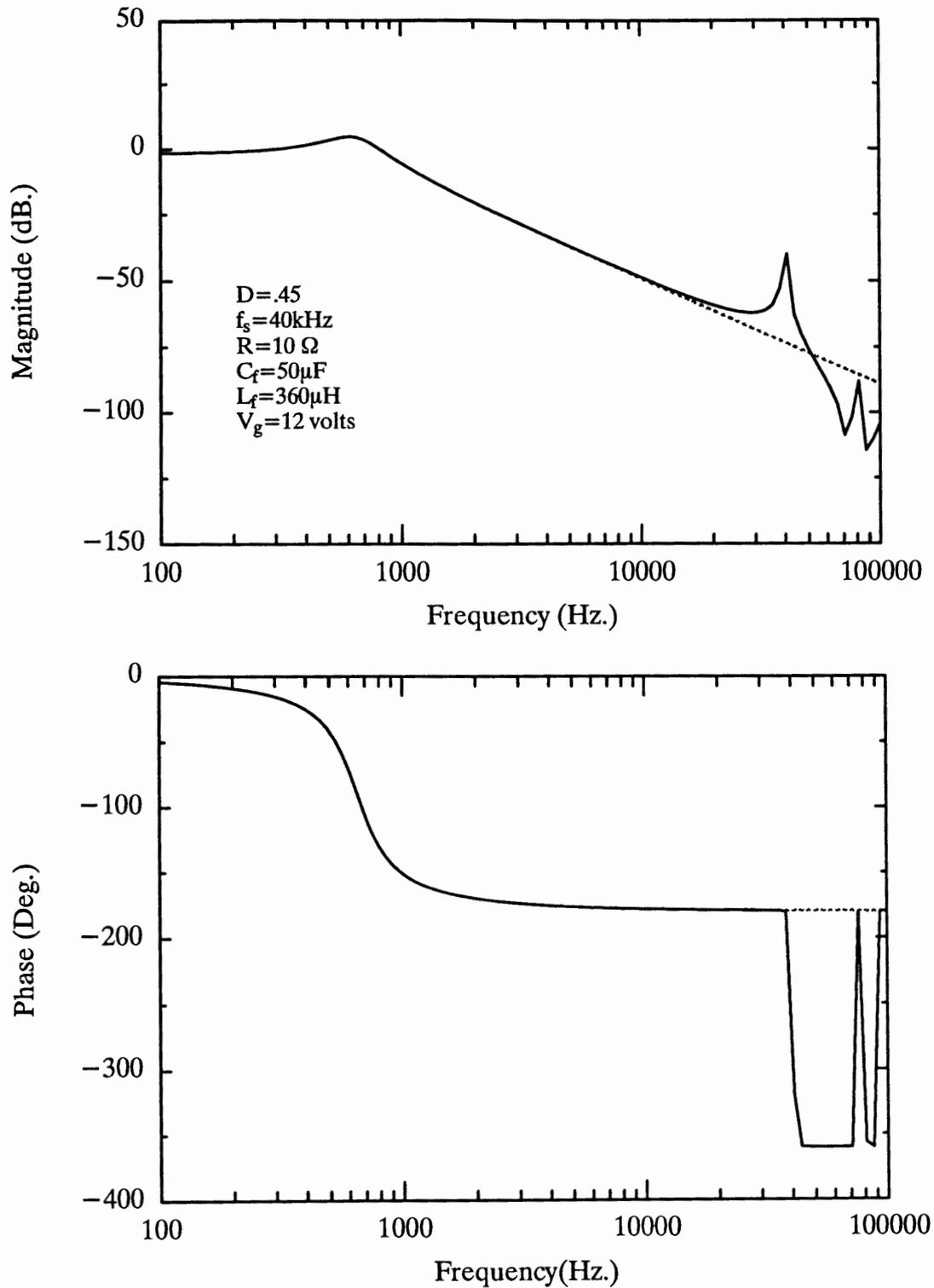


Figure 5 - 28 The input-to-output response of the PWM buck-boost converter operating in CCM; exact (solid line) versus the second model (dashed line).

5.3.2 PWM Current Programmed Buckboost Converter

Figure 5 – 29 represents a basic buckboost converter operating in current mode programming. Replacing the switch by its equivalent current mode hybrid model will result in Figure 5 – 30.

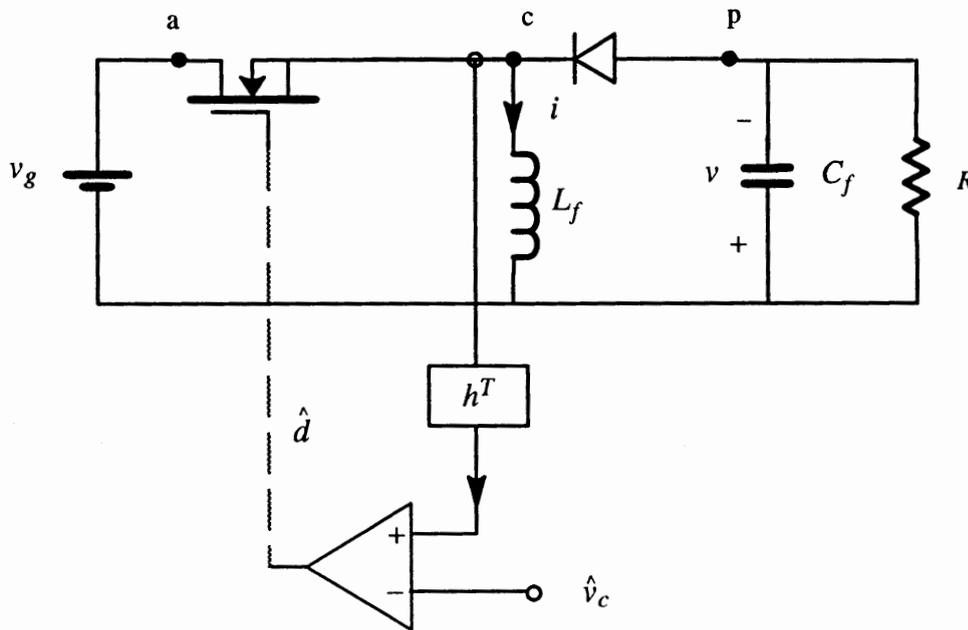


Figure 5 – 29 Circuit schematics for PWM buckboost converter operating in current programming mode.

From Figure 5 – 29, the slope of the rising edge of the inductor current is found to be

$$m_1 = \frac{V_g}{L_f} \quad (5 - 91)$$

In this circuit, no compensation ramp has been used, therefore, substituting equation (5 – 91) into equation (5 – 61) will give:

$$\mu = \frac{L_f}{h^T V_g} \quad (5 - 92)$$

Next Γ can be found, but before that ξ has to be identified.

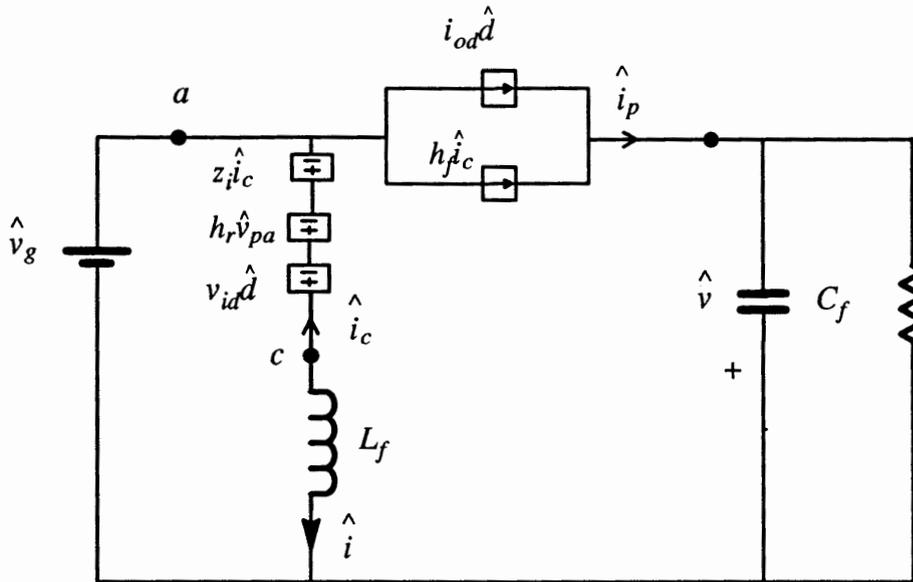


Figure 5 – 30 Equivalent small–signal hybrid parameter switch model for current mode analysis of the buckboost converter of Figure 5 – 23.

From (3 – 48) one finds:

$$\xi = \frac{V_g}{L_f} + \frac{V}{L_f} \quad (5 - 93)$$

The steady state output capacitor voltage is actually related to the input voltage by the dc conversion ratio. For the buckboost converter used in this example,

$$V = \frac{V_g D}{D'} \quad (5 - 94)$$

Substituting equation (5 – 94) in equation (5 – 93) will result in:

$$\xi = \frac{V_g}{L_f} \left(1 + \frac{D}{D'} \right) \quad (5 - 95)$$

and substituting equations (5 – 92) and (5 – 95) into equation (5 – 62) gives:

$$\Gamma = 1 - \frac{V_g}{L_f} \left(1 + \frac{D}{D'} \right) h^T \frac{L_f}{h^T V_g} \quad (5 - 96)$$

which simplifies to

$$r = -\frac{D}{D'} \quad (5 - 97)$$

In the buckboost converter of Figure 5 – 30, the common terminal current is flowing towards the opposite side of the inductor current as shown in equation (5 – 74). This means that the sign of h^T must be changed so it would be consistent with the direction of the inductor current. Therefore, equation (5 – 70) holds true for the buckboost converter as well.

Figure 5 – 30 could be used to find the small–signal state equations of the system. The current programmed model is essentially the same as the duty ratio model with the addition of the z_i parameter. Therefore, the only change will be in the A matrix which is given below:

$$A = \begin{bmatrix} -\frac{z_i}{L_f} & -\frac{h_r}{L_f} \\ \frac{h_f}{C_f} & -\frac{1}{RC_f} \end{bmatrix} \quad (5 - 98)$$

From Figure 5 – 31, one can see that the control–to–output response is very accurate; therefore, v_{id} and i_{od} are exact and give good results. Figure 5 – 32 shows that the input to output parameters do not produce satisfactory results, especially in the phase plot. The capacitor voltage and inductor current ripples are found to be:

$$\frac{\Delta v/2}{V} \times 100 \approx 10\%$$

$$\frac{\Delta i/2}{I} \times 100 \approx 11\%$$

The input–to–output responses of both the buck and buckboost converters using the second model were inaccurate. In the next chapter, a new approach is presented which leads to accurate results for the input–to–output responses of different systems.

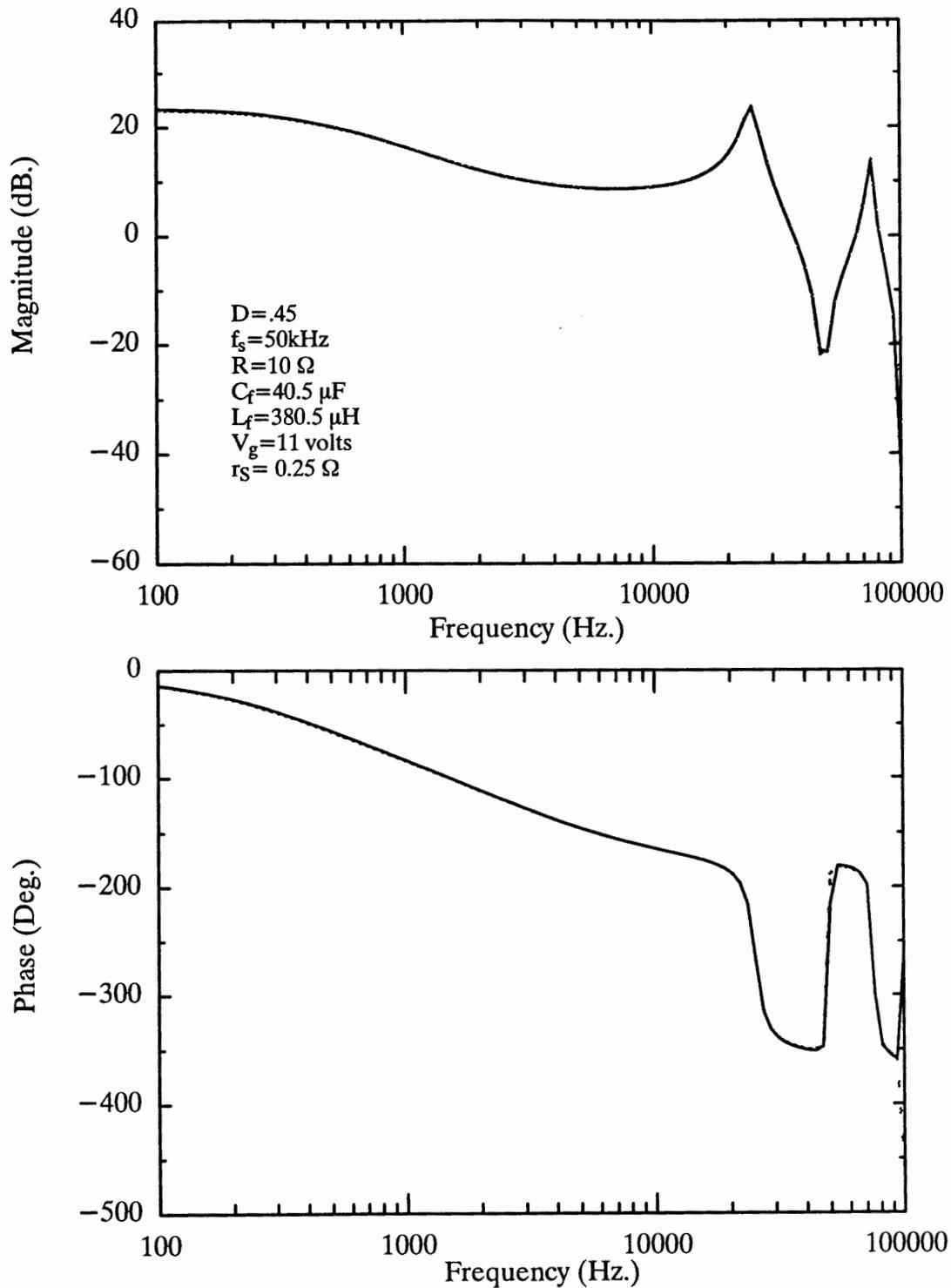


Figure 5 - 31 The control-to-output response of the PWM buckboost converter operating in CCM under current mode programming; exact (solid line) versus the second model (dashed line).

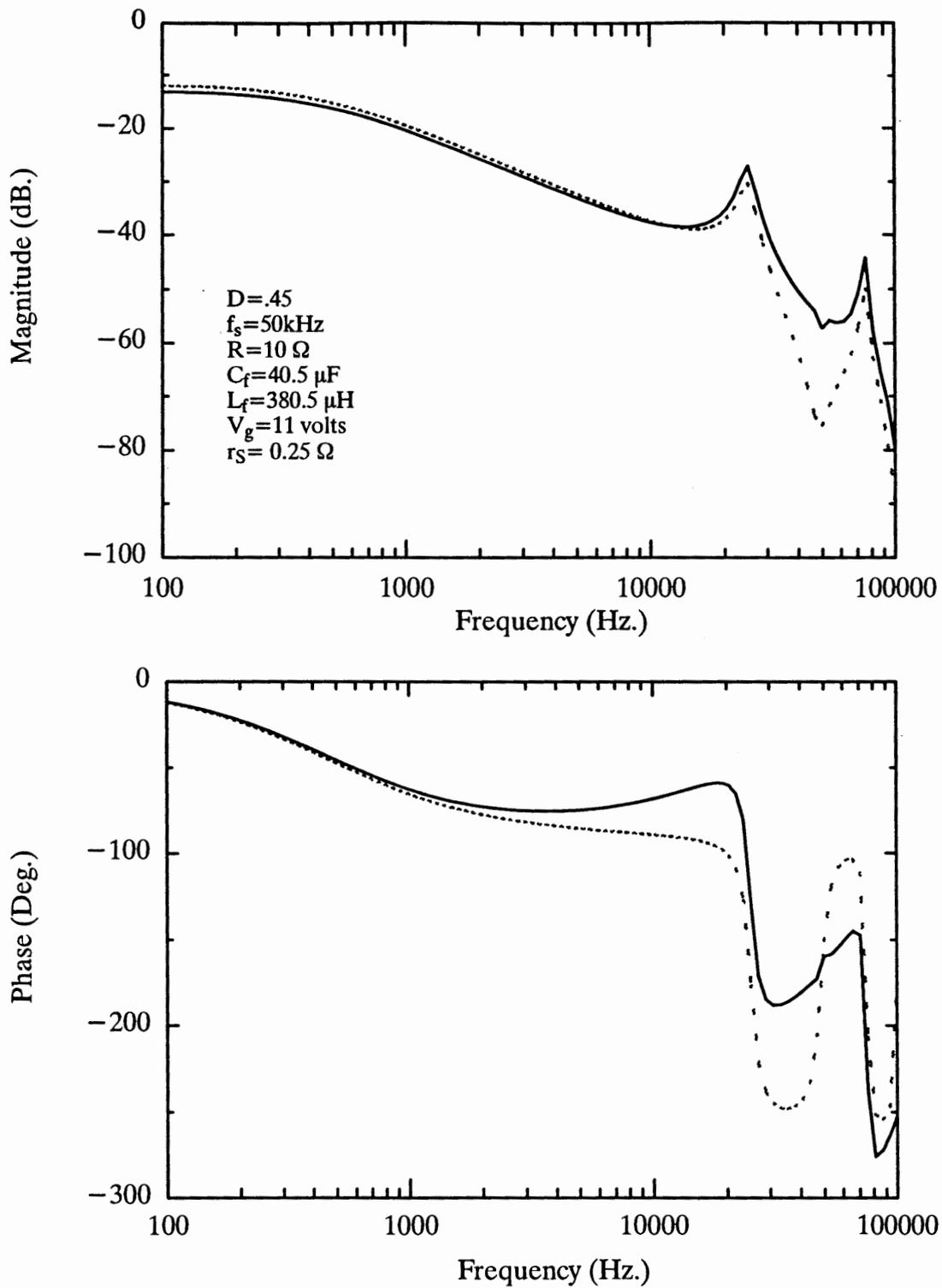


Figure 5 - 32 The input-to-output response of the PWM buck-boost converter operating in CCM under current mode programming; exact (solid line) versus the second model (dashed line).

CHAPTER 6

THIRD MODEL: NEW METHOD FOR OBTAINING INPUT PARAMETERS

6.1 General Analysis of the Model

The model found in the previous chapter failed to accurately predict the input–to–output response of current mode programmed converters. Therefore, a new approach is undertaken which produces exact but more complicated expressions.

Since input to output parameters, h_r , z_i , h_f , and y_o , obtained in the last modeling approach gave inaccurate results, an attempt is made to alter the procedure for finding these parameters.

The controller terms in equations (2 – 41) and (2 – 42) do not contribute to the input to output parameters. Therefore, when finding the input terms, the equations (2 – 41) and (2 – 42) can be found by deactivating the effect of the controller input; i.e.

$$\hat{v}_{ca} \Big|_{\hat{d}=0} = h_r \hat{v}_{pa} + z_i \hat{i}_c \quad (6 - 1)$$

$$\hat{i}_p \Big|_{\hat{d}=0} = h_f \hat{i}_c + y_o \hat{v}_{pa} \quad (6 - 2)$$

In equation (6 – 1), there are two unknown parameters. If one of the parameters is found by nulling, then there will be one equation and one unknown. So it is possible to solve for the second parameter. The question is which parameter should be subjected

to nulling in order to produce accurate results both for the full order and reduced order analysis?

The fact that the number of states is reduced to a single inductor state imposed more restriction on the proposed current programmed model. That was the reason for which the previous model did not produce accurate results. To clarify this, let us look at the h_r parameter.

$$h_r = \frac{\hat{v}_{ca}}{\hat{v}_{pa}} \begin{cases} \hat{d} = 0 \\ \hat{i}_c = 0 \end{cases}$$

The exact input-to-output transfer function given in equation (3 – 52) is used to obtain the h_r parameter. First the inductor current state \hat{i}_l must be nulled. Noting that h^T is the term which isolates the inductor current state, $i_l = h^T x$. Therefore, the terms containing h^T should also be nulled. One of the places in equation (3 – 52) the effect of the sensed current appears in is

$$\xi h^T \mu \beta_1 \beta_2 \quad (6 - 3)$$

When the number of states was reduced, the contribution of the sensed current towards the second state disappeared in expression (6 – 3). To clarify this, consider the full order model with

$$B = \beta_1 \beta_2 \quad (6 - 4)$$

where B is a 2x2 matrix given as:

$$B = \begin{bmatrix} b_{11} & b_{12} \\ b_{21} & b_{22} \end{bmatrix} \quad (6 - 5)$$

h^T is a 1x2 vector containing only R_i which is a current to voltage transformation gain.

$$h^T = [R_i \quad 0] \quad (6 - 6)$$

ζ and μ are scalars, therefore the expression (6 – 3) can be expanded as follows:

$$\zeta\mu [R_i b_{11} \quad R_i b_{12}] \quad (6 - 7)$$

The reduced order model does not contain the following term:

$$\zeta\mu R_i b_{12} \quad (6 - 8)$$

and if \hat{i}_c is nulled in the reduced model, equation (6 – 8) will be reduced to zero.

Therefore, equation (6 – 3) also goes to zero.

In general, when \hat{i}_c is nulled, equation (3 – 52) will reduce to the following:

$$\frac{\hat{y}}{\hat{u}_i}(j\omega) = \frac{1}{T_s} \{E_2(T_3 - T_2)\} \quad (6 - 9)$$

Hence using equation (6 – 9), the h_r parameter reduces to:

$$h_r = \frac{\hat{y}}{\hat{u}_i}(j\omega) = \frac{1}{T_s} \{D'T_s\} = D' \quad (6 - 10)$$

Equation (6 – 9) was used in obtaining two of the input–to–output parameters of the second model. Furthermore, the proposed model did not give accurate results when tested on different converters. Therefore, a new method is tried. Now, the main concern in the reduced order analysis is to avoid nulling the inductor current state. Hence, the other parameter in equation (6 – 1), namely z_i , will be subjected to nullification.

The z_i parameter is given in expression (2 – 47) which is also repeated here for reference:

$$z_i = \frac{\hat{v}_{ca}}{\hat{i}_c} \left| \begin{array}{l} \hat{d} = 0 \\ \hat{v}_{pa} = 0 \end{array} \right.$$

Since both the numerator and the denominator of the z_i parameter are outputs, expression (2 – 47) can be written in the following form:

$$z_i = \frac{\hat{v}_{ca}/\hat{v}_g}{\hat{i}_c/\hat{v}_g} \left| \begin{array}{l} \hat{d} = 0 \\ \hat{v}_{pa} = 0 \end{array} \right. \quad (6 - 11)$$

After finding the z_i parameter by deactivating the effect of \hat{v}_{pa} , the h_r parameter can be found using the following method:

Dividing equation (6 – 1) by \hat{v}_{pa} will give:

$$\frac{\hat{v}_{ca}}{\hat{v}_{pa}} \Big|_{\hat{d}=0} = h_r + z_i \frac{\hat{i}_c}{\hat{v}_{pa}} \Big|_{\hat{d}=0} \quad (6 - 12)$$

Rearranging equation (6 – 12) results in:

$$h_r = \frac{\hat{v}_{ca}}{\hat{v}_{pa}} \Big|_{\hat{d}=0} - z_i \frac{\hat{i}_c}{\hat{v}_{pa}} \Big|_{\hat{d}=0} \quad (6 - 13)$$

The same procedure is applied to equation (6 – 2). The h_f parameter is obtained first by nulling \hat{v}_{pa} . Then dividing equation (6 – 2) by \hat{v}_{pa} and rearranging the terms gives:

$$y_o = \frac{\hat{i}_p}{\hat{v}_{pa}} \Big|_{\hat{d}=0} - h_f \frac{\hat{i}_c}{\hat{v}_{pa}} \Big|_{\hat{d}=0} \quad (6 - 14)$$

The controller parameters are then found by substituting equation (6 – 13) into equation (5 – 1) and substituting equation (6 – 14) into equation (5 – 2). Therefore, the new controller parameter v_{id} is:

$$v_{id} = \frac{\hat{v}_{ca}}{\hat{d}} - \left[\frac{\hat{v}_{ca}}{\hat{v}_{pa}} \Big|_{\hat{d}=0} - \left(\frac{\hat{v}_{ca}}{\hat{i}_c} \Big|_{\hat{d}, \hat{v}_{pa}=0} \frac{\hat{i}_c}{\hat{v}_{pa}} \Big|_{\hat{d}=0} \right) \right] \frac{\hat{v}_{pa}}{\hat{d}} - \frac{\hat{v}_{ca}}{\hat{i}_c} \Big|_{\hat{d}, \hat{v}_{pa}=0} \frac{\hat{i}_c}{\hat{d}} \quad (6 - 15)$$

and i_{od} is given by

$$i_{od} = \frac{\hat{i}_p}{\hat{d}} - \left[\frac{\hat{i}_p}{\hat{v}_{pa}} \Big|_{\hat{d}=0} - \left(\frac{\hat{i}_p}{\hat{i}_c} \Big|_{\hat{d}, \hat{v}_{pa}=0} \frac{\hat{i}_c}{\hat{v}_{pa}} \Big|_{\hat{d}=0} \right) \right] \frac{\hat{v}_{pa}}{\hat{d}} - \frac{\hat{i}_p}{\hat{i}_c} \Big|_{\hat{d}, \hat{v}_{pa}=0} \frac{\hat{i}_c}{\hat{d}} \quad (6-16)$$

Equations (6-15) and (6-16) have been derived under one important constraint. Namely, the input to output parameters are obtained by using a different condition than the control to output parameters. Under this assumption, equations (6-15) and (6-16) will be valid.

Equations (6-13) and (6-14) can be written in a simpler manner.

$$h_r = h_r' - z_i y_r \quad (6-17)$$

$$y_o = y_o' - h_f y_r \quad (6-18)$$

where

$$h_r' = \frac{\hat{v}_{ca}}{\hat{v}_{pa}} \Big|_{\hat{d}=0} \quad (6-19)$$

$$y_o' = \frac{\hat{i}_p}{\hat{v}_{pa}} \Big|_{\hat{d}=0} \quad (6-20)$$

$$y_r = \frac{\hat{i}_c}{\hat{v}_{pa}} \Big|_{\hat{d}=0} \quad (6-21)$$

The controller equations (6-15) and (6-16) can be given in the form of equations (5-3) and (5-4) which are:

$$v_{id} = v_{id}' - h_r v_{od} - z_i i_{id}$$

$$i_{od} = i_{od}' - y_o v_{od} - h_f i_{id}$$

The next step in the development of the new model is to reduce the order of the system, so expressions will be easier to derive. The same assumption made in the last modeling approach are used here as well. It is assumed that the capacitor has a negligible voltage ripple, therefore, it is replaced with a constant voltage source.

6.1.1 PWM Duty Ratio Programmed Model

First, the full order PWM duty ratio boost model is examined. It is shown in Figure 6 – 1, and Figure 6 – 2 that the control to output and input to output frequency responses of this model perfectly match the exact results. Next, the reduced order model will be examined.

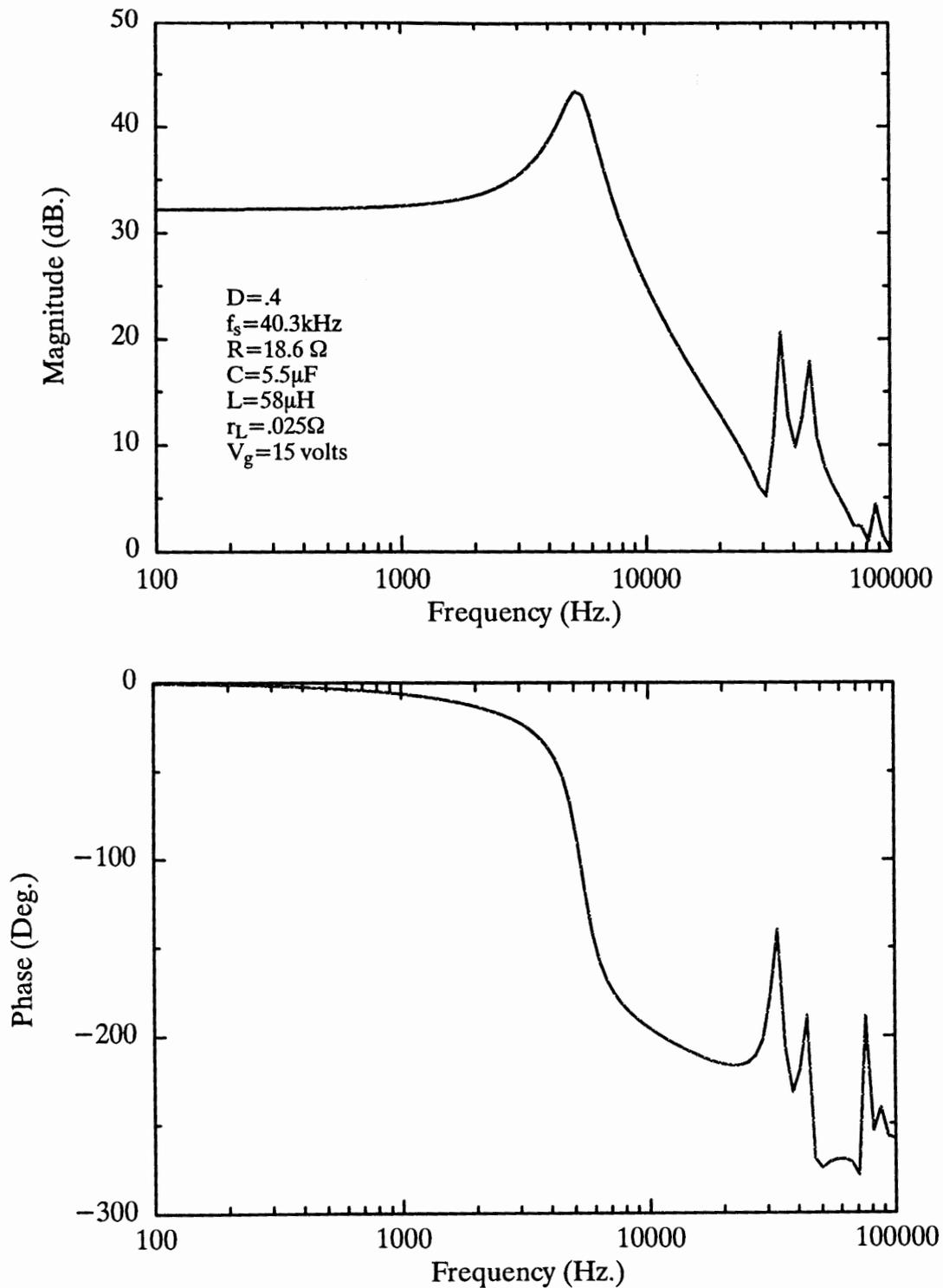


Figure 6 - 1 The control-to-output response of the PWM boost converter operating in CCM; exact (solid line) versus the third model (dashed line).

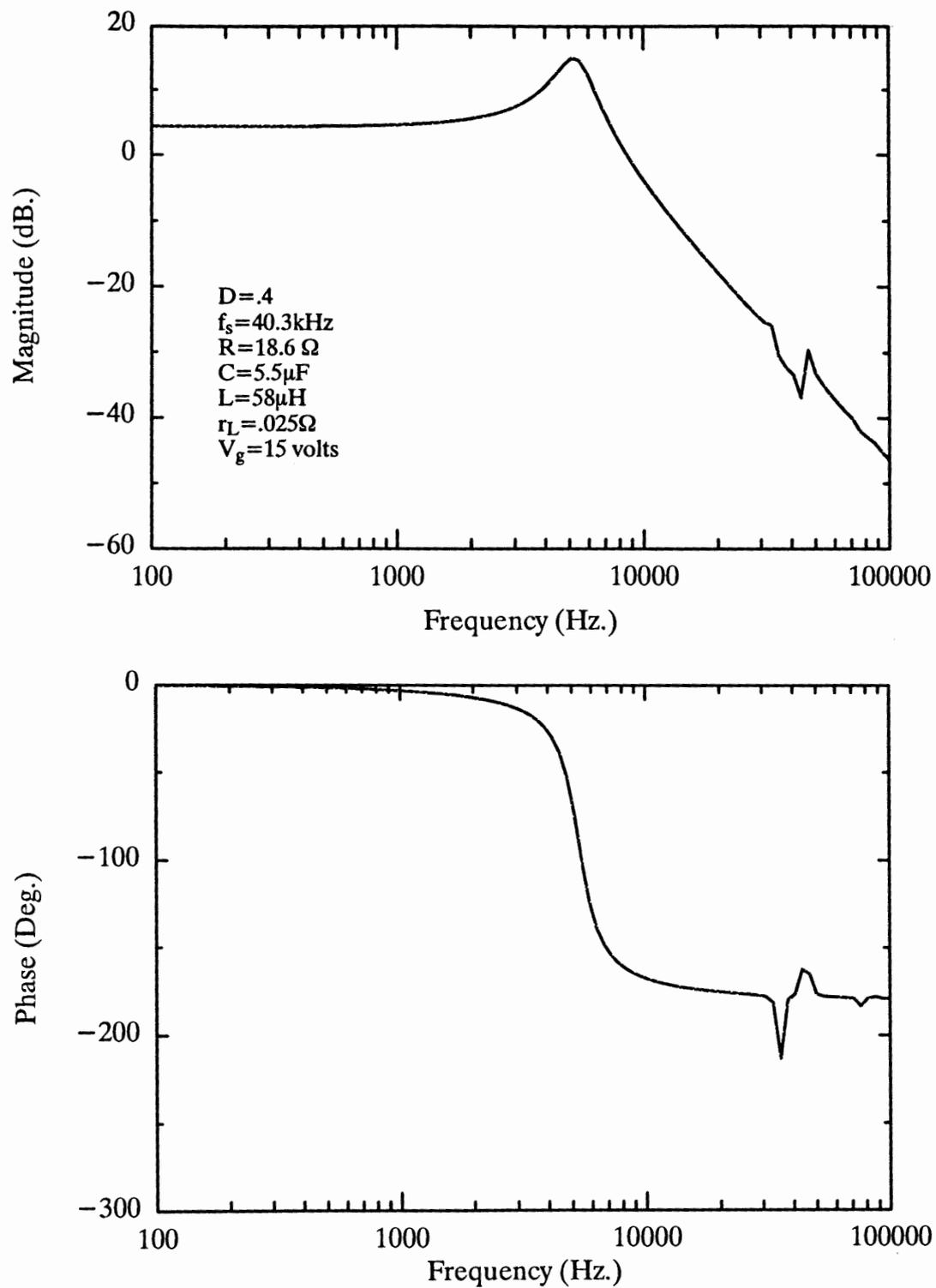


Figure 6 - 2 The input-to-output response of the PWM boost converter operating in CCM; exact (solid line) versus the third model (dashed line).

The control to output frequency response of the reduced PWM duty ratio boost model is plotted in Figure 6 – 4. The component values are chosen to produce high ripple. The capacitor voltage ripple is around 4.85% of the DC voltage level. The inductor current ripple is chosen to be around 37.2% of the DC inductor current level. The low frequency magnitude of the model is shifted below the exact results by less than 1 dB. At high frequencies, it is exact up to half of the switching frequency. The control to output phase plot is very accurate up to $f_s/2$. The input–to–output frequency response of the new reduced model is plotted in Figure 6 – 5. As it can be observed, the magnitude and phase plots are exact everywhere except at the switching frequency.

In equation (6 – 11), the line input, \hat{v}_g , to output transfer functions are used. This means only the \hat{v}_g column of the input and the transmission matrices will be used for this analysis.

Next, the analytical solution for the parameters are found using Maple V. v_{id}' , z_i , i_{od}' , and h_f parameters are obtained using the same technique as in the previous approach. Hence, they will not be repeated here. The new expressions are:

$$h_r' = D' \quad (6 - 22)$$

$$y_{o'} = -\frac{(1 - e^{-j\omega D'T_s})(1 - e^{-j\omega DT_s})}{\omega^2 T_s L(1 - e^{-j\omega T_s})} + j\frac{D'}{\omega L} \quad (6 - 23)$$

$$y_r = j\frac{D'}{\omega L} \quad (6 - 24)$$

Using equation (6 – 17) one gets:

$$h_r = D' \quad (6 - 25)$$

and similarly the y_o parameter is found by using equation (6 – 18)

$$y_o = -\frac{(1 - e^{-j\omega D'T_s})(1 - e^{-j\omega DT_s})}{\omega^2 T_s L(1 - e^{-j\omega T_s})} + j\frac{DD'}{\omega L} \quad (6 - 26)$$

The new controller parameters, v_{id} and i_{od} , are the same as those given in the previous modeling approach.

Since z_i is zero, the new duty ratio hybrid parameter switch model reduces to the one shown in Figure 6 – 3.

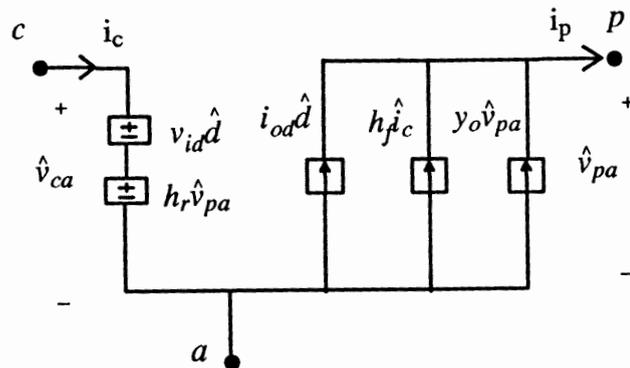


Figure 6 – 3 The hybrid parameter switch model for PWM duty ratio programming.

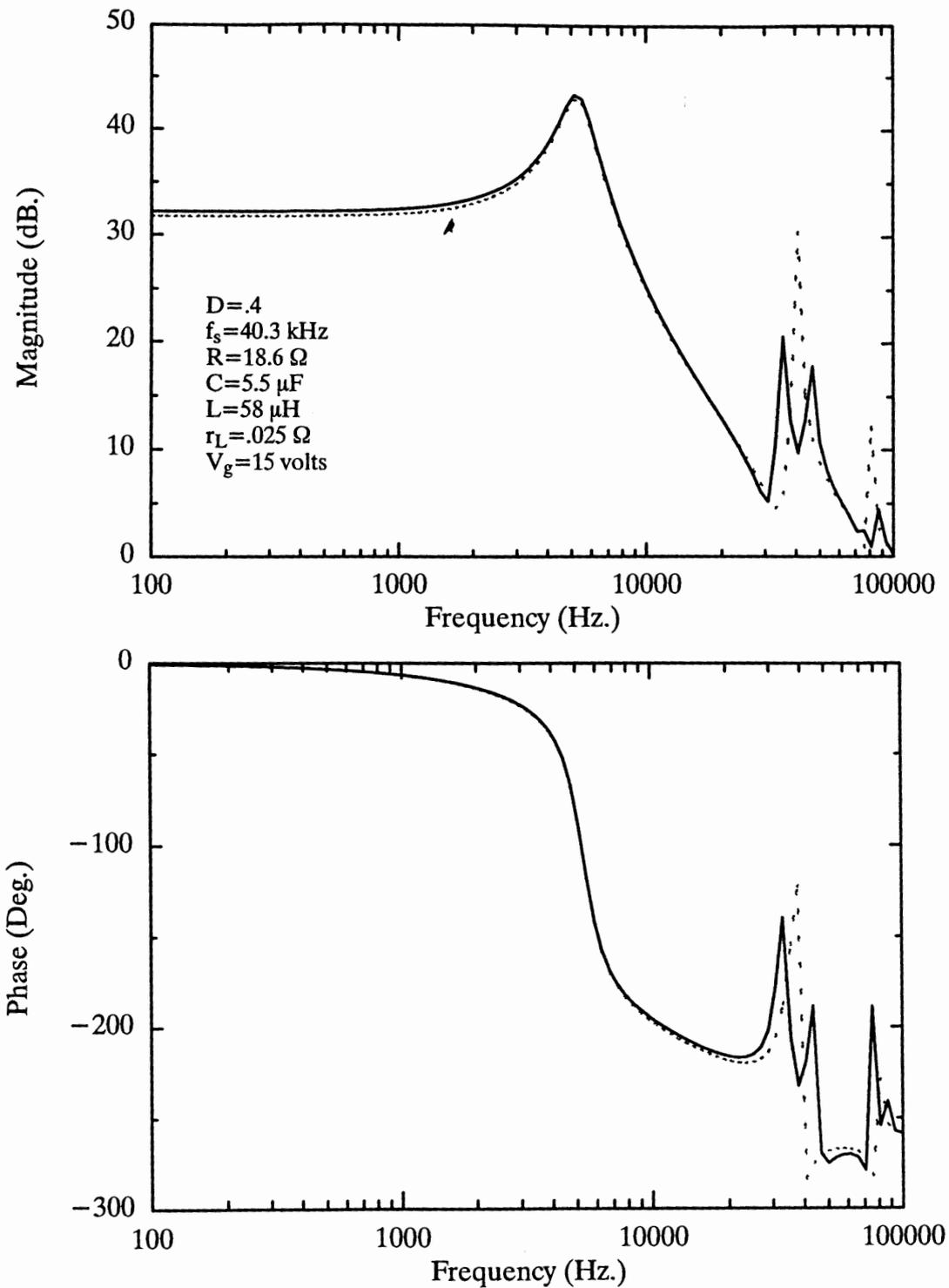


Figure 6 - 4 The control-to-output response of the PWM boost converter operating in CCM; exact (solid line) versus the reduced order third model (dashed line).

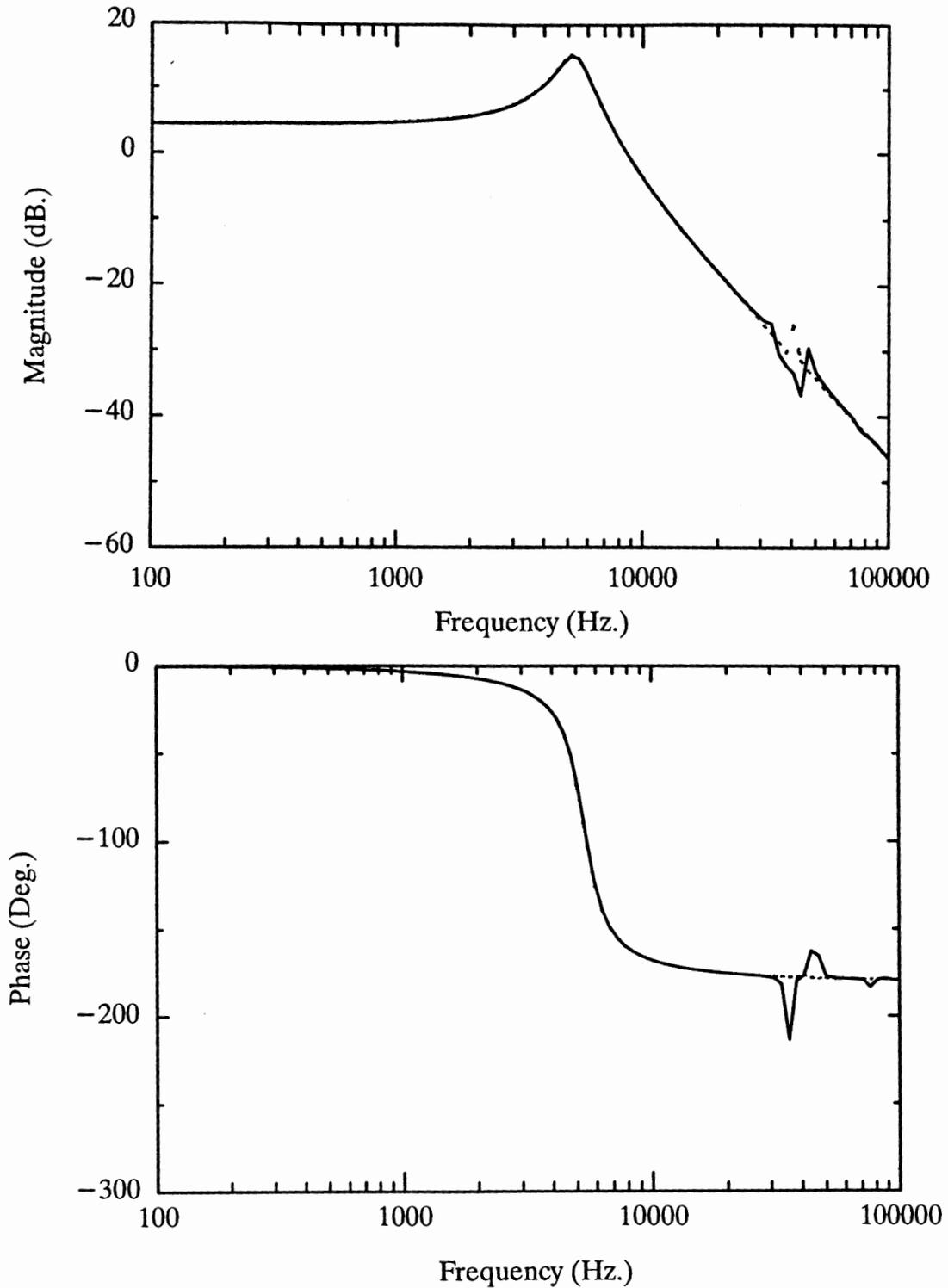


Figure 6 - 5 The input-to-output response of the PWM boost converter operating in CCM; exact (solid line) versus the reduced third model (dashed line).

6.1.2 PWM Current Programmed Model

In section 6.1, a comprehensive hybrid parameter switch model was proposed. In this section the model will be examined for current programming case. The control-to-output magnitude and phase responses of the new model are shown in Figure 6 – 6. The exact results are plotted on the same graph. The agreement is excellent. The input-to-output responses of the new model match that of the exact results as shown in Figure 6 – 7.

The next step is to reduce the number of states in order to obtain simplified expressions. This is done by replacing the capacitor by a constant voltage source. The control to output and input to output transfer functions of the reduced model are plotted next.

The control to output transfer function of the new (third) reduced model is shown by the dashed line in Figure 6 – 8. The theoretical prediction of the exact small signal control to output transfer function given by equation (3 – 51) is shown by the solid line. A comparison of the plots shows an excellent agreement up to half of the switching frequency.

The input to output response is shown in Figure 6 – 9. The accuracy of the third model is shown to be very good. The magnitude plot follows the exact plot extremely well up to the switching frequency. The phase plot is also very accurate up to at least half the switching frequency.

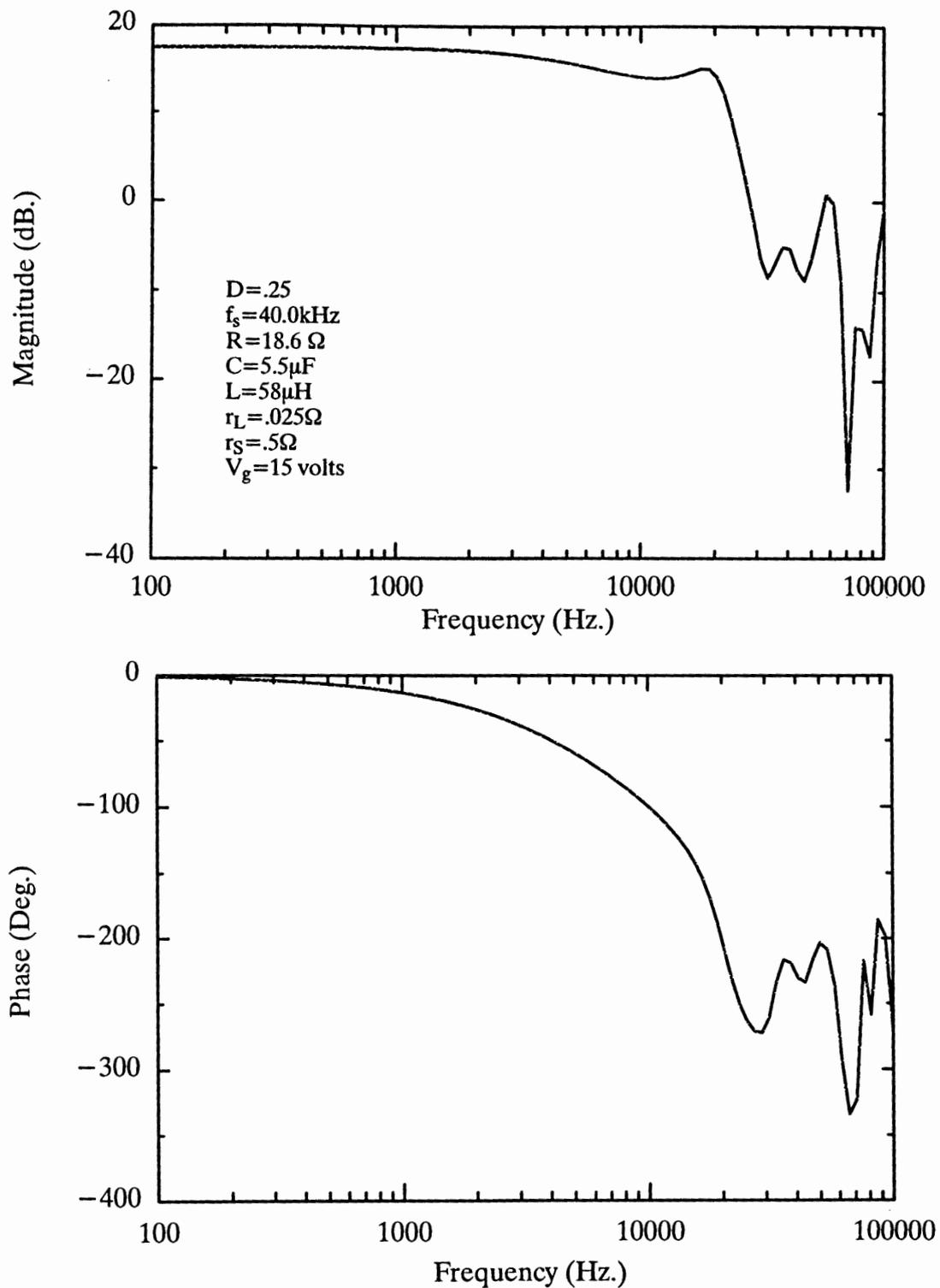


Figure 6 – 6 The control-to-output response of the PWM boost converter operating in CCM under current mode programming; exact (solid line) versus the third model (dashed line).

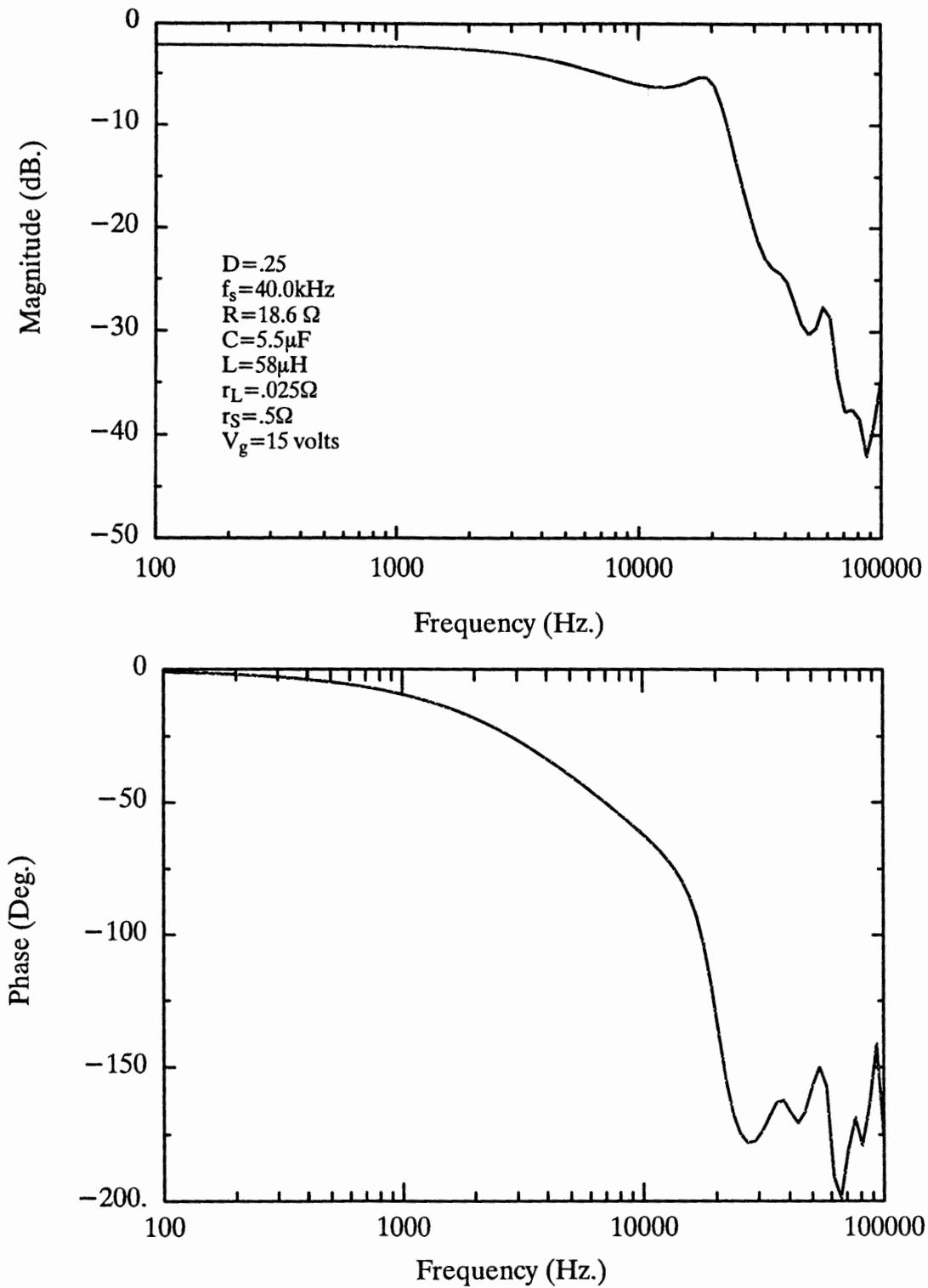


Figure 6 – 7 The input-to-output response of the PWM boost converter operating in CCM under current mode programming ; exact (solid line) versus the third model (dashed line).

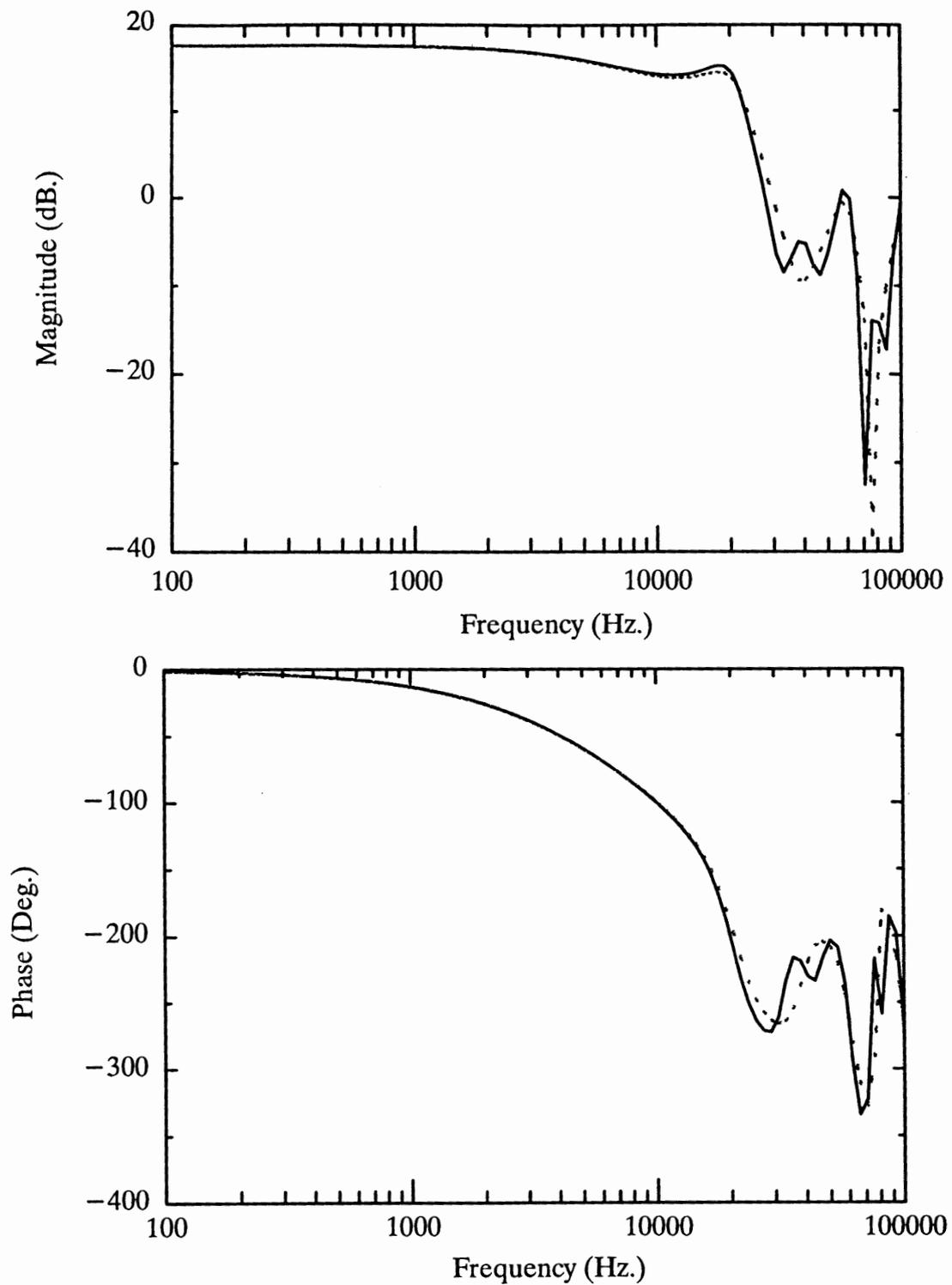


Figure 6 - 8 The control-to-output response of the PWM boost converter operating in CCM under current mode programming. Exact (solid line) versus the reduced order third model (dashed line).

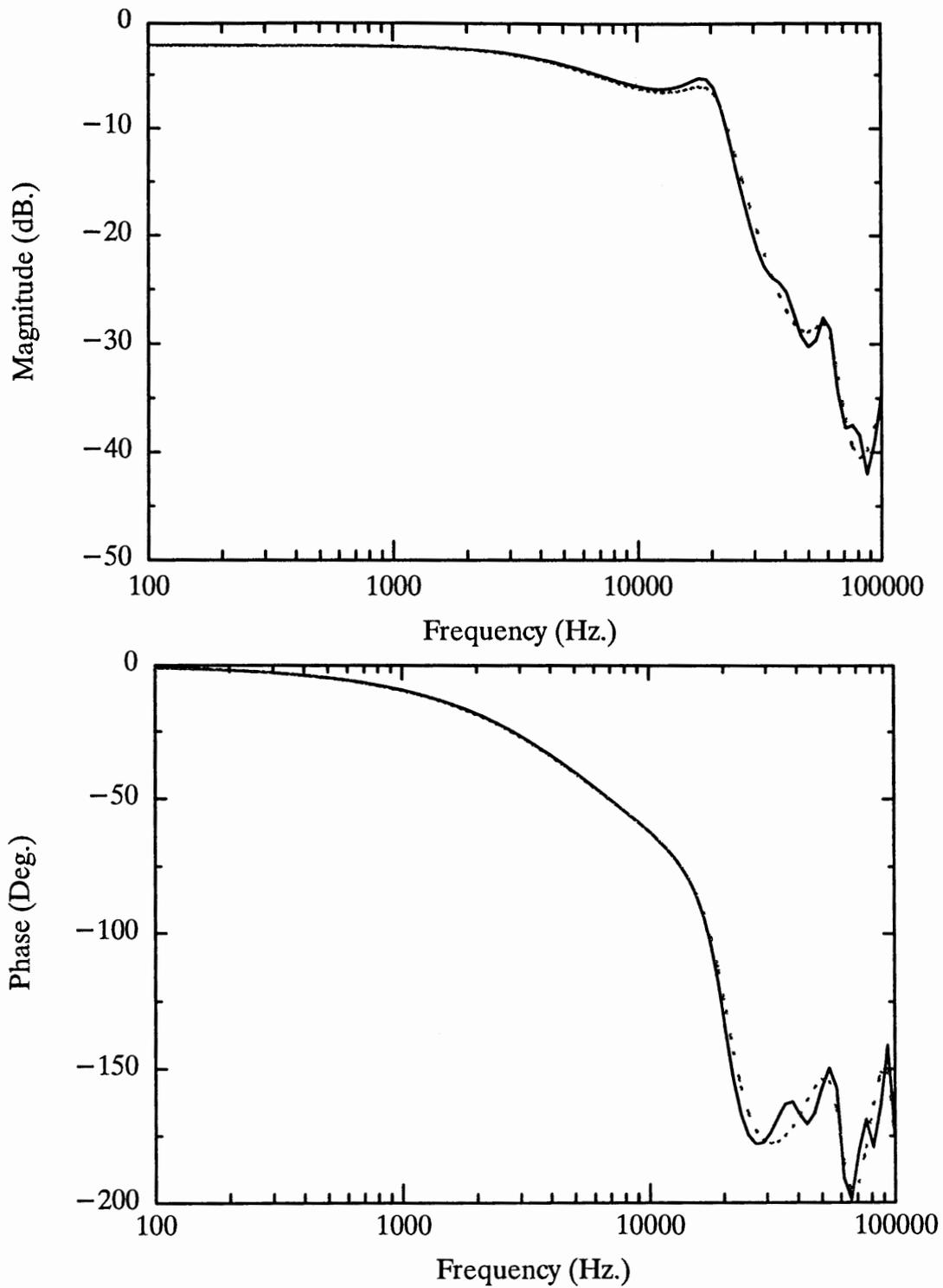


Figure 6 – 9 The input-to-output response of the PWM boost converter operating in CCM under current mode programming. Exact (solid line) versus the reduced order third model (dashed line).

The expressions for the current mode parameters are obtained using Maple V. As mentioned before, the new parameters are lengthy. Whenever possible, they are reduced by employing some algebraic manipulations and simplifications. Again v_{id}' , z_i , i_{od}' , and h_f parameters are obtained using the same technique as in the previous approach and are not repeated here. The following is a list of parameters that are different from the expressions obtained using the previous modeling technique:

$$h_r' = \frac{j\mu V_{pa} h^T}{T_s \omega L} \frac{e^{-j\omega D T_s} (1 - e^{-j\omega D' T_s})}{(1 - \Gamma e^{-j\omega T_s})} + D' \quad (6 - 27)$$

$$y_o' = \frac{(1 - j\omega D' T_s)(1 + \Gamma e^{-j\omega T_s}) - (1 + \Gamma)e^{-j\omega D' T_s}}{\omega^2 L T_s (1 - \Gamma e^{-j\omega T_s})} - \frac{j h^T I_c (e^{-j\omega D T_s} - e^{-j\omega T_s})}{\omega L T_s (1 - \Gamma e^{-j\omega T_s})} \quad (6 - 28)$$

$$y_r = \frac{(\Gamma - 1) (e^{-j\omega D T_s} - e^{-j\omega T_s})}{\omega^2 L T_s (1 - \Gamma e^{-j\omega T_s})} + j \frac{D'}{\omega L} \quad (6 - 29)$$

Then equation (6 - 17) is used to find the new h_r parameter:

$$h_r = \left(\frac{j\mu V_{pa} h^T}{T_s \omega L} \frac{e^{-j\omega D T_s} (1 - e^{-j\omega D' T_s})}{(1 - \Gamma e^{-j\omega T_s})} + D' \right) - \left\{ \left(\frac{-j\omega V_{pa} \mu h^T (1 - e^{-j\omega T_s})}{1 - \Gamma - j\omega T_s - (1 - \Gamma - j\omega T_s \Gamma) e^{-j\omega T_s}} \right) \cdot \left(\frac{(\Gamma - 1) (e^{-j\omega D T_s} - e^{-j\omega T_s})}{\omega^2 L T_s (1 - \Gamma e^{-j\omega T_s})} + j \frac{D'}{\omega L} \right) \right\} \quad (6 - 30)$$

The y_o parameter is found using equation (6 - 18)

$$\begin{aligned}
y_o = & \left(\frac{(1 - j\omega D'T_s)(1 + \Gamma e^{-j\omega T_s}) - (1 + \Gamma)e^{-j\omega D'T_s}}{\omega^2 L T_s (1 - \Gamma e^{-j\omega T_s})} - \frac{j h^T I_c (e^{-j\omega D T_s} - e^{-j\omega T_s})}{\omega L T_s (1 - \Gamma e^{-j\omega T_s})} \right) \\
& - \left\{ \left(\frac{1 - j\omega D'T_s - e^{-j\omega D'T_s} - \Gamma(1 - e^{-j\omega D'T_s} - j\omega D'T_s e^{-j\omega T_s})}{1 - \Gamma - j\omega T_s - (1 - \Gamma - j\omega T_s \Gamma)e^{-j\omega T_s}} \right. \right. \\
& \quad \left. \left. + \frac{j\omega \mu h^T I_c (1 - e^{-j\omega T_s})}{1 - \Gamma - j\omega T_s - (1 - \Gamma - j\omega T_s \Gamma)e^{-j\omega T_s}} \right) \right. \\
& \quad \left. \left(\frac{(1 - \Gamma)(e^{-j\omega D T_s} - e^{-j\omega T_s})}{\omega^2 L T_s (1 - \Gamma e^{-j\omega T_s})} + j \frac{D'}{\omega L} \right) \right\} \quad (6 - 31)
\end{aligned}$$

Next equations (5 - 3) and (5 - 4) will be used to find the new v_{id} and i_{od} parameters.

$$\begin{aligned}
v_{id} = & - \frac{V_{pa}(1 - e^{-j\omega T_s})}{(1 - \Gamma e^{-j\omega T_s})} - \left\{ \left(\frac{-j\omega V_{pa} \mu h^T (1 - e^{-j\omega T_s})}{1 - \Gamma - j\omega T_s - (1 - \Gamma - j\omega T_s \Gamma)e^{-j\omega T_s}} \right) \right. \\
& \quad \left. \left(- \frac{jV_{pa}}{\omega L} \frac{(1 - e^{-j\omega T_s})}{(1 - \Gamma e^{-j\omega T_s})} \right) \right\} \quad (6 - 32)
\end{aligned}$$

$$\begin{aligned}
i_{od} = & - \left(\frac{I_c(1 - e^{-j\omega T_s})}{(1 - \Gamma e^{-j\omega T_s})} + \frac{jV_{pa}(1 - e^{-j\omega D'T_s})}{\omega L(1 - \Gamma e^{-j\omega T_s})} \right) - \\
& \left\{ \left(\frac{1 - j\omega D'T_s - e^{-j\omega D'T_s} - \Gamma(1 - e^{-j\omega D'T_s} - j\omega D'T_s e^{-j\omega T_s})}{1 - \Gamma - j\omega T_s - (1 - \Gamma - j\omega T_s \Gamma)e^{-j\omega T_s}} \right. \right. \\
& \quad \left. \left. + \frac{j\omega \mu h^T I_c (1 - e^{-j\omega T_s})}{1 - \Gamma - j\omega T_s - (1 - \Gamma - j\omega T_s \Gamma)e^{-j\omega T_s}} \right) \right. \\
& \quad \left. \left(- \frac{jV_{pa}}{\omega L} \frac{(1 - e^{-j\omega T_s})}{(1 - \Gamma e^{-j\omega T_s})} \right) \right\} \quad (6 - 33)
\end{aligned}$$

Equations (6 - 30), (6 - 31), (6 - 32), and (6 - 33) were given so one could get a feel for how complex the new parameters have become. For simulation purposes,

their compact forms which are given by equations (6 – 17), (6 – 18), (5 – 38), and (5 – 39) will be used.

The new hybrid parameter switch model for the current mode programming could be represented by Figure 6 – 10.

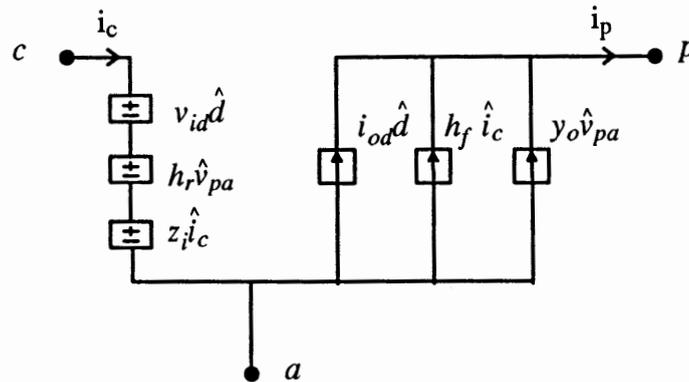


Figure 6 – 10 Hybrid parameter switch model for current mode programming.

It can be shown that the current programmed switch model can be used to analyze the PWM duty ratio programmed circuits. This is easily done by setting the h^T of the current programmed model to zero.

6.2 Buck Converter Analysis

In this section, the proposed hybrid parameter switch model is used to draw equivalent small signal model for the buck converter in a) duty ratio programmed; b) current programmed. Next, the accuracy of these models are examined by plotting the frequency responses of each model versus that of the exact results.

6.2.1 PWM Duty Ratio Programmed Buck Converter

The new hybrid parameter switch model can be used to construct an equivalent small signal circuit model for the buck converter of Figure 5 – 13. This model is shown in Figure 6 – 11. The analysis of the new model is essentially the same as that of the second model. The only difference is the addition of the v_{pa} dependent current source appearing as $y_o \hat{v}_{pa}$.

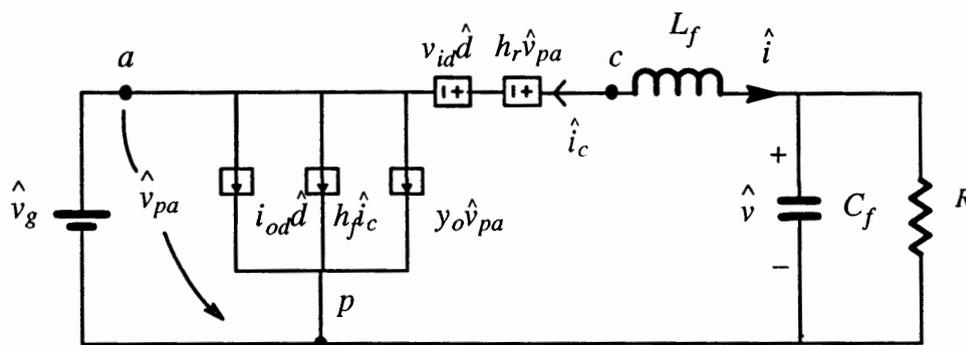


Figure 6 – 11 Hybrid parameter switch model for small-signal analysis of the buck converter of Figure 5 – 13.

The state equation of Figure 6 – 11 is found to be:

$$\frac{d}{dt} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} = \begin{bmatrix} 0 & -\frac{1}{L_f} \\ \frac{1}{C_f} & -\frac{1}{RC_f} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} \frac{1-h_r}{L_f} \\ 0 \end{bmatrix} \hat{v}_g + \begin{bmatrix} \frac{v_{id}}{L_f} \\ 0 \end{bmatrix} \hat{d} \quad (6 - 34)$$

The control-to-output and input-to-output frequency responses of the hybrid model for the buck converter are plotted against the exact results as shown in Figure 6 – 12 and Figure 6 – 13, respectively. There are no discrepancies in the results over the an entire range of frequencies.

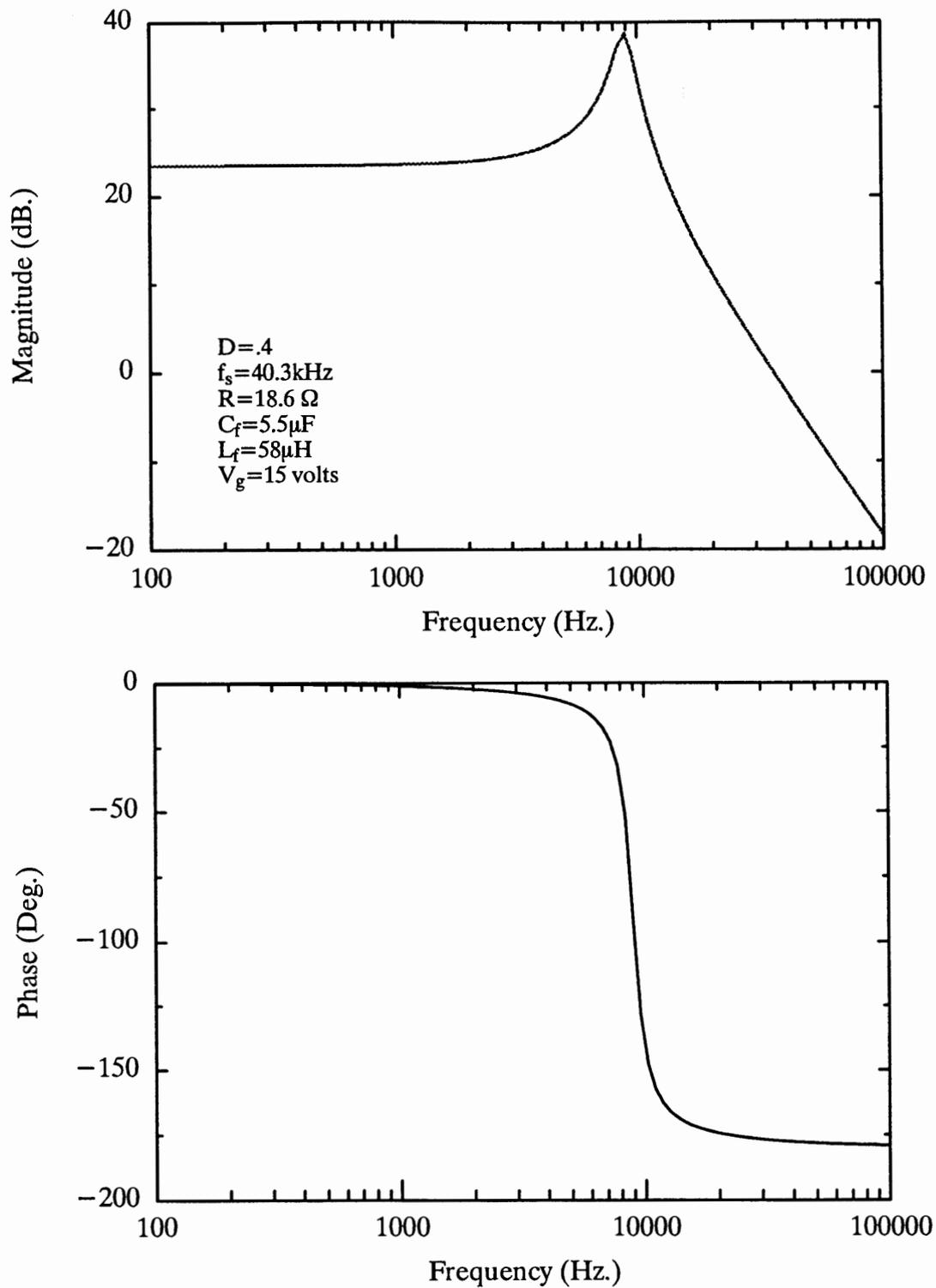


Figure 6 – 12 The control-to-output response of the PWM buck converter operating in CCM; exact (solid line) versus the third model (dashed line).

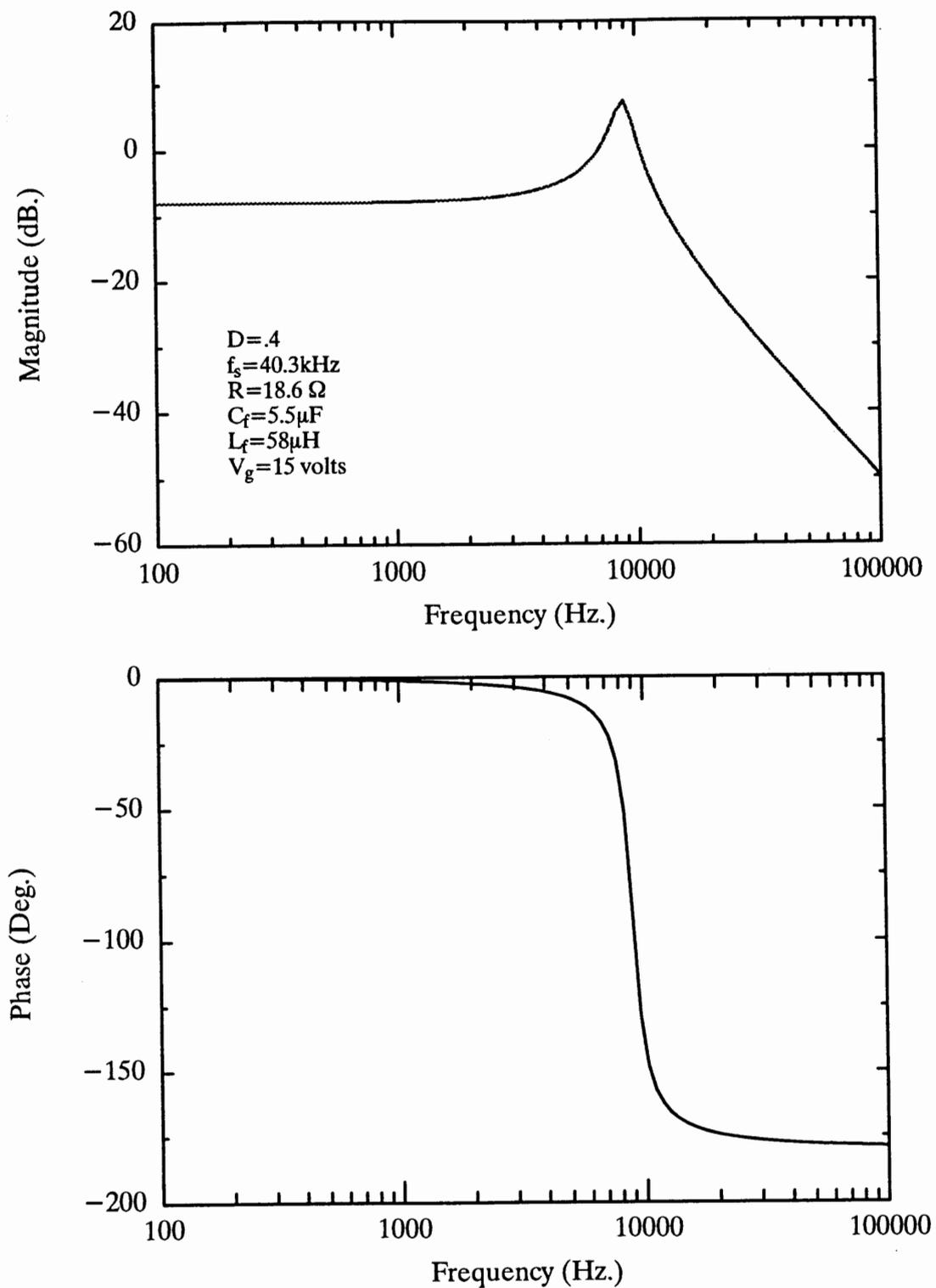


Figure 6 - 13 The input-to-output response of the PWM buck converter operating in CCM ; exact (solid line) versus the third model (dashed line).

6.2.2 PWM Current Programmed Buck Converter

Figure 6 – 14 shows the equivalent hybrid model for the buck converter under current mode control.

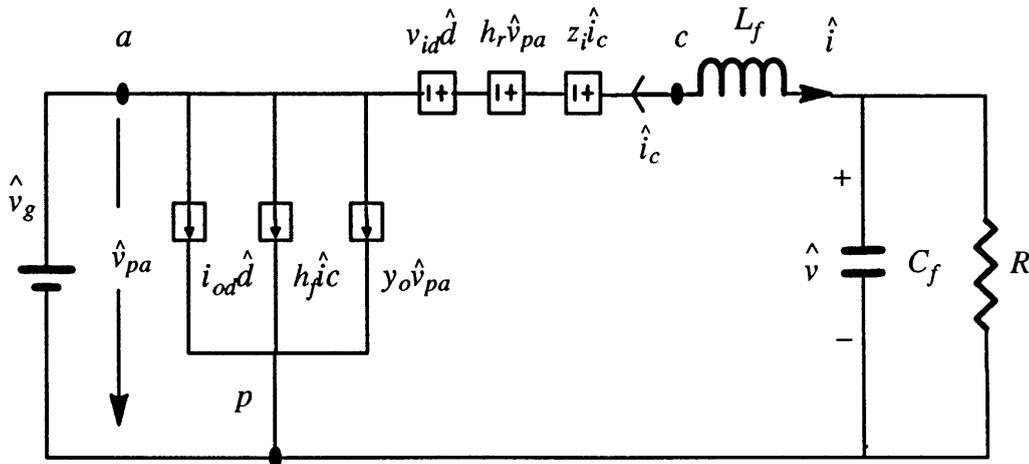


Figure 6 – 14 Hybrid parameter switch model for small–signal analysis of the Current programmed buck converter of Figure 5 – 19.

The small signal state equation of the above current programmed model is:

$$\frac{d}{dt} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} = \begin{bmatrix} -\frac{z_i}{L_f} & -\frac{1}{L_f} \\ \frac{1}{C_f} & -\frac{1}{RC_f} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} \frac{1-h_r}{L_f} \\ 0 \end{bmatrix} \hat{v}_g + \begin{bmatrix} \frac{v_{id}}{L_f} \\ 0 \end{bmatrix} \hat{d} \quad (6-35)$$

Using equation (6 – 35), the input–to–output and control–to–output transfer functions can be found. Next, the magnitude and phase plots of these transfer functions are compared against the exact model plots.

As shown in Figure 6 – 15, the control–to–output magnitude and phase plots of the new model accurately predict the exact small–signal results except at the switching frequency f_s and its first harmonic. The input–to–output frequency response of the model is exact as shown in Figure 6 – 16. This is an excellent improvement over the previous model.

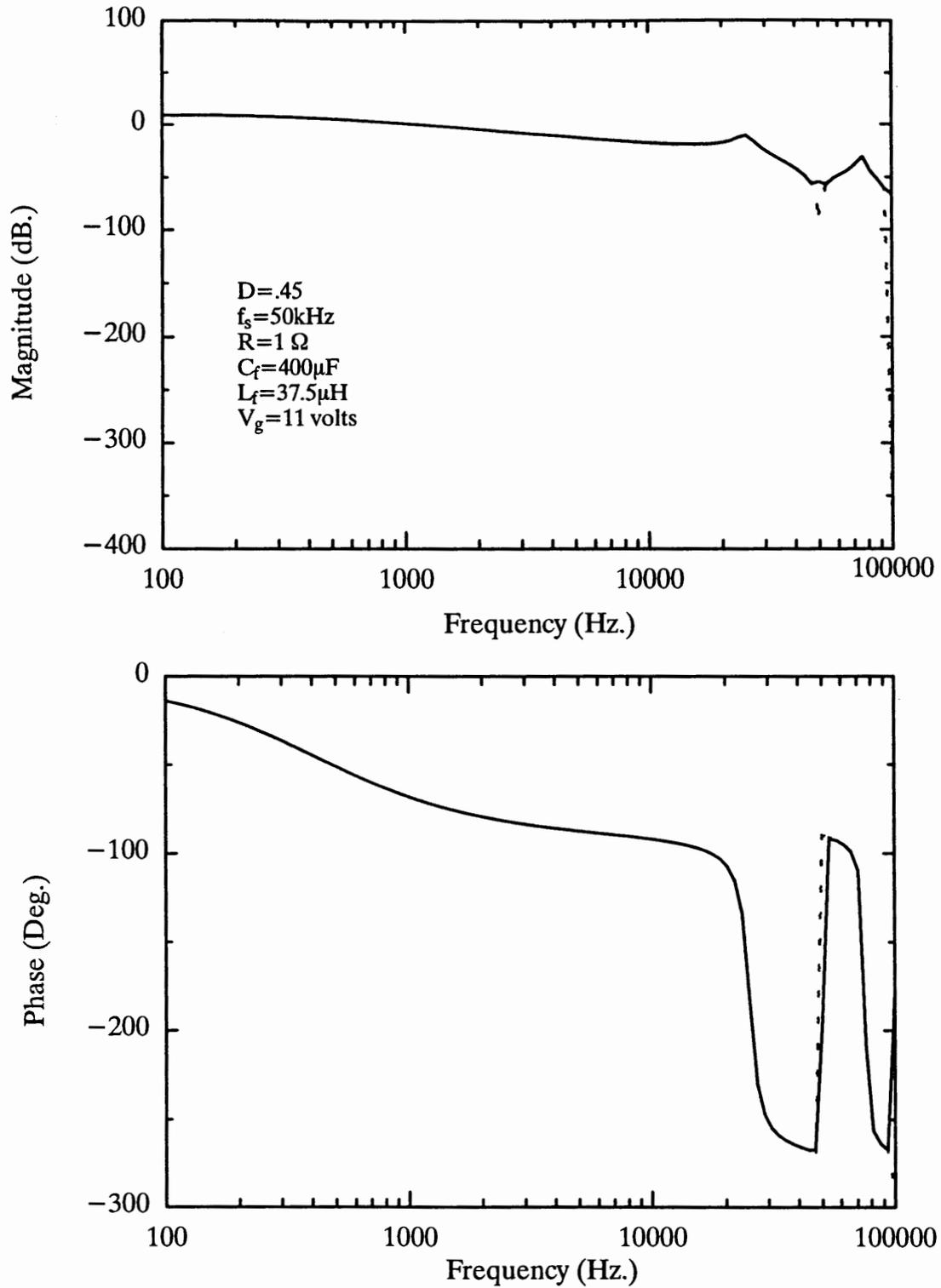


Figure 6 – 15 The control to output response of the PWM buck converter operating in CCM under current mode programming; exact (solid line) versus the third model (dashed line).

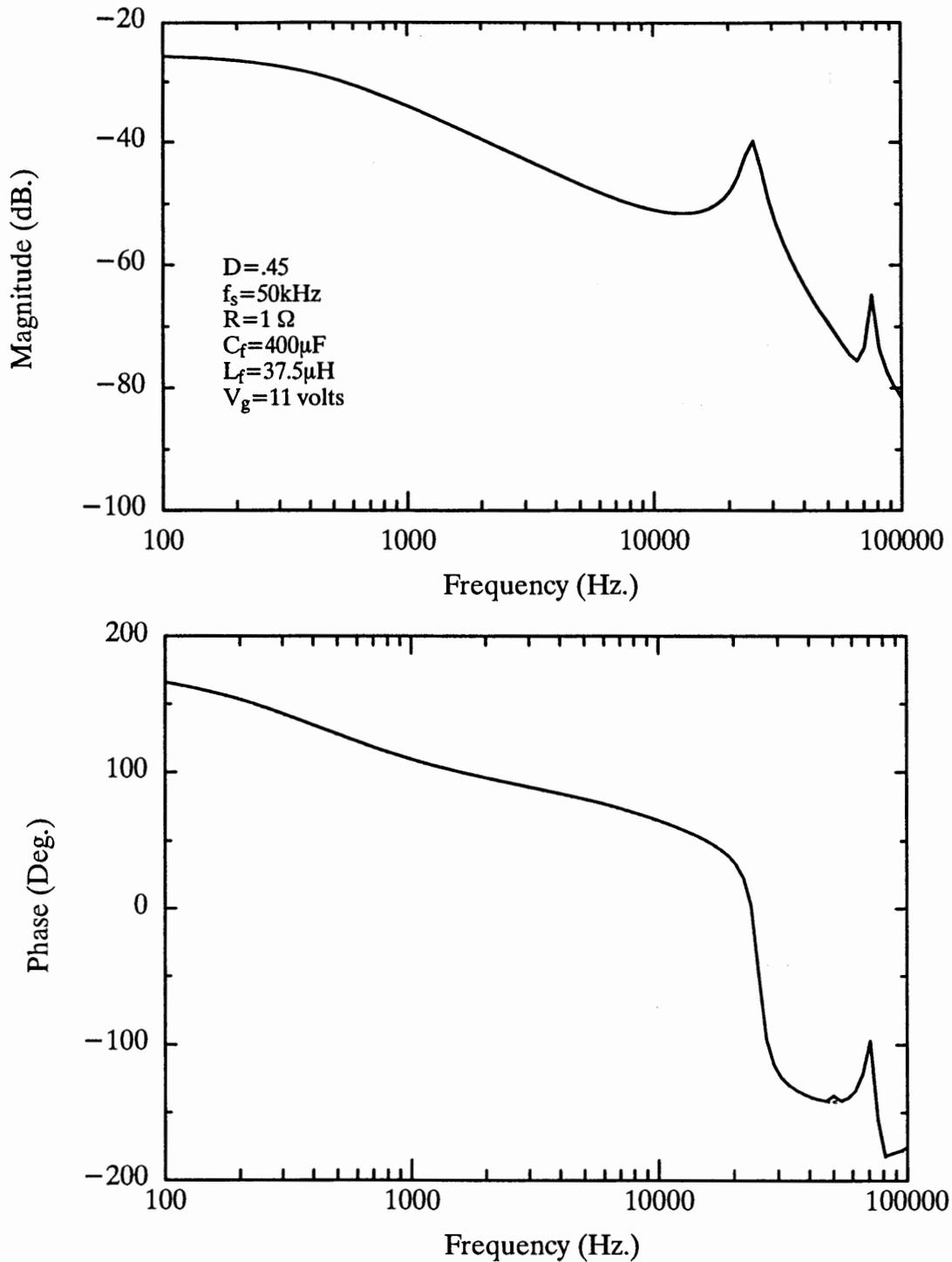


Figure 6 - 16 The input-to-output response of the PWM buck converter operating in CCM under current mode programming; exact (solid line) versus the third model (dashed line).

6.3 Buckboost Converter Analysis

In this section, the new hybrid parameter switch model is used to perform small signal analysis of a basic buckboost converter. The objective is to examine the accuracy of the model. Next, the frequency response of the equivalent buckboost model is compared with that of the exact method.

6.3.1 PWM Duty Ratio Programmed Buckboost Converter

The equivalent small signal model of the buckboost converter of Figure 5 – 23 is shown in Figure 6 – 17. The state equation of this model can be found using analysis presented in section 5.3.1.

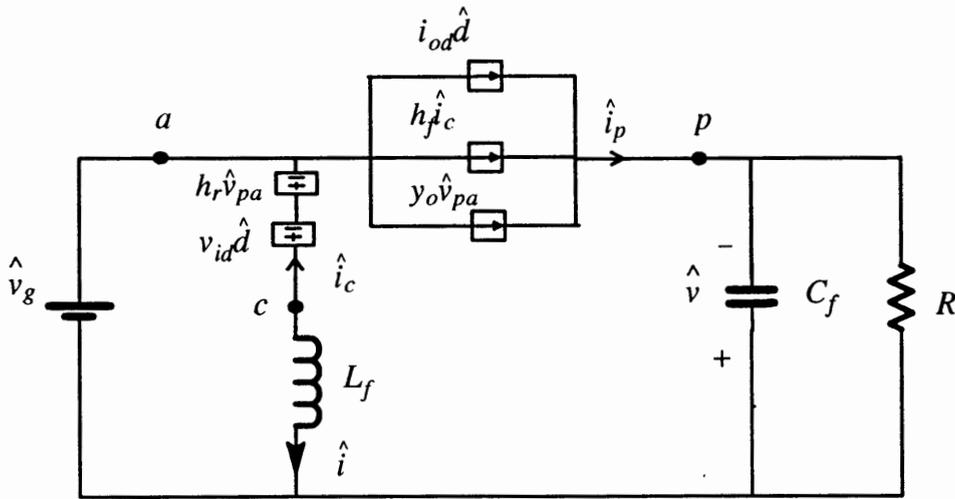


Figure 6 – 17 Small-signal hybrid parameter switch model for the PWM duty-ratio buck-boost converter of Figure 5 – 23.

The state equation of Figure 6 – 17 is:

$$\frac{d}{dt} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} = \begin{bmatrix} 0 & -\frac{h_r}{L_f} \\ \frac{h_f}{C_f} & \frac{y_o}{C_f} - \frac{1}{R} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} \frac{1-h_r}{L_f} \\ \frac{y_o}{C_f} \end{bmatrix} \hat{v}_g + \begin{bmatrix} \frac{v_{id}}{L_f} \\ -\frac{i_{od}}{C_f} \end{bmatrix} \hat{d} \quad (6 - 36)$$

As shown in Figure 6 – 18, the control–to–output frequency response of the new model matches that of the exact results. The only difference appears to be at the switching frequency, f_s .

The input–to–output magnitude and phase responses of the model are plotted in Figure 6 – 19. The exact input–to–output results are also plotted on the same graph. The magnitude agreement is excellent. The phase response of the model fails to predict exact results beyond the switching frequency, f_s .

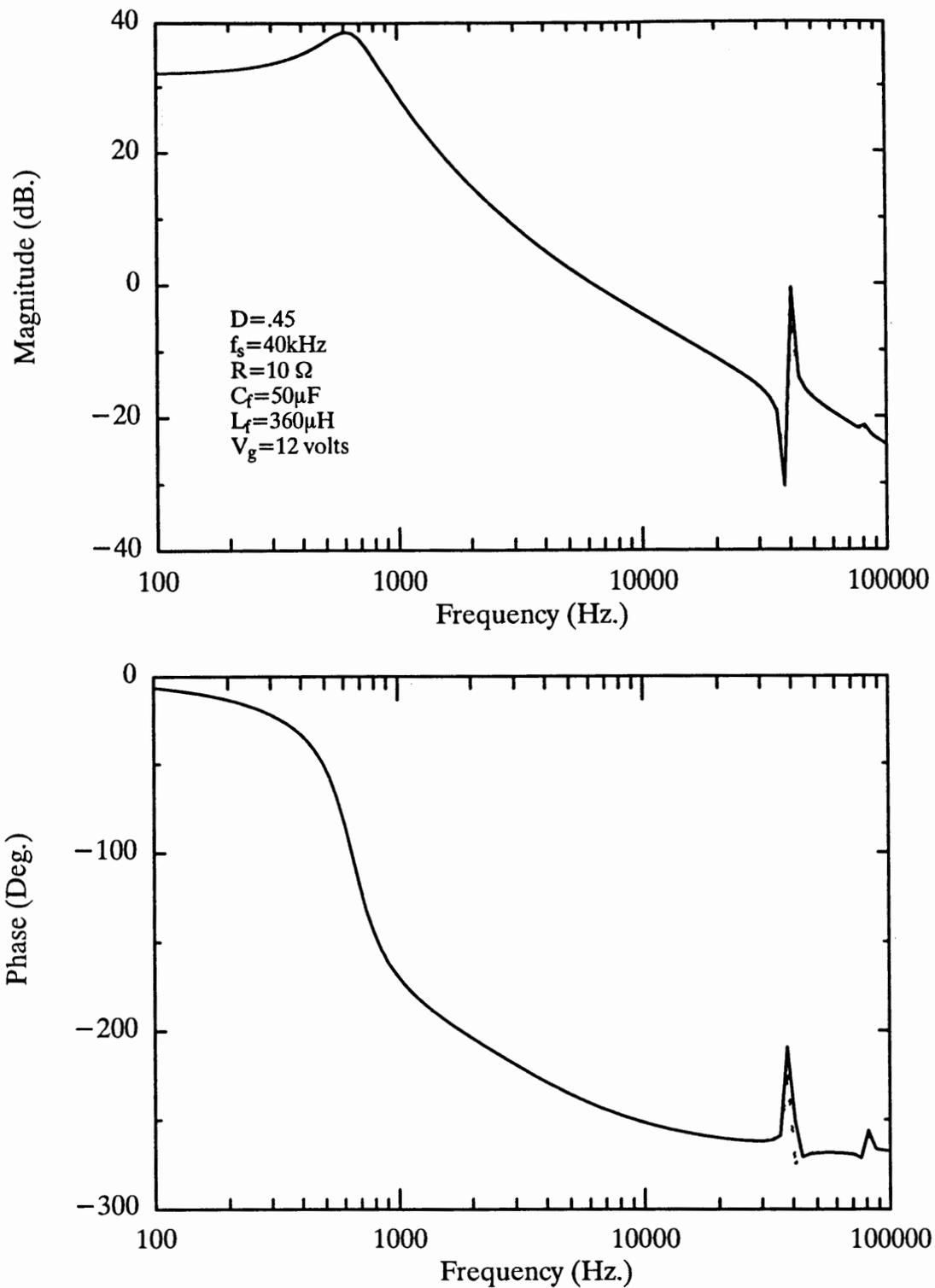


Figure 6 – 18 The control-to-output response of the PWM buckboost converter operating in CCM; exact (solid line) versus the third model (dashed line).

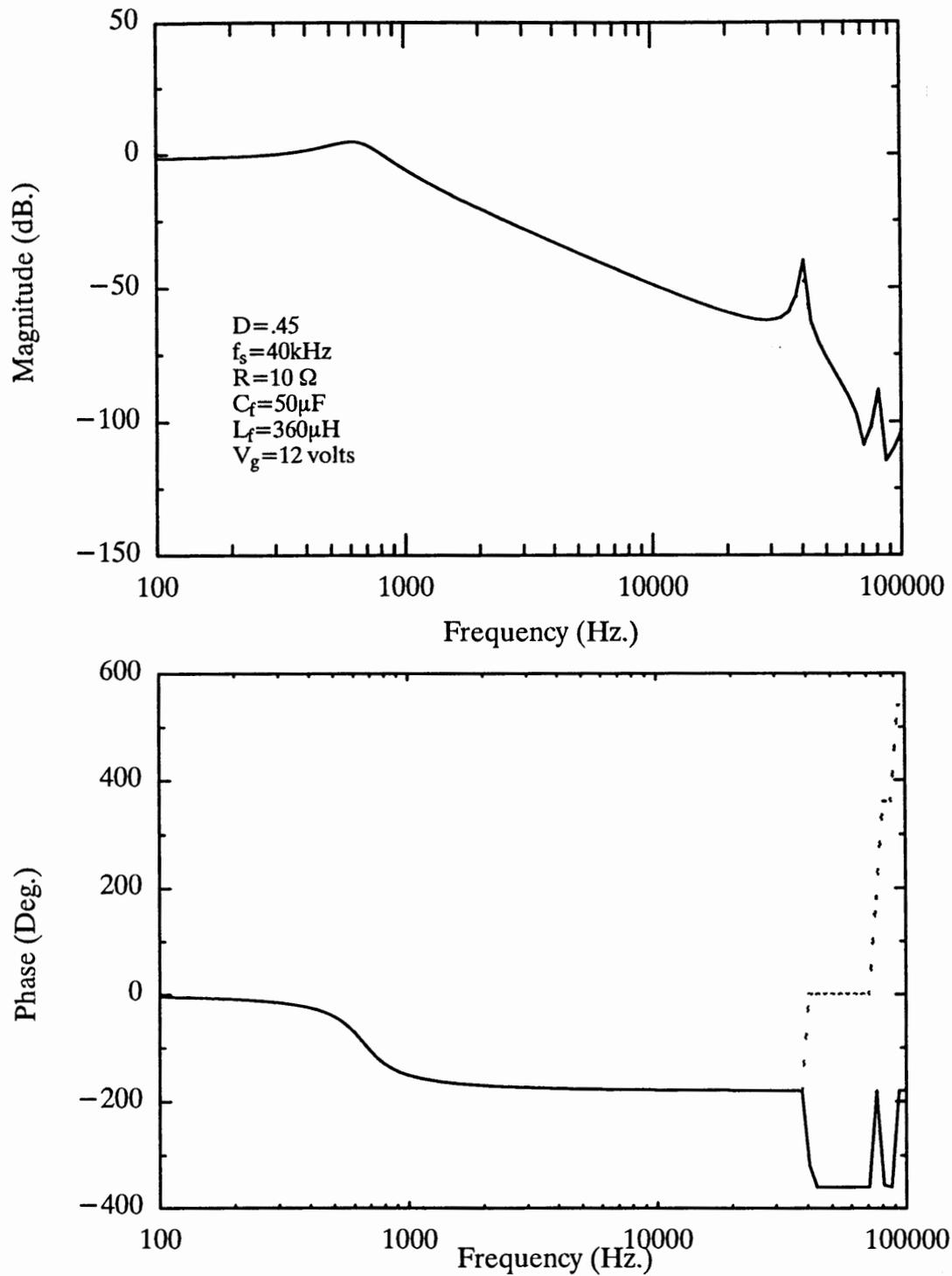


Figure 6 – 19 The input-to-output response of the PWM buck-boost converter operating in CCM; exact (solid line) versus the third model (dashed line).

6.3.2 PWM Current Programmed Buckboost Converter

The state equation for the current mode buckboost model shown in Figure 6 – 20 is given below:

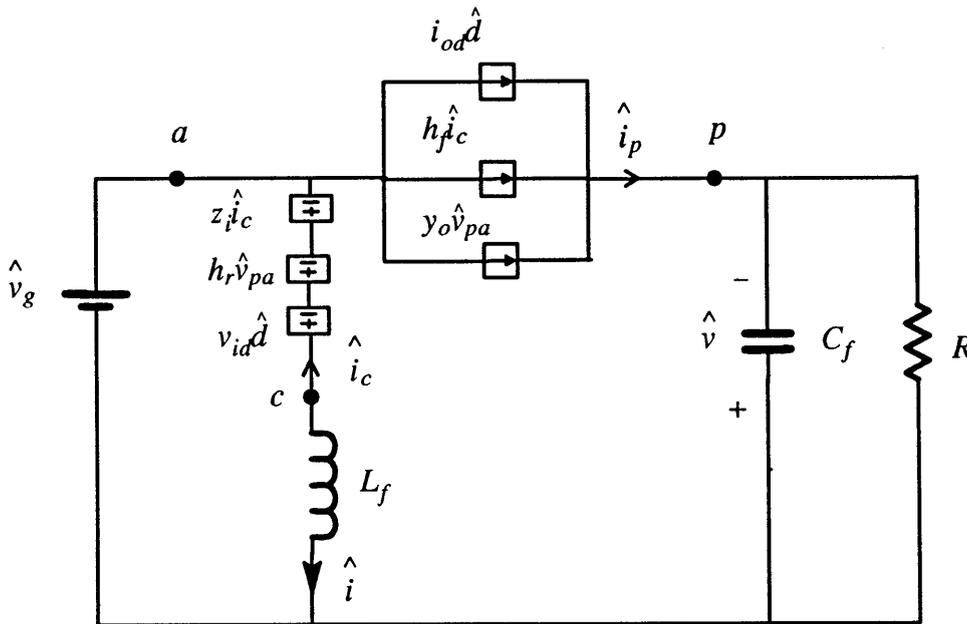


Figure 6 – 20 Small-signal equivalent model for current mode analysis of the buckboost converter in Figure 5 – 23.

$$\frac{d}{dt} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} = \begin{bmatrix} -\frac{z_i}{L_f} & -\frac{h_r}{L_f} \\ \frac{h_f}{C_f} & \frac{y_o - \frac{1}{R}}{C_f} \end{bmatrix} \begin{bmatrix} \hat{i} \\ \hat{v} \end{bmatrix} + \begin{bmatrix} \frac{1 - h_r}{L_f} \\ \frac{y_o}{C_f} \end{bmatrix} \hat{v}_g + \begin{bmatrix} \frac{v_{id}}{L_f} \\ -\frac{i_{od}}{C_f} \end{bmatrix} \hat{d} \quad (6 - 37)$$

As shown in Figure 6 – 21, the control to output responses of the model are exact up to at least twice the switching frequency. The input-to-output magnitude and phase responses of this model are shown in Figure 6 – 22. They are also exact up to twice the switching frequency.

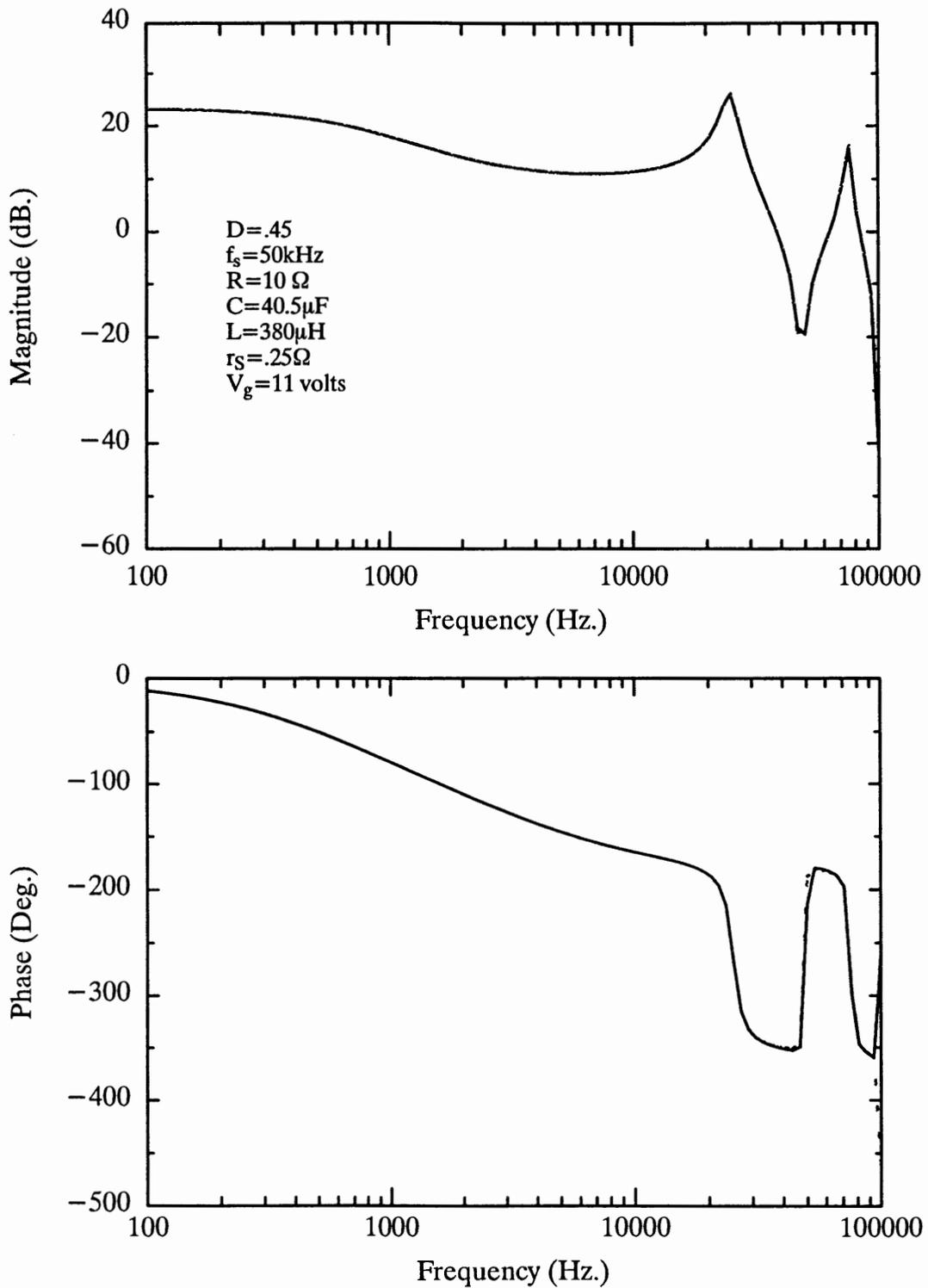


Figure 6 – 21 The control-to-output response of the PWM buckboost converter operating in CCM under current mode programming; exact (solid line) versus the third model (dashed line).

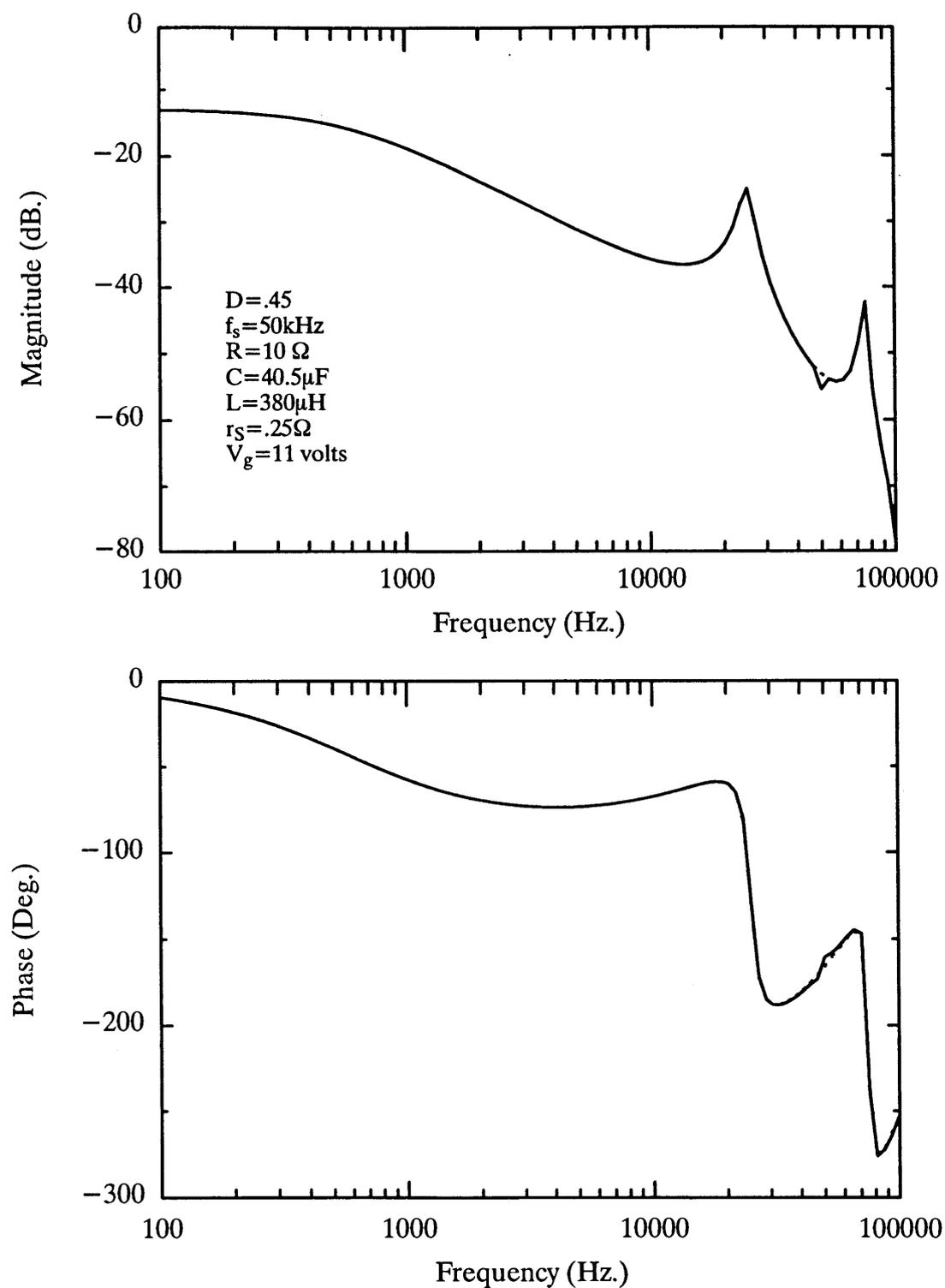


Figure 6 – 22 The input-to-output response of the PWM buck-boost converter operating in CCM under current mode programming; exact (solid line) versus the third model (dashed line).

CHAPTER 7

CONCLUSION

A new theory in modeling the power converters was presented and a unified small signal switch model was obtained. The aim of this thesis was originally to derive a simple model that could be used for both circuit and simulation analysis. The new model was found through modeling the PWM switch as a hybrid parameter two-port network. In this modeling approach, exact small-signal transfer functions [1]–[3] for two cases were considered and consequently two models were found: 1) PWM duty-ratio converters in CCM, 2) PWM current mode control converters in CCM. The new method was considered to be general and applicable to any converter circuit. Using this method other models besides the switch model can be developed. In chapter 4, the first modeling approach was presented. This model failed to produce accurate control-to-output response because when both inputs (states) were nulled, the control-to-output equation reduced to the state space averaging model. A second (new) method was presented in chapter 5 to overcome the inaccuracies of the previous method. The control-to-output and input-to-output frequency responses of the second model were plotted with the exact small-signal responses and showed to be exact for the full order system. Therefore, the next step was to obtain expressions for the parameters. In order to reduce the complexity of the expressions, the output capacitor was replaced with an independent voltage source. Then, using this second model, equivalent small-signal models for buck and buckboost converters were found. From these models, the

state space representation of the buck and buckboost converters were derived. Then, the responses were plotted with the responses obtained through the exact small-signal analysis. The PWM duty-ratio switch model results were satisfactory. The PWM current programmed model did not produce accurate input-to-output results. As explained in chapter 6, when the full order state equations were reduced to a single state, the effect of current sensing disappeared from the equations, hence; the current mode model was very inaccurate. In chapter 6, the third method was presented which successfully worked for both control schemes in CCM. This model (third model) is very accurate up to the switching frequency. The third model was applied to the buck and buckboost converters and the frequency response results were excellent. It should be noted here that some of the current mode expressions for the parameters were lengthy. This model can be easily coded for simulations.

There are significant advantages in using this modeling approach over previous current mode or duty ratio models:

- This modeling approach can be applied to any three terminal component of the converter, i.e. If needed one can use it to model the converter as a y -parameter model, etc.
- The (third) switch model applies to both control schemes, i.e. setting $h^T=0$ reduces the current mode model to duty ratio model.
- The results can be evaluated using state space transfer functions. Therefore, different transfer functions can be tested. Stability analysis is straightforward, i.e. location of poles and zeros can be determined using the state space analysis.

The disadvantages are that the switch parameters are lengthy at this time and cannot be used for circuit analysis by hand. It should also be noted that the lengthy expressions are found for the switch model, and one can try to use the new theory (two-port analysis) on the whole circuit and see how lengthy the expressions will be.

The switch modeling approach can be extended to Discontinuous Conduction Mode (DCM). At this time, these models are expected to be lengthy in derivations. Once a method is found to reduce the parameter expressions, the DCM model will also be developed.

REFERENCES

- [1] R. Tymerski, "Application of the Time Varying Transfer Function for Exact Small-Signal Analysis," *IEEE Power Electronics Specialists Conference (PESC) '91*, June 1991, pp. 80–87.
- [2] R. Tymerski, "Frequency Analysis of Time-Interval-Modulated Switched Networks," *IEEE Transactions on Power Electronics*, Vol. 6, No. 2, April 1991, pp. 287–295.
- [3] Weihe Niu. "The Exact Modeling of Time-Interval-Modulated Switched Networks," M.S. Thesis, Portland State University, Portland, Oregon, February 1993.
- [4] B. Y. B. Lau and R. D. Middlebrook, "Small-Signal Frequency Response Theory for Piecewise-Constant Two-Switched-Network Dc-to-Dc Converters," *IEEE Power Electronics Specialists Conference (PESC)*, 1986, pp. 186–200.
- [5] R. D. Middlebrook and S. Cuk, "A General Unified Approach to Modelling Switching Converter Power Stages," *IEEE Power Electronics Specialists Conference (PESC)*, 1976, pp. 18–31.
- [6] Andrez Pietkiewicz and Daniel Tollik, "Unified Topological Modeling Method of Switching DC-DC Converters in Duty-Ratio Programmed Mode," *IEEE Transactions on Power Electronics*, Vol. PE-2, No. 3, July 1987, pp. 218–226.
- [7] R. Tymerski and V. Vorpérian, "Generation, Classification and Analysis of Switched-Mode DC-to-DC Converters by the Use of Converter Cells," *IEEE International Telecommunications Energy Conference (INTELEC)*, October 1986, pp. 181–195.

- [8] R. Tymerski, V. Vorpérian, F. C. Lee, and W. T. Baumann, "Nonlinear Modeling of the PWM Switch," *IEEE Transactions on Power Electronics*, Vol. 4, No. 2, April 1989, pp. 225–234.
- [9] V. Vorpérian, R. Tymerski, and F. C. Lee, "Equivalent Circuit Model for Resonant and PWM Switches," *IEEE Transaction on Power Electronics*, Vol. 4. No. 2, April 1989, pp. 205–214.
- [10] V. Vorpérian, "Simplified Analysis of PWM Converters Using Model of the PWM Switch: Part I and II," *IEEE Transaction on Aerospace and Electronics Syst.*, Vol. 26, No. 2, May 1990.
- [11] G. C. Verghese, C. A. Bruzos, and K. N. Mahabir, "Averaged and Sampled-Data Models for Current Mode Control: A Reexamination," *IEEE Power Electronics Specialists (PECS)*, 1989, pp. 484–491.
- [12] R. Tymerski and Duwang Li, "State-Space Models for Current Programmed Pulsewidth-Modulated Converters," *IEEE Transactions on Power Electronics*, Vol. 8, No. 3, July 1993, pp. 271–278.
- [13] R. D. Middlebrook, "Topics in Multiple-Loop Regulators and Current Mode Programming," *IEEE Power Electronics Specialists*, 1985, pp. 716–732.
- [14] S. Hsu, A. R. Brown, L. Resnick, and R. D. Middlebrook, "Modeling and Analysis of Switching DC-to-DC Converters in Constant-Frequency Current Programmed Mode," *IEEE Power Electronics Specialists (PESC)*, 1979, pp. 284–301.
- [15] V. Vorpérian, "Analysis of Current-Mode Controlled PWM Converters using the model of the current-controlled PWM Switch," Presented at *Power Conversion and Intelligent Motion Conference*, PCIM '90, Oct. 1990.
- [16] R. Ridley, "A New Continuous-Time Model for Current Mode Control," *IEEE Transaction on Power Electronics*, Vol. 6, Apr. 1991, pp. 271–280.