3-28-2019

Investigation of Electronic and Optical Properties of 2-Dimensional Semiconductor Tin Selenide (SnSe) Thin Films

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Investigation of Electronic and Optical Properties of 2-Dimensional Semiconductor Tin Selenide (SnSe) Thin Films

by

Shakila Afrin

A thesis submitted in partial fulfillment of the requirements for the degree of

Master of Science
in
Electrical & Computer Engineering

Thesis Committee:
Raj Solanki, Chair
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Portland State University
2019
Abstract

Over the last 5 decades, the semiconductor industry has been well served by Si based technology due to its abundant availability, lower manufacturing cost, large wafer sizes and less complexity in fabrication. Over this period, electronic devices and integrated systems have been miniaturized by downscaling of the transistors. The miniaturization has been guided by the Moore's law where the numbers of transistors have doubled over every two years. However, the trend of transistor miniaturization is fast approaching its limit. Hence, alternate and innovative solutions are necessary to tackle this problem and this propels the research for finding novel materials with unique properties.

The isolation of graphene, a single layer of graphite in 2004 had dramatically pioneered a new regime of research and investigation as a potential material to replace traditional Si. Graphene is the most widely studied two dimensional (2D) material exhibiting fascinating electronic, optoelectronic and electrochemical properties. Room temperature graphene has very high carrier mobility, a hundred times larger than that of Si, but it lacks a bandgap preventing its application in digital electronics. However, the advent of graphene initiated exploration of other 2D materials as a possible replacement for Si for future generation of electronic devices. Other 2D layered materials include transition metal dichalcogenides (TMDs), other layered metal chalcogenides, black phosphorus (BP), boron nitride (BN) etc which are also attractive due to fascinating electronic band structure and layer dependent properties that have demonstrated potential applications in optoelectronics and semiconductor devices. Metal chalcogenides are among the well-studied layered materials that have been isolated as high-quality and two-dimensional crystals. Among the 2D layered metal chalcogenide materials is tin selenide (SnSe), which
belongs to group IV–VI that has attracted considerable attention due to its interesting structural and optical properties, hence it has potential applications in optoelectronics, photovoltaics, memory, energy storage, and catalysis.

To date, SnSe films have been produced by exfoliation or chemical vapor deposition that produces flaky films. In this research, uniform, smooth and high quality SnSe thin films were grown over large area (5cm x 5cm) Si/SiO$_2$ substrates using Atomic Layer Deposition (ALD). Films were grown over a temperature range of 350°C to 450°C, which exhibit p- type semiconductor characteristics. ALD is perfect for the growth of layered materials due to its precise controllability of film composition and thickness as the growth proceeds layer by layer. Structural and optical properties of the as-grown films were investigated using X-ray diffraction (XRD), Raman spectroscopy, and X-ray photoelectron spectroscopy (XPS). These analyses show growth of 2 dimensional, orthorhombic phase films. Magnetic analysis shows a paramagnetic behavior. Back-gated transistors were fabricated for electrical characterization which showed p-type conductance, with an average hole mobility of 10 cm$^2$/V.s and $I_{on}/I_{off}$ ratio of $\sim 10^5$. 

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Dedication

To my parents
Acknowledgments

I would like to acknowledge my supervisor Prof. Raj Solanki for supporting me in this research. Dr. Solanki has been a continuous source of knowledge and encouragement throughout my research, without his guidance I would not be able to succeed in this endeavor.

I owe my gratitude to all of the defense committee members, Prof. Richard Campbell and Prof. Shankar Rananavare for their kind advice and suggestion in general.

I would like to thank Capri Price from Dr. Clare’s lab for assisting me in Raman analysis. I also would like to express my gratitude to Sheng Kuei Chiu from Dr. Goforth’s lab for helping with the XRD measurements.

I would also like to thank ECE Chair Prof. Douglas Hall for his academic advice. His guidance helped me to set my career goal.

I thank to my colleagues Neal Kuperman and Michael Hopkins for providing help in the Lab of Naonoelectronics. Special thanks to Robert Browning for helping me with the fabrication.
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1.1 2D materials

Significant research effort has been directed towards the search for new materials with unique properties that can shape the technological advancement of electronic devices. Graphene is one such new material discovered lately and has true potential to be used for advanced technological applications due to its novel properties. It was first isolated in 2004 from its parent material graphite in the form of a single crystal mono layer. The invention of graphene showed that the two-dimensional (2D) material properties can be significantly different and far superior to those of the bulk materials. The term “two-dimensional (2D)” implies that the material thickness is much smaller than the wavelength of the light. Graphene soon became the role model and the most studied 2D material due to its exceptional electronic, optoelectronic, and sensing applications. Although graphene has many unique properties, it has its own limitations hindering practical applications.

Figure 1.1: Elements comprising most of 2D materials [1].
The lack of an intrinsic band gap is one of the major challenges restricting its potential to be fully utilized in electronics. However, graphene’s discovery sparked off tremendous interest toward other alternate 2D semiconductors.

Following the success of graphene, researchers started to explore other two dimensional (2D) layered materials with an aim to addressing the desired properties for the next generation of electronic devices. Hence, a large number of 2D materials with various properties were investigated in the form of isolated monolayers. Fig. 1.1 shows the most common elements in the periodic table that comprise 2D materials. The most common layered 2D materials include hexagonal boron nitride (hBN), transition metal, non-metal chalcogenides, oxides, hydroxides and halides. Hexagonal 2D boron nitride (hBN) attracts a lot of research interest due to its potential to inducing an energy bandgap in graphene when graphene was exfoliated onto it [2].

Figure 1. 2: Library of some of more common 2D layered materials [4].
Transition metal dichalcogenides (TMDCs) such as MoS$_2$, MoSe$_2$, WS$_2$, WSe$_2$, etc. are among the highly studied layered 2D materials due to their novel properties. Outside TMDCs, transition metal chalcogenides (NbSe$_3$, TaSe$_3$) and transition metal oxides (TMOs: LaVO$_3$, LaMnO$_3$) are also isolated in monolayer form and are well studied [3]. Metal chalcogenides have also received particular attention, owing to their fascinating electronic band structures and remarkable layer-dependent properties. Fig. 1.2 illustrates a partial list of the most common 2D layered materials and some of their properties.

![Crystal structures and interlayer distance of three important 2D layered materials](image)

Figure 1.3: Crystal structures and interlayer distance of three important 2D layered materials graphene, molybdenum disulphide and tin selenide are shown in a, b and c respectively [5-7].

Crystal structures of graphene, MoS$_2$ (TMDC) and SnSe (Metal Chalcogenide) bilayers are illustrated in Fig. 1.3. In 2D layered materials, the interlayers are connected through weak van der Walls interaction while within the same layer the atoms are
covalently bonded. Inter layer distances for some of the key 2D layered materials are noted in the Fig. 1.3 (a-c).

Unlike typical nanostructures, the surface of 2D layered materials are naturally passivated due to their dangling-bond-free atomic structures. This superior dangling-bond-free lattice structure makes them easily exfoliated or synthesized and integrated with various electronic and photonic devices at the atomic scale. Heterogeneous integration is another key advantages of 2D layered materials and allows significant freedom in fabricating complex heterostructures using various nanoscale and other 2D materials. 2D layered materials do not have the constraints of crystal lattice mismatch since layers with different lattice constants are bonded by weak van der Waals force. Thus they can be exploited to create novel devices with van der Waals heterostructures (vdWHs). Due to their diverse electronic and optoelectronic properties, 2D materials provide a wide range of Electromagnetic (EM) spectrum from radio wave to ultraviolet as seen in Fig. 1.4. Graphene has zero bandgap and it interacts with light from microwave to ultraviolet spectrum thus making it ideal candidate for signal modulation, light detection, etc. Its gapless nature hinders application in the light emitting devices. MoS\textsubscript{2}, one of the promising transition metal dichalcogenides (TMDCs), attracts its potential for light emitting properties primarily in near-infrared wavelength range due to its direct bandgap of \(~1.84\) eV. Hexagonal boron nitride (hBN) is another important type of 2D material, it has a large bandgap of around 6 eV, making it an excellent dielectric. Hexagonal boron nitride (hBN) is typically integrated in various heterostructures for electrostatic gating of other 2D materials [2]. Bulk black phosphorus (BP) has a direct bandgap of around 0.3 eV, which
can be increased as high as ~2eV in monolayer form, is an excellent material for near-infrared to mid-infrared wavelength applications.

Figure 1. 4: 2D layered materials with a broad spectral range (a) electromagnetic (EM) spectrum represented by a rainbow arrow. Top portion of the panel illustrates potential applications for different spectral ranges. Bottom panel represents the atomic structures for some of the key 2D layered materials: hexagonal boron nitride (hBN), molybdenum disulfide (MoS$_2$), black phosphorus (BP), and graphene (from left to right). The possible spectral ranges covered by different materials are indicated using colored polygons. Electronic band structures of single layer hBN, MoS$_2$, BP, and graphene are shown in (b), (c), (d) and (e) respectively [8].
1.2 SnSe, a 2D semiconductor

Extensive research has been conducted on 2D layered metal chalcogenides due to their unique surface and layer-dependent properties [9-10]. Metal chalcogenides have a very wide range of bandgaps which offer multiple applications in electronic, optical, and optoelectronic fields. Among the various metal chalcogenides, Tin-based binary chalcogenide compounds Sn-X (where X denotes a chalcogen: S, Se, Te) are particularly interesting due to their potential applications in the next generation devices and have been heavily investigated. Hence, the focus of this research was to examine the electrical, optical and the crystal structure of SnSe.

The layered 2D Sn-X compounds are typically crystallized into three phases: hexagonal, monoclinic, and orthorhombic. Chemical formula Sn-X$_2$ represents the hexagonal and monoclinic phases, while orthorhombic crystal structure is denoted by Sn-X. Sn-X materials are less researched compared to the Sn-X$_2$ family despite the fact that they are the ideal candidate materials for photovoltaic and optoelectronic applications with suitable energy band structures and excellent electronic properties [11-12]. However, the recent discovery of tin selenide (SnSe) as a superior thermoelectric (TE) material has refocused the research interest on Sn-X compounds.

Thermoelectric (TE) materials have been investigated extensively for energy harvesting and possible thermal power management applications such as in refrigerator or in heat pump. These types of materials have the ability to provide direct conversion between electric and thermal energy and the energy conversion efficiency is primarily represented by material dimensionless term called figure of merit (ZT). ZT defines how efficient a material can provide interconversion between thermal energy and electrical
energy (or vice versa). Traditionally, group IV-VI compounds and their alloys such as PbTe, PbSe, GeTe and SnTe are well recognized as efficient thermoelectric materials owning to their unique structural properties [13-16]. Recent investigations confirmed that group IV-VI tin-based mono-chalcogenides (Sn-X) such as SnSe and SnS also hold a lot of promise for their potential in thermoelectric applications. Recent studies by Zhao et al. shows a high ZT of 2.6 +/- 0.3 at 923 K in single crystal 2D SnSe, which is far superior compared to the other popular TE materials [17]. At room temperature, SnSe belongs to Pnma space group with orthorhombic structure, however it converts to a high symmetry orthorhombic Cmcm space group at high temperature around 800 K [17-18]. Fig. 1.5 shows the room temperature lattice structure (Pnma group) of layered SnSe with orthorhombic crystal structure. For each of the layers, Sn and Se (S) atoms are bonded covalently and fabricate zig-zag formats along the b-axis [Fig. 1.5]. Interlayers are bonded by the weak van der Walls forces. The interlayer chalcogen to chalcogen distance in a unit cell is around 0.34 nm. [19].

The high ZT value of 2D SnSe materials is explained by the compounding effects of its ultra-low intrinsic thermal conductivity (<0.4 Wm\(^{-1}\)K\(^{-1}\) at 923 K along the b-axis) and low power factor (<10 uW cm\(^{-1}\)K\(^{-2}\) at 900K). Immense research effort has been focused on SnSe to increase the energy conversion efficiency (ZT) value by tuning hole-doping. [20].
Figure 1.5: SnSe crystal structure along b axis at room temperature.

Tin selenide (SnSe) belongs to IV–VI group which has attracted great attention due to its interesting structural and optical properties, hence has potential applications in optoelectronics, photovoltaics, memory, energy storage, and catalysis [12, 21-25]. The crystal structure of SnSe is orthorhombic with a direct band gap of about 1.3 eV, well suited for applications in photovoltaics and optoelectronics devices. Single crystal SnSe, which exhibits p-type semiconductor property, has potential applications in memory switching, owing to its interesting polarity dependent electronic properties [26]. Both tin and selenium, the constituents of SnSe are nature-abundant, as well as non-toxic, which makes
it very suitable for the consumer market. 2D SnSe thin films display superior photosensitivity and tunable bandgap over bulk form due to higher surface area and quantum confinement effects.

SnSe has become more and more popular recently, yet there is a need to find feasible and controllable method to isolate or synthesize 2D single-crystal. The single and few layers SnSe thin films can be made by top-down mechanical exfoliation or bottom-up chemical synthesis process. Mechanical exfoliation has been used to prepare monolayer or few layer graphene, MoS$_2$ and black phosphorus (BP) due to its low cost and high quality output. However a recent study by Hong-Yue et al. suggests that it might be difficult to mechanically exfoliate monolayer SnSe from its bulk phase due to the stronger inter-layer interaction [27]. It has been shown that the inter-layer binding energy (BE) of SnSe is much larger than that of graphite, MoS$_2$ and BP. Based on density functional theory (DFT) the strength of inter-layer binding (meV/atom) for SnSe shows same trend using different exchange-correlation functionals, hence mechanical exfoliation might not be ideal technique for isolating SnSe from bulk phase.

In the literature a number of bottom-up techniques have been reported for synthesis of tin selenide (SnSe), including aqueous solution [27], thermal evaporation [28], electro-deposition [30], spray pyrolysis [31-33], hot wall epitaxy [34], vacuum evaporation [35], laser ablation [36], D.C. magnetron sputtering [37], chemical vapor deposition [38], and chemical bath deposition [39]. The films produced by these techniques are rough and composed of particulates. Unlike these techniques, we have employed atomic layer deposition (ALD) for growth of smooth, continuous films of SnSe.
ALD is a step-wise growth process where reactants of the material are alternately injected into the growth area, followed by purging of the excess species and by-products after each growth cycle. The sequential surface reactions produce high quality films and could be grown over large substrates [40]. ALD is perfect for the growth of layered materials due to its precise controllability of film composition and thickness as the growth proceeds layer by layer. We have used this method to produce several layers thick, smooth and continuous thin films of SnSe over large areas (5x5 cm). The films have been characterized by various techniques to determine their physical and electronic properties.

1.3 Contribution of this research
To date, the SnSe films produced suffer from size limitation (exfoliation) or grainy texture for films grown via various chemical vapor deposition (CVD) or physical vapor deposition (PVD) or other growth processes. Because of these limitations, ALD was investigated for growth of SnSe films. It is shown that using ALD, smooth and continuous films can be grown over large areas. To evaluate the quality of these films, their structural, optical and electrical properties have been characterized. It is shown that the quality of these films match those of exfoliated films, but are of much larger areas.

1.4 Organization of the thesis
The thesis starts (Chapter 1) with an introduction to two dimensional (2D) layered materials. The isolation of graphene from graphite and the birth of 2D materials are presented at first. Recent advancements in “beyond graphene” 2D materials is highlighted
next. The importance of IV-VI metal chalcogenide tin selenide (SnSe) for next-generation of devices are also emphasized in this chapter. Chapter 2 exclusively focuses on the synthesis of tin selenide (SnSe) thin films. A comprehensive study of Atomic Layer deposition technique including benefits and growth mechanism is discussed in this chapter. Chapter 3 deals with different structural characterization techniques of the as-grown SnSe thin films. The development of the SnSe based back gated field effect transistor is described in Chapter 4. The details of the fabrication steps are presented here along with the electrical testing. Chapter 5 summarizes the major contributions of this work. Also we propose future extension of this work and further development.
CHAPTER 2
Synthesis

2.1 Atomic Layer Deposition (ALD)

There has been a continuous push to develop new materials with different dimensionality and functionality for applications in various fields such as electronics, photonics, energy harvesting and sensing. Atomically thin nanostructured materials serve the building blocks of highly promising advanced devices due to their superior and unique properties with down-scaling of device dimensions. In order to continue the relentless miniaturization, the atomically thin materials need to be coupled with different materials with precise controllability. Modern advanced devices use complex heterostructures to take advantage of the surface modification and fabrication steps. However, the synthesis of heterostructure materials on a limited space and high aspect ratio is very challenging. Furthermore, the synthesis of novel heterostructures based nanomaterials where one material needs to be grown on top of others with substantial surface area and chemical, physical resistivity is a major challenge. This requires a synthesis technique that is capable of providing atomic level procession while maintaining the native characteristics intact. Among all the various available deposition techniques, atomic layer deposition (ALD) appears to be one of the most promising techniques due to its simplicity, reproducibility and the high conformality of as-grown films. ALD has already been in widespread use in microelectronic and semiconductor industries because of its compatibility with modern micro/nano-fabrication processes. Modern CMOS fabrication processes use ALD to deposit gate dielectrics to produce conformal and homogenous thin films [41]. ALD is used as the primary deposition
technique for high aspect ratio structures, various nanostructures such as nanoparticles, nanowires, nanotubes and biological materials [42-45].

Atomic layer deposition (ALD) is a step-wise growth technique where reactants or precursors of the material are alternatively injected into the growth area, followed by purging of the excess species and by-products after each growth cycle. The principle of the ALD technique is basically a reaction between the precursor materials where they are separated into successive surface reactions and it belongs to the class of chemical vapor deposition (CVD) process. Over the last few decades, tremendous progress has made in the variety of materials made by ALD [46] and their applications in related industries [42]. The commonly acknowledged ALD technique was invented back in 1974 under the name “atomic layer epitaxy” (ALE) in Finland by Dr. Tuomo Suntola. The “atomic layer epitaxy” was developed for the growth of high-quality polycrystalline ZnS thin films used in electroluminescent (EL) flat panel displays. However, the principle of ALD was first introduce in 1960s under the name “molecular layering” in the Soviet Union. In the earlier days ALD did not receive a lot of interest since the film growth process is very slow. The precise controllability of the thickness of the as-grown films combined with the high step coverage for nanostructure fabrication increased the interest toward ALD techniques starting mid 1990’s.

ALD is similar to the chemical vapor deposition (CVD) type processes except that it is a repeated process and carried out by breaking a typical CVD process into an iterated sequence of self-saturating deposition cycles [47]. Unlike CVD where the reacting gases are injected and mixed in the process chamber simultaneously to form a film, the ALD reactants or precursors are inserted individually to react with the surface, one layer at a
Basic ALD process starts with first introducing a precursor gas onto the substrate surface inside the reactor. A chemisorption process takes place at the surface of the substrate which is referred to an adsorption driven by chemical reaction. This chemisorption process produces a new chemical species covalently bonded to the substrate surface. A finite number of species are deposited on the surface due to the availability of limited number of surface sites which ensures self-terminating process. One of the biggest advantages of the ALD technique is that the exact precursor dosage is not required. The surface saturation limits the reaction; however, enough precursor molecules need to be there to completely cover the substrate surface. The chemisorption process is followed by a purge step where the unreacted precursors and the byproducts are removed with an inert gas (N₂ or Ar). The second precursor is then introduced into the reactor, which reacts with the first chemisorbed precursor. Excess precursors and the byproducts are removed by another nitrogen purge, which completes the process and creates one monolayer of the final film. The growth per cycle (GPC) is constituted by the sequence of self-limited reactions and purges. Due to the self-terminating reactions in ALD, the thickness of the as-deposited film is controlled by the number of cycles.

2.2 Synthesis of Tin Selenide (SnSe) using ALD

Fig. 2.1 shows a flow diagram as an example of how a single layer of SnSe is created using tin bisacetylacetonate (Sn(acac)₂) and H₂Se precursors as the reactants. Using this process, we have demonstrated growth of several of the 2D films, including MoS₂,
WS₂, SnS and WSe₂, SnSe over a large area (5cm x 5cm). This process can be readily scaled up for industrial applications.

Figure 2. 1: ALD cycle of SnSe.

ALD’s self-limiting growth mechanism leads to as-grown thin films with robust and precise atomic level control. The layer-by-layer growth is not affected by gas flow pattern, thus it ensures high degree of uniformity and conformity, making it one of the most promising thin film deposition techniques for nanostructures, patterned substrates, and high aspect ratio features. There are no nucleation based inferior discontinuities in the ALD process, hence the as-grown films uniquely grow pinhole and practically stress free [48]. Chemical Vapor Deposition (CVD) and Physical Vapor Deposition (PVD) techniques are based of nucleation followed by the growth of grains. By the time, the grains finally blend into continuous films the thickness could be on the order of 50-100 Å [49-50] in the case of CVD, and even thicker in the case of (PVD). CVD and PVD grown films exhibit
significant compressive stress and lots of pinholes extending far beyond coalescence depth. Pinholes and compressive stress result non-ideal grain boundaries and typically the CVD and PVD films lack passivation. These films yield poor encapsulation performance at any layer where the thicknesses is less than 0.5 μm. In contrast, very thin encapsulation films can be realized by ALD with minimum adverse impact on device performance. Another characteristic of ALD is that the films can be deposited at low temperatures, which is beneficial for processes where low thermal budget is required. Depending on the film, ALD can deposit films at temperatures as low as 100°C which has been demonstrated with Al₂O₃ and ZrO₂ [51], although most of the TMD films are grown at higher temperatures to produce crystalline films of appropriate phase.

Figure 2.2: Microchemistry F-120 ALD reactor.
Thin films of SnSe were grown in a Microchemistry F-120 ALD reactor shown in Fig. 2.2 that can handle two 50mm x 50mm substrates per run. The substrates for these films consisted of p-type Si wafers coated with a 320 nm thick film of thermal silicon oxide. The Sn precursor was tin (II) acetylacetonate and H₂Se (8%, balance N₂) was the Se source. The exhaust of the system included a burn-box to decompose any unreacted gases. The tin source temperature was held at 95°C. The growth pulse sequence was as follows: Sn source 0.6s, N₂ purge of 1 s, H₂ Se pulse of 1.2 s, and N₂ 1 s purge. Uniform film growth took place over a temperature ranged from 40°C to 410°C. The films reported here were grown at 390°C, where the growth rate was 0.5 nm per cycle.
CHAPTER 3
Thin Film Characterization

3.1 Characterization Techniques

ALD grown films were characterized using several analytical techniques. Raman spectroscopy measurements were carried out at ambient room temperature on Horiba HR Evolution spectrometer to detect the phase of the films. The optical source for Raman analysis was a doubled YAG laser (532 nm) and the spectrum was recorded over the range of 40 cm\(^{-1}\) to 300 cm\(^{-1}\). Grazing (0.5 degree) incidence x-ray diffraction (XRD) pattern of the SnSe thin film was recorded with a RIGAKU Ultima IV X-ray diffractometer using Cu K\(\alpha_1\) radiation (lambda = 1.5405 Å) at grazing incidence in the 2 theta range of 10-70 degrees at room temperature. X-ray photoelectron spectroscopy (XPS) was employed by using Versaprobe II to confirm the stoichiometry of the films. Magnetic property of SnSe was examined with a SQUID system.

3.2 Raman analysis

Raman spectroscopy is a popular characterization technique used to identify the stoichiometry or crystalline properties of the materials. It is one of the structural characterization tools that can be used to observe vibrational and other low frequency modes. Inelastic scattering from laser light interaction is the fundamental mechanism of Raman spectroscopy. When laser light interacts with the molecular vibrations or excitations, the energy of the laser photons shifts up or down which provides information regarding the vibrational modes of the system. This is known as Raman Effect. Raman
spectroscopy measurements were performed to investigate the layered phase and the quality of the as-grown SnSe thin films. The Raman spectrum of the SnSe sample is shown in Fig. 3.1. Five distinct peaks are clearly observed at 69.26, 100.17, 122.59, 151.69 and 184.289 cm\(^{-1}\).

![Raman spectrum of as-grown SnSe thin film on Si/SiO\(_2\) substrate](image)

**Figure 3.1: Raman spectrum of as-grown SnSe thin film on Si/SiO\(_2\) substrate [52].**

The highest intensity peak was found at 101 cm\(^{-1}\) which corresponds to the B\(_{3g}\) phonon mode while peaks at 70, 123 and 151 cm\(^{-1}\) belong to A\(_{1g}\), A\(_{2g}\) and A\(_{3g}\) vibrational modes, respectively. A\(_{g}\) and B\(_{3g}\) are the two rigid shear modes of a layer with respect to its neighbors and confirm the characteristic planar vibration modes of orthorhombic phase SnSe [53]. The last band A\(_{1g}\) at 184 cm\(^{-1}\) could be due to the vibrational mode coming from some trace of SnSe\(_2\) as reported by Chitra *et al* [54]. Although our XRD and XPS
characterizations did not find any trace of SnSe$_2$, it may be worthwhile to investigate further to determine the source of this peak.

3.3 **X-ray photoelectron spectroscopy (XPS)**

X-ray photoelectron spectroscopy (XPS) is a material analysis technique that measures the elemental composition of the material and provides information regarding interatomic bonds. It can determine the empirical formula, electronic state and chemical state of elements that exist within the material. The XPS spectra typically are acquired by irradiating a material with a beam of x-rays while at the same time determining the kinetic energy and number of electrons escaping from the top to 10nm of the material being analyzed. XPS is very useful for metal chalcogenide films analysis to determine the

![VersaProbe II](image_url)

*Figure 3.2: VersaProbe II.*
proper phase and composition of various films as a variant of the growth parameters. It can be used to analyze the surface chemistry of the materials, inorganic compounds, polymers, ceramics, bio-materials etc.

Figure 3. 3: X-ray photoelectron spectroscopy (XPS) on SnSe thin film (a) shows Se 3d and (b) Sn 3d binding energies, respectively [52].
XPS of SnSe films was performed using VersaProbe II (Fig 3.2) to determine compositional analysis of the ALD thin films. Fig. 3.3 shows the resulting XPS spectrum of the as-grown SnSe sample. Two strong peaks at approximately 486.2 and 495.7 eV correspond to the Sn\textsubscript{3d\textsubscript{5/2}} and Sn\textsubscript{3d\textsubscript{3/2}} state binding energies of Sn, respectively. The Se peak is located at 54.7 eV which corresponds to Se\textsubscript{3d\textsubscript{5/2}}. These results are in close agreement with the earlier reported binding energies for Sn and Se in bulk or nanocrystalline SnSe [27, 55-56].

### 3.4 X-ray diffraction (XRD)

X-ray diffraction (XRD) is a non-destructive characterization tool that can be used to identify various materials and their crystalline properties such as minerals, inorganic compounds, fine-grained minerals, clays and mixed layer clays, which are difficult to determine optically. Other applications include obtaining phase, orientation and unit cell dimensions of the materials, structural properties such as lattice parameters (interplanar spacing), strain, grain size, epitaxy, and phase composition. It can also be used to determine the thickness of thin films, samples purity, multilayers and atomic arrangement.
In this research, X-ray diffraction (XRD) was performed to investigate the phase structure of SnSe thin films. Grazing incident X-ray beam angle was kept very small (0.5 degree) to maintain the background scattering as low as possible from the amorphous SiO$_2$ substrate. This technique is particularly helpful for thin film XRD characterization where the penetration depth of the X-ray is important. Grazing incidence XRD pattern from the as-grown SnSe shown in Fig. 3.4 was recorded at room temperature. Formation of well crystallized films is also evident from the sharp peaks. The XRD pattern shows the presence of sharp peaks corresponding to (200), (400), (311), (511), (402) and (800) planes of SnSe. It is worthwhile to note that the predominant peaks are (400) and (200). These peaks are consistent with the peaks of orthorhombic SnSe reported in literature synthesized by various techniques [7, 27-28,
A broad XRD peak located approximately at 22.5° is from the amorphous SiO$_2$ film on the silicon substrate [58].

### 3.5 Magnetic property

Magnetic properties of these films were characterized on a superconducting quantum interference device (SQuID). The system used for this technique allowed samples to be inspected over a temperature range from room temperature to 1.8 K, and magnetic fields of -7 to 7 teslas. The magnetic moment (emu) vs applied field (Oe) plot measured at 1.8 K is shown in (Fig. 3.5). After subtracting the Si substrate diamagnetic background, the thin SnSe film shows a paramagnetic profile which suggests that there are unpaired electrons attracted by the magnetic field due to the electrons' magnetic dipole moments.

![Magnetic properties of thin film SnSe illustrating a paramagnetic profile](image)

**Figure 3.5:** Magnetic properties of thin film SnSe illustrating a paramagnetic profile [52].
3.6 Optical Properties

Tin selenide, due to its narrow band gap and high optical absorption coefficient, is a material of interest for optoelectronics and photovoltaic applications [12]. However the electronic structure and physical properties are affected by the dimension and phase of these materials. As reported by Guangsha *et al.* different layer thickness of p-type SnSe thin films exhibit different values of bandgap energy [59]. In the case of bulk orthorhombic tin selenide (SnSe) the direct bandgap is 1.3 eV whereas 1.66 eV and 1.62 eV respectively for single-layer and double-layer as illustrated in Fig. 3.6. Quantum confinement effect is responsible for the variation in bandgap, thus direct bandgap of SnSe thin films is greater than that of bulk SnSe [60]. N. Kumar *et al.* reported that bandgap increases monotonically with the decrease of film thickness [61]. Furthermore, experiments demonstrate that the derived optical bandgap of bulk orthorhombic SnSe is less than that of bulk cubic SnSe (SnSe-CUB) [62-63]. As reported in the literature for solution-phase synthesis, SnSe nanocrystals show a direct bandgap of about 1.71 eV whereas SnSe nanowires exhibit ~1.55 eV direct bandgap [60, 64]. The variation of the bandgap energy (in eV) for different structures of orthorhombic SnSe is summarized in Table 1.

<table>
<thead>
<tr>
<th>Bandgap</th>
<th>Nanocrystal</th>
<th>Single layer</th>
<th>Double layer</th>
<th>Nanowires</th>
<th>Bulk</th>
</tr>
</thead>
<tbody>
<tr>
<td>Direct</td>
<td>1.71</td>
<td>1.66</td>
<td>1.62</td>
<td>1.55</td>
<td>1.3</td>
</tr>
</tbody>
</table>

Table 1: Optical bandgap energies of different thickness of orthorhombic SnSe in eV [65].

The band structures of single-layer, double-layer and bulk SnSe are shown in Fig. 3.6 (a-c). The distance between the valence band maximum (VBM) point and conduction
Band minimum (CBM) point indicates the energy band gap. For single-layer and double-layer structures, the conduction band minima are located along both the $\Gamma$-X and $\Gamma$-Y directions while the valence band maximum is along $\Gamma$-Y. Thus, minimum direct gap in single-layer and double-layer SnSe is located along $\Gamma$-Y. In bulk SnSe, the conduction band minimum is located along $\Gamma$-X, whereas the valence band maximum is along the $\Gamma$-Y direction. The smallest direct gap is 1.30 eV for bulk and located along the $\Gamma$-X direction [59].

![Figure 3. 6: Optical band structures of a) single-layer, b) double-layer, and c) bulk SnSe. Arrows indicate direct transitions [59].](image)

Bandgap energy is one of the primary factors for any material to be considered for optoelectronics and photovoltaic applications. The bandgap energy of the ALD-grown 2D layered SnSe samples were characterized by UV-Vis spectroscopy. The bandgap energy
(E_g) was determined from the absorption spectra of the films grown on glass plates. The direct bandgap was calculated from extrapolation of the linear plot of \((a h v)^2\) versus photon energy \((h v)\) onto the x-axis directly (shown in Fig. 3.7) following the Tauc formula:

\[
(a h v)^2 = k (h v - E_g)
\]

Figure 3.7: Energy band gap calculation of ALD synthesized SnSe thin film.

Where \(a\), \(h\) and \(v\) are the absorption coefficients, Planck constant and light frequency, respectively. The linear behavior of the plot observed at high energy region indicates band to band direct transitions in these films. The band gap energy was estimated to be about 1.74 eV for the SnSe synthesized thin film samples. This value is in close agreement with the reported values for SnSe nanocrystals deposited by other techniques [60].
CHAPTER 4
Device fabrication and electrical characterization

4.1 Background

Digital and analog electronic circuits are generally composed of metal-oxide-semiconductor field-effect transistor (MOSFET) as the fundamental block. In logic circuits, these transistors constitute large assemblies of logic gates where each of the transistor in the logic gates has two distinct on and off states of conductance differentiated by several order magnitude difference in the channel current. Unlike in gapless graphene, 2D metal chalcogenide materials have band gaps greater than 1 eV which makes them ideal candidates for electronic circuits requiring large on/off current ratios. Tin selenide (SnSe) exhibits a direct bandgap of 1.3 eV, a good candidate for applications in photovoltaics and optoelectronics devices. Due to its polarity dependent electronic properties, single crystal SnSe has potential applications in memory devices. A few monolayer thick SnSe films possess excellent photosensitivity and tunable bandgap over bulk form due to the higher surface to volume ratio and quantum confinement effects. To investigate the electronic properties of the ALD grown SnSe thin films, several back-gated FET transistors were fabricated using optical lithography.
4.2 Photolithography

The 2D semiconductor SnSe films were grown on p-type doped (100) silicon substrates with 320nm thick thermally grown SiO$_2$. The SnSe film thickness was typically 5-6 nm depending on the number of ALD growth cycles. The different layers of the wafer are shown in Fig. 4.1.

![Figure 4.1: Schematic of ALD synthesized SnSe thin film on Si/SiO$_2$ substrate.](image)

A photo-mask was used for patterning the wafers to define source and drain contacts. The contact mask used for the fabrication was a 5inch x 5inch soda lime glass mask is shown in Fig. 4.2. The mask was designed using AutoCad and then commercially fabricated. The mask was designed to pattern transistors of gate lengths 2 μm, 4 μm, 6 μm, 8 μm, and 10 μm. All the transistors had gate width of 100 μm. The mask also included TLM structures to measure the contact and sheet resistances of the SnSe film.
The wafer was cleaned using standard photolithography cleaning process using solvent (Acetone, Methanol and IPA) followed by DI (deionized water) rinse and N₂ blow dry. Shipley Microposit S1813 positive photoresist was used to coat the wafers in order to pattern the features. To achieve the resist film thickness around 1.5 micrometer, the spin coater spinning (shown in Fig. 4.3) was set at 3200 RPM for 110 seconds.
The resist was soft baked at 100°C on a hotplate for around 60 seconds to improve adhesion and prevent the stickiness with the contact mask. The resist coated wafers were patterned and exposed using the OAI Hybralign 200 Contact Aligner (shown in Fig. 4.4) with UV light of 365 nm wavelength. The dose was optimized for 120 seconds.
NaOH was used as the developer solution with a concentration between 0.133 and 0.194 M for removing the exposed photoresist from the wafers. This was followed by DI water rinse and blow dry. It is noteworthy to mention that the concentration of NaOH solution was varied and optimized in order to achieve good pattern uniformity across the wafers. Fast development causes poor uniformity which may create overdeveloped structures in the center and underdeveloped at the edges of the wafers. The final optimized development time was set approximately between 15 to 20 seconds. The developed patterns were verified using optical microscope shown in Fig. 4.5. The next process was metal deposition on the patterned substrates which was performed via sputtering.
Metal deposition was employed to produce source/drain contacts. Au was selected as the source/drain metal because of its large work function (~5.3 eV). This film was produced via sputter deposition. The thickness of the Au films on the different substrates was around 30 to 60 nm. After the metal deposition, the unexposed photoresist was lifted off using acetone followed by DI rinse and blow dry. An Ag paste was applied to the backside of the wafers to provide backside metal contact. The schematic of the back-gated transistor with source, drain and gate contacts are shown in Fig. 4.6 (a). Optical image for one of the final fabricated SnSe samples with array of active transistors are shown in Fig. 4.6 (b). Fig. 4.6 (c) shows one set of transistors with varying gates lengths and associated TLM structures (details in next section).
Figure 4.6: a) Schematic diagram of a SnSe back-gated transistor, b) patterned features on the substrate after metallization showing array of devices, c) optical view of the patterned substrate focusing on a cluster of transistors of gate lengths of 2, 4, 6, 8, and 10 μm and gate width of 100 μm.
4.3 Test of electrical properties

Electrical characterization is one of the most important factors to understand the electronic properties of the semiconductor materials. To investigate the electrical properties, drain current–voltage ($I_d-V_d$) and drain current-gate voltage ($I_d-V_g$) scans were performed by using back gated filed effect transistors in order to extract basic electrical information such as carrier mobility, on/off current ratio and contact parameters.

Electrical tests were performed by using a probe station (MODEL 7000-LTE) shown in Fig. 4.7, which was connected to HP 4145B semiconductor parameter analyzer (Fig. 4.8). The parameter analyzer was interfaced to a workstation through serial port and could be controlled by characterization software ICS (Interactive Characterization Software). This software provided the inputs to the analyzer as well as recorded the output parameters for every test and stored the data and the plots.

![Probe Station](image)

Figure 4.7: Probe Station used for electrical testing.
Electrical properties of the as-synthesized SnSe were investigated by fabricating back-gated field-effect transistors (FETs) using standard optical lithography techniques as described in section 4.2. Each cluster consisted of multiple transistors of varying gate lengths with a fixed gate width of 100 μm as shown in Fig. 4.6 (b).
The fabricated FETs were characterized at room temperature (Fig. 4.9). With zero gate bias, the drain current versus drain voltage exhibited linear profile indicating ideal ohmic contacts between Au electrodes and SnSe (Fig. 4.10).

![Graph of linear Id-Vd characteristic](image)

**Figure 4.10:** Linear $I_d$-$V_d$ characteristic of SnSe thin film with Au electrodes at zero gate bias [52].

Prior to each transistor characterization, I-V curves were plotted between S/D and back gate to detect onset of leakage. For subsequent transistor characterization, gate voltages were maintained much lower than this value in order to prevent measurement of leakage current. Also, a trench was scribed around each transistor to eliminate leakage across the edge of the substrate. The photo-response of the as-grown SnSe thin films was also characterized using the FET device by turning the illumination on and off as shown in [Fig 4.11]. Drain current increases with illumination which implies that the SnSe thin film exhibits direct band gap.
Figure 4. 11: Current–voltage characteristics of a SnSe FET under illumination at zero gate bias.

Transfer characteristics were determined by keeping the drain voltage ($V_{ds}$) constant at 0.1V while varying the back-gate voltage $V_g$ to the Si substrate from 0 to -15V.

Figure 4. 12: Transfer characteristics of a SnSe p-FET [52].
The $I_d$-$V_g$ transfer characteristic of the device showed p-type transistor behavior as can be seen in (Fig. 4.12). Drain current varied with the gate voltage corresponding to an on/off current ratio of nearly $10^5$ which is well within the desired $I_{on}/I_{off}$ ratio of $10^4$-$10^7$ for any silicon replacement FET [66]. The field effect mobility was calculated using the following equation:

$$\mu = (\Delta I_d/\Delta V_g) \times (L/WC_{SiO_2}V_d)$$

Where $L$ and $W$ are the length and width of the device, and $C_{SiO_2} = 11 \text{ nF/cm}^2$ is the capacitance for 320 nm SiO$_2$ layer. The average room temperature mobility of several of these devices were about $10 \text{ cm}^2/(\text{V.s})$.

![Graph showing transfer characteristics of a SnSe n-FET.](image)

**Figure 4.13: Transfer characteristics of a SnSe n-FET.**

Some of the 2D layered structures including black phosphorus (BP), WSe$_2$, WS$_2$/SnS heterostructure, etc. have previously been reported to exhibit ambipolar behavior. Recently SnS on top of black phosphorus also showed good ambipolar characteristics.
Although SnSe has been reported to be an intrinsic p-type semiconductor, the ALD synthesized FET devices in our case showed both p- and n-type behavior. The $I_d$-$V_g$ transfer characteristic of the n-FET is shown in Fig. 4.13. The calculated electron mobility of the device is found to be $\sim 0.7 \text{ cm}^2/(\text{V.s})$ while the $I_{on}/I_{off}$ ratio is $\sim 10^2$. The n-type behavior of SnSe thin film is unknown and needs further investigation. However good ambipolar characteristics of SnSe will enable the application of these devices in modern CMOS process without the need for doping methods.

<table>
<thead>
<tr>
<th>Synthesis Technique</th>
<th>Mobility (p type) cm$^2$/V.s</th>
<th>Source</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chemical Vapor Deposition</td>
<td>$\sim 2.6$</td>
<td>Ref. 67</td>
</tr>
<tr>
<td>Exfoliation from bulk single crystals</td>
<td>$\sim 2.7$</td>
<td>Ref. 57</td>
</tr>
<tr>
<td>Vapor Transport Deposition</td>
<td>$\sim 1.5$</td>
<td>Ref. 7</td>
</tr>
<tr>
<td>Thermal Evaporation</td>
<td>$\sim 6.1$</td>
<td>Ref. 29</td>
</tr>
<tr>
<td>Atomic Layer Deposition</td>
<td>$\sim 10$</td>
<td>This work using ALD [Ref. 52]</td>
</tr>
</tbody>
</table>

Table 2: Literature review of SnSe hole mobility reported by various techniques.

P-type hole mobility of the as-grown SnSe thin film is amongst the highest that have been recently reported by other groups. CVD grown mobility was found $2.6 \text{ cm}^2/\text{V.s}$ in 2018, exfoliated SnSe had a mobility of around $2.7 \text{ cm}^2/\text{V.s}$ as reported in 2017 [67, 57].
Reported mobilities for vapor deposition technique and thermal evaporation were 1.5 cm$^2$/V.s and 6.1 cm$^2$/V.s respectively [7, 29]. In the case of ALD grown SnSe we have found 10 cm$^2$/V.s [52]. A complete list of mobilities found by various techniques is illustrated in Table 2.
CHAPTER 5
Conclusion and future work

5.1 Conclusion

Beyond graphene, other novel 2D materials such as metal chalcogenides, black phosphorus, hBN etc. have also gained interest recently due to their unique properties and applications that emerge upon 2D carrier confinement. In this research one of the most promising two-dimensional layered metal chalcogenides from group IV–VI, tin selenide (SnSe) was investigated due to its potential applications in optoelectronics, photovoltaics and other areas. With its remarkable structural and electronic properties, SnSe is the material of interest for next generation of opto- and microelectronic devices. In order to realize these applications, techniques need to be developed to produce uniform films of this material over large areas. Hence, the objective of this research was develop such a growth technique and show that the films produced were comparable to those produced by exfoliation, which has been considered to be the gold standard of 2D materials.

Various synthesis methods have been reported in literature for growing the SnSe thin films, however none of these methods can produce uniform films over large areas. Hence, ALD was investigated for growth of SnSe for the goal of producing uniform, smooth and continuous films over the larger areas. As a result of this investigation, this goal was achieved, demonstrating that self-terminating reaction nature of the process is one of the greatest benefits of the ALD growth for precise controllability of the film.

The electrical characterization of the ALD synthesized SnSe films using fabricated back-gated FETs showed p-type transistor behavior with moderate hole mobility and
yielded high $I_{on}/I_{off}$ ratios, demonstrating suitability for advanced semiconductor devices. Raman, XRD and XPS characterizations confirmed crystalline and orthorhombic phase of the as-grown SnSe films consistent with the characteristics reported in literature for several other growth techniques. Magnetic properties showed paramagnetic behavior for the thin film SnSe. The optical band gap energy of 1.74 eV as characterized from the SnSe film makes it a promising material for optoelectronic applications. All these properties show that ALD grown films possess the necessary characteristics for their commercial applications.

5.2 Future work

Several hundred transistors were fabricated and tested on numerous SnSe substrates (more than 10 substrates) with different thicknesses ranging between 5 nm and 10 nm. The mobility of 2D layered materials has a non-monotonic relation with layer thickness. Due to improved screening of charge impurities at the first few layers, mobility initially increases with the number of layers. However it tends to decrease in thicker samples due to increased interlayer resistance [68]. A detail study of the layer dependent mobility on SnSe thin films is necessary and this work can be extended by optimizing the number of layers in order to maximizing the hole mobility using the ALD growth sequence.

We can fabricate top gated monolithic transistors, where better gate control can be achieved by depositing a thin layer of gate oxide. This will yield higher drain saturation current and larger $I_{on}/I_{off}$ ratio as compared to the back-gated transistor for practical switching devices. A prototype of the top-gated SnSe FET device is shown in Fig. 5.1.
Since the synthesized SnSe film has a direct bandgap, one could design and fabricate optical devices using these thin films. Furthermore, the optical devices can be integrated with the top gated FETs in monolithic transistors for next generation of novel devices.
References


